
SR5 E1 line–Devices errata JTAG_ID = 0x0012_2041 and 0x1012_2041

Introduction

This errata sheet describes functional and electrical problems of the SR5E1x devices.

The reference documentation is reported in [Section 1: Reference documents](#).

Device identification for cut 1.0:

- JTAG_ID = 0x0012_2041
- MIDR1 register:
 - MAJOR_MASK[3:0]: 4'b0000
 - MINOR_MASK[3:0]: 4'b0000

Device identification for cut 1.1:

- JTAG_ID = 0x1012_2041
- MIDR1 register:
 - MAJOR_MASK[3:0]: 4'b0000
 - MINOR_MASK[3:0]: 4'b0001

The list of errata is contained in a Microsoft® Excel workbook file attached to this document.

Locate the paper clip symbol on the left side of the PDF window, and click it. Double-click on the Excel file to open it.



1 Reference documents

Table 1. Reference documents

Document name	Document title
RM0483	SR5E1x 32-bit Arm® Cortex®-M7 architecture microcontroller for electrical vehicle applications
DS13808	SR5 E1 line of Stellar electrification MCUs — 32-bit Arm® Cortex®-M7 automotive MCU 2x cores, 300 MHz, 2 MB flash, rich analog, 104 ps 24-ch high-resolution timer, HSM, and ASIL D

Revision history

Table 2. Document revision history

Date	Revision	Changes
29-Mar-2023	1	Initial release.
11-Jan-2024	2	<p>All references to Security Errata have been removed from this file, refer to the Security Errata Sheet.</p> <p>New Errata:</p> <p>DAN-0058114 DAN-0058219 DAN-0058223 DAN-0061454 DAN-0062235 DAN-0062237 DAN-0062258 DAN-0062401</p> <p>Moved in Security Errata:</p> <p>DAN-0044555 DAN-0057876</p> <p>Removed watermark "ST Restricted".</p>
05-Nov-2024	3	Updated Introduction .

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