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# STM32L4 series

## Ultra-low-power MCUs with performance





# The STM32 portfolio

## Five product categories



Wireless  
MCU

Short- and long-range connectivity



Ultra-low-power  
MCU

32-bit general-purpose microcontrollers: from 75 to 3,360 CoreMark score



Mainstream  
MCU



High-performance  
MCU



Embedded  
MPU

32- and 64-bit microprocessors



Enabling edge AI solutions



Scalable security



# Why choose the STM32L4 series



+

## ULP and performance booster

STM32L4 architecture reaches 100 MIPS thanks to an Arm® Cortex®-M4 core with FPU and ST ART Accelerator™ at 80 MHz, while keeping best-in-class, ultra-low-power (ULP) performance

+

## Innovation

Innovative peripherals are embedded to optimize the BOM cost.

+

## Integration and safety

1 Mbyte of Flash and 320 Kbytes of SRAM with safety and security features, many smart peripherals, advanced and low power analog circuits in packages as small as 2.58 x 3.07 mm.

+

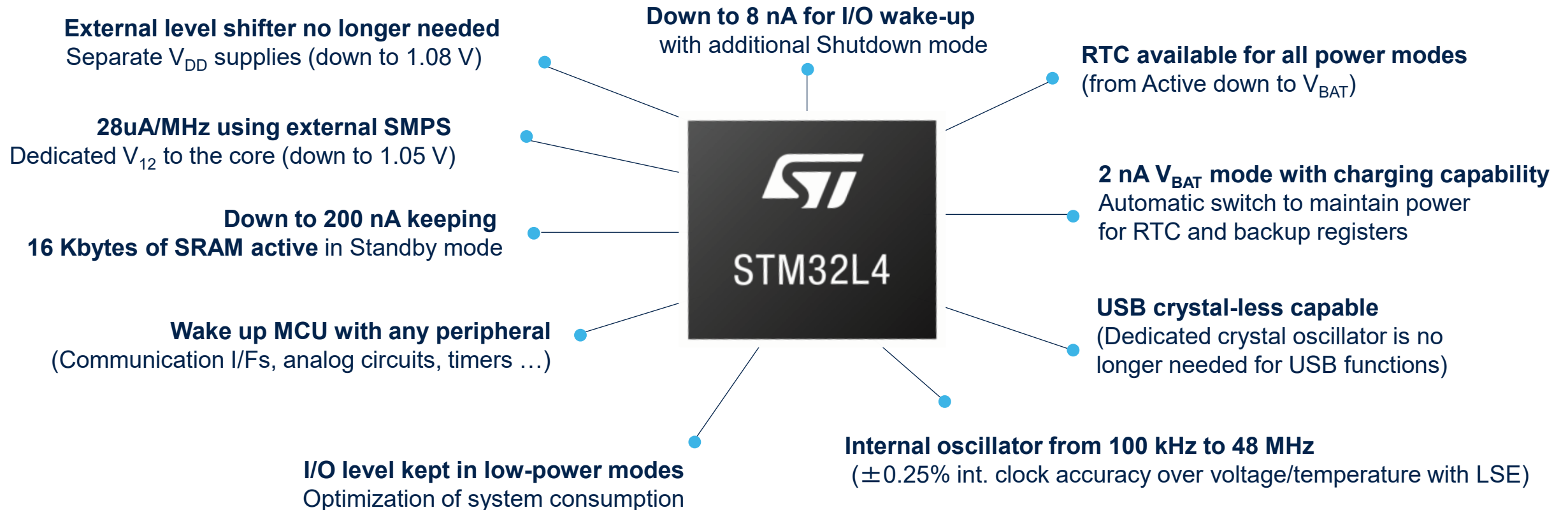
## Long term investment

Pin-to-pin compatibility with the STM32L4 and STM32L4+ and integrated in the STM32 ecosystem.



# Ultra-low power and flexibility

## FlexPowerControl: more flexibility when managing power modes and reduced power consumption





# Ultra-low-power modes

## Best power consumption with high flexibility

### Wake-up time

250  $\mu$ s

14  $\mu$ s

14  $\mu$ s

5  $\mu$ s

4  $\mu$ s

6 cycles

$V_{BAT}$

2 nA / 200 nA\*

Tamper detection: 2 I/Os, RTC

Shutdown

8 nA / 200 nA\*

Wake-up sources: reset pin, 5 I/Os, RTC

Standby

34 nA / 280 nA\*

Wake-up sources: + BOR, IWDG

Standby + 8-Kbyte RAM

200 nA / 440 nA\*

Stop 2 (full retention)

720 nA / 950 nA\*

Wake-up sources: + all I/Os, PVD, LCD, COMPs, I<sup>2</sup>C, LPUART, LPTIM

Stop 1 (full retention)

3.2  $\mu$ A / 3.4  $\mu$ A\*

Wake-up sources: + all I<sup>2</sup>C, UART

Sleep

8  $\mu$ A/MHz \*\* / 20  $\mu$ A/MHz \*\*

Wake-up sources: any interrupt or event

Run at 24 MHz

28  $\mu$ A /MHz \*\* / 79  $\mu$ A /MHz

Run at 80 MHz

35  $\mu$ A/MHz \*\* / 90  $\mu$ A/MHz

Note : \* without RTC / with RTC

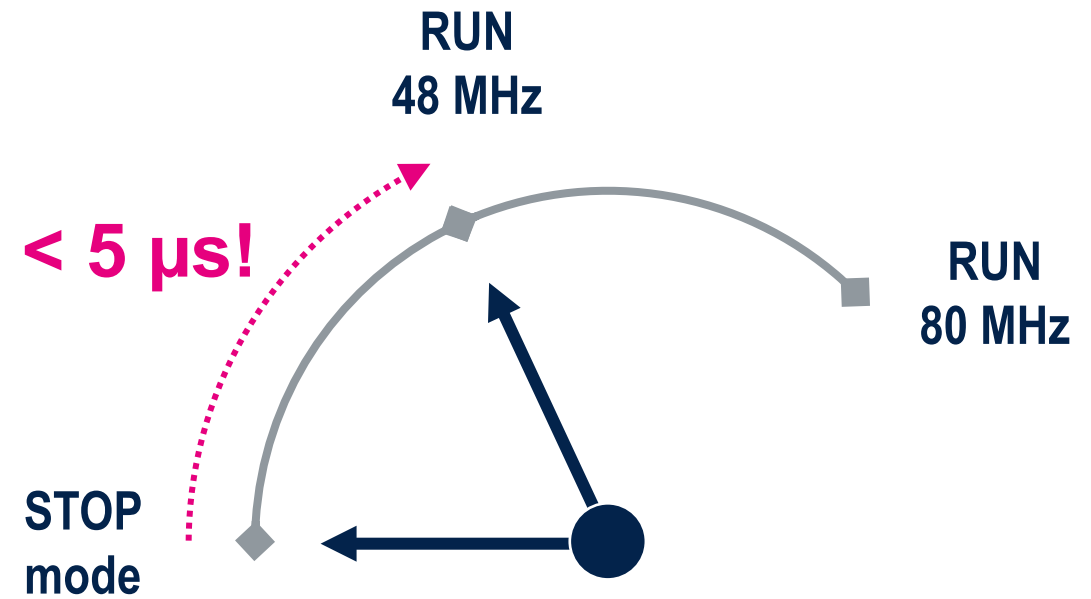
\*\* with external SMPS



# STM32L4 wake up

STM32L4 takes off like a rocket!

<b>Dhrystone</b> MIPS	<b>100</b>
<b>ULPBENCH™</b> An EEMBC Benchmark	<b>447 ULPMark-CP™</b>
<b>ULPBENCH™</b> An EEMBC Benchmark	<b>167 ULPMark-PP™</b>
<b>COREMARK®</b> An EEMBC Benchmark	<b>273</b>



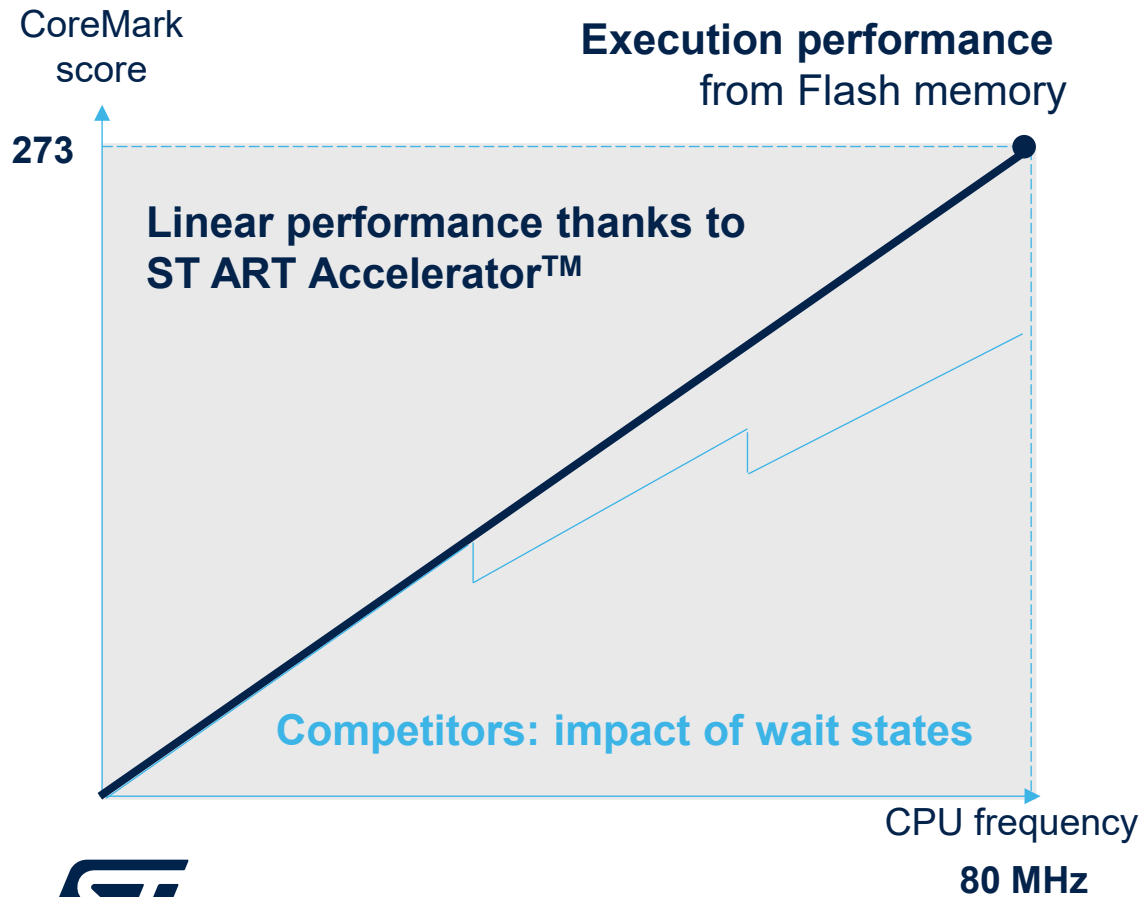
From 0 to 48 MHz in less than 5  $\mu$ s

From 0 to 80 MHz in less than 20  $\mu$ s



# Providing more performance

## No compromise on performance with STM32L4 MCUs



- **Up to 80 MHz/ 100 DMIPS** with ST ART Accelerator™
- **Up to 273** CoreMark result
- Arm® Cortex®-M4 with FPU and DSP instructions
- 2x DMA (14 channels)
- SPI up to 40 Mbit/s, USART 10 Mbit/s

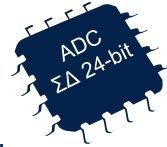




# Smart peripherals metering

## Digital Filter for Sigma Delta Modulators

8 x parallel inputs  
with up to 24-bit data output resolution



## V<sub>BAT</sub> with RTC for battery backup

200 nA in V<sub>BAT</sub> mode  
for RTC and  
32 x 32-bit backup registers



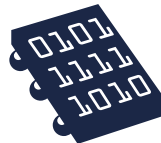
## TRNG & AES

for Security  
128-/256-bit AES  
key encryption hardware accelerator



## FSMC

External memory interface  
for static memories supporting SRAM,  
PSRAM, NOR and NAND



**STM32L4**

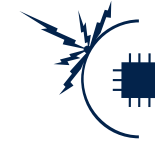


**Electricity/Gas  
/ Water  
Smart Meter**



## LCD Display

SPI, Parallel or TFT Interface



## Anti Tamper pin

3 x tamper pins  
for battery domain



## SPI / UART/ SDIO

for Wireless

3x SPIs (4x SPIs with the Quad SPI)  
6x UARTs (ISO 7816, LIN, IrDA, modem)  
1 x SDIO



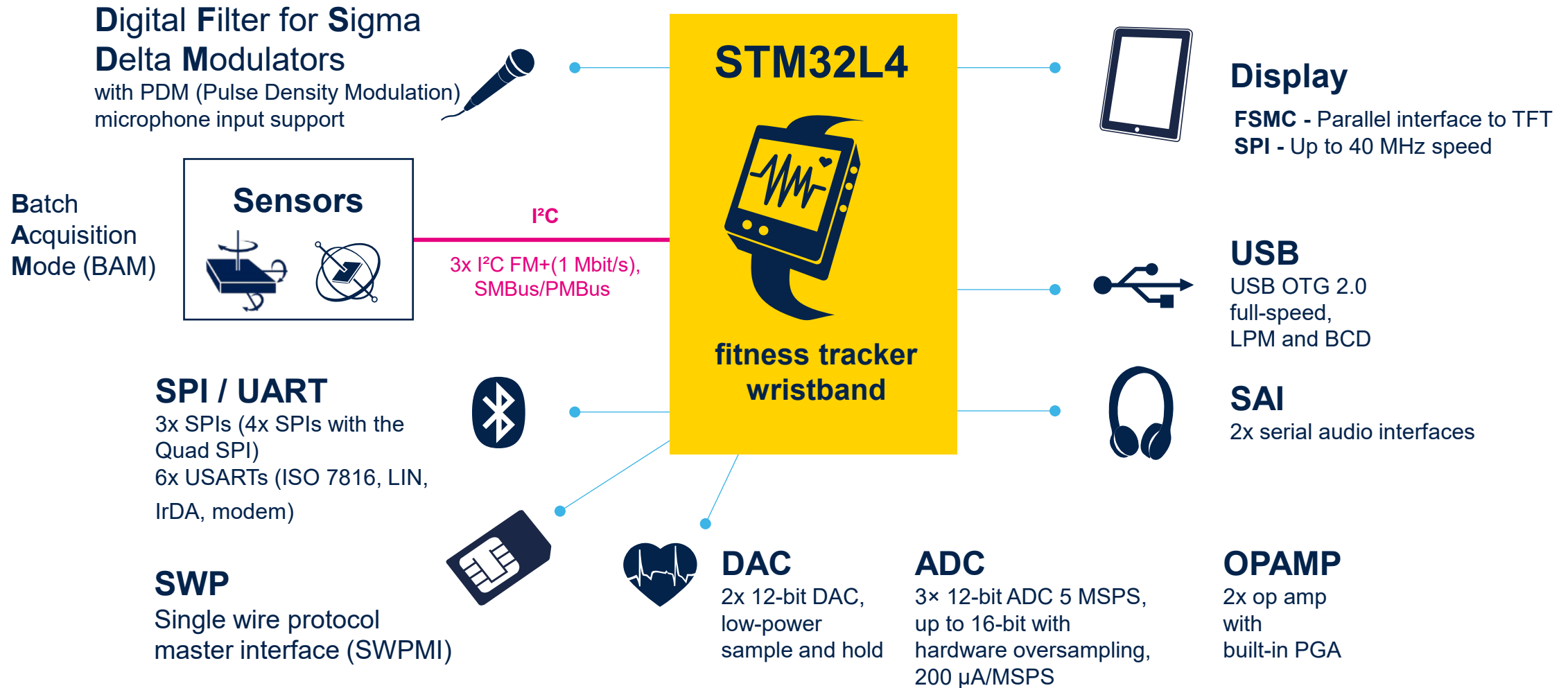
## I/Os

Up to 114 fast I/Os for buttons & relays



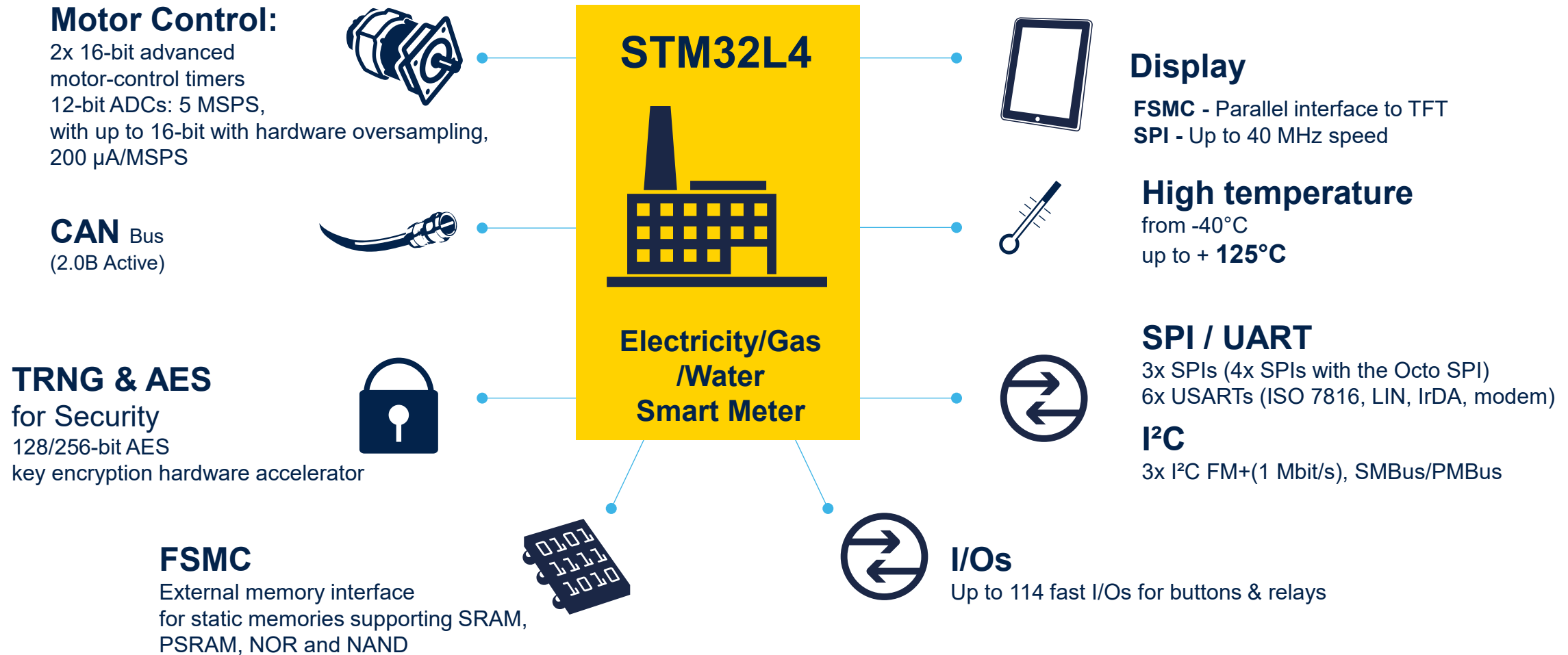


# Smart peripherals fitness tracker - wristband



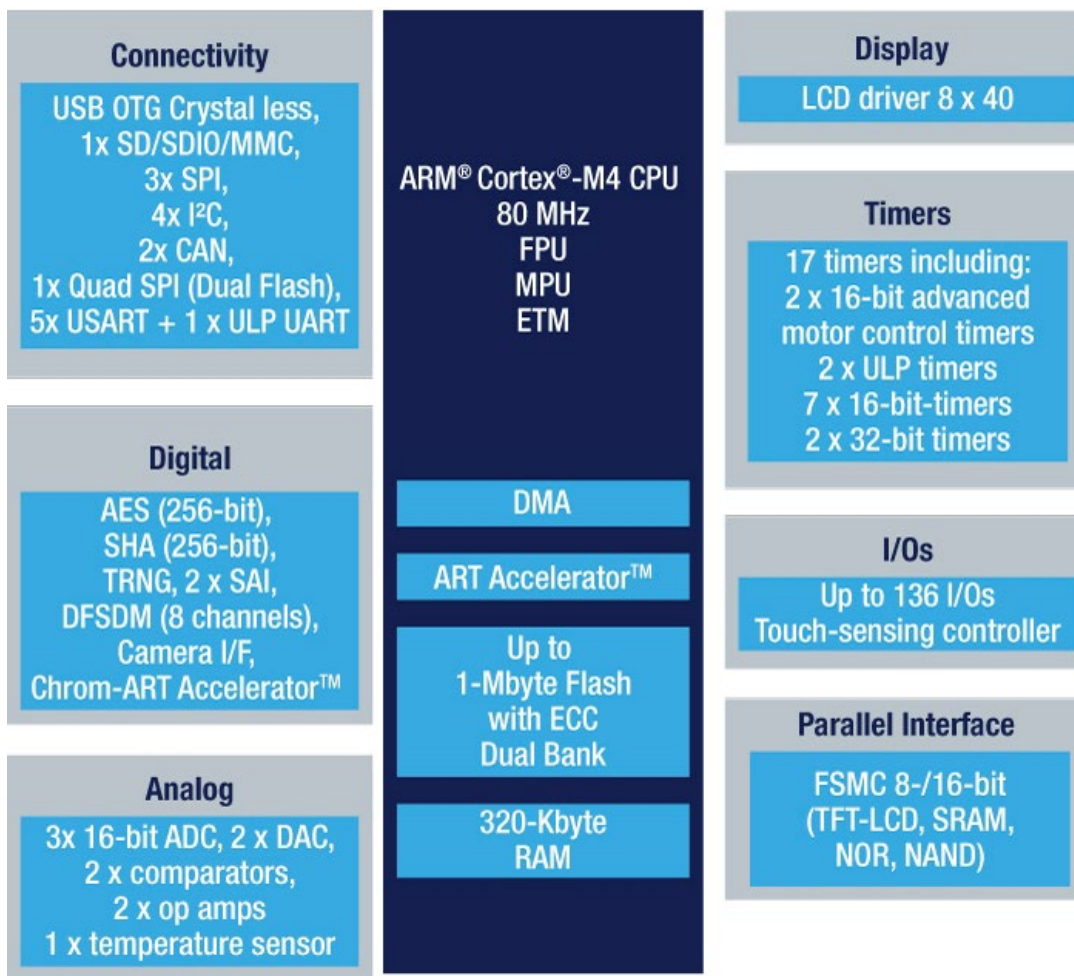


# Smart peripherals industrial sensors





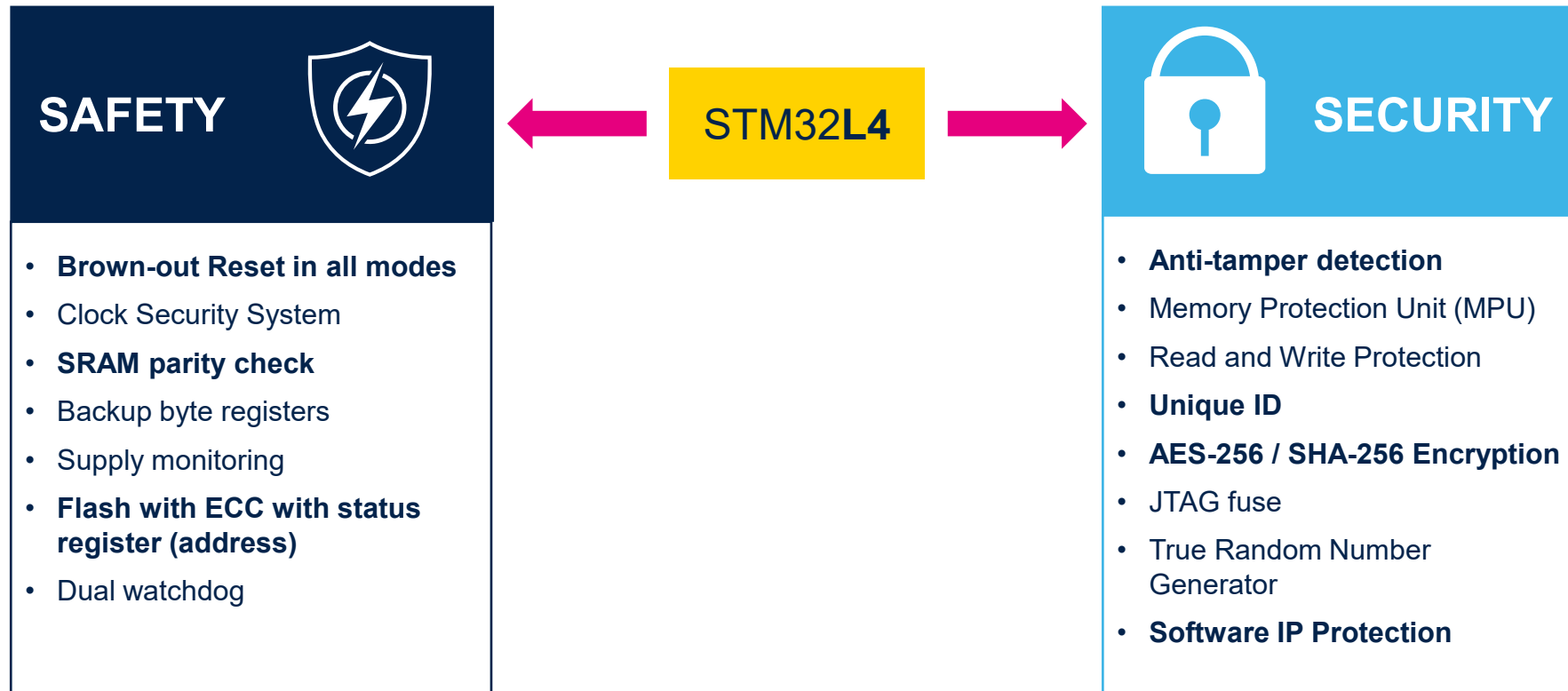
# High integration with large memory size in small packages



**Package size down to 2.58 x 3.07 mm**

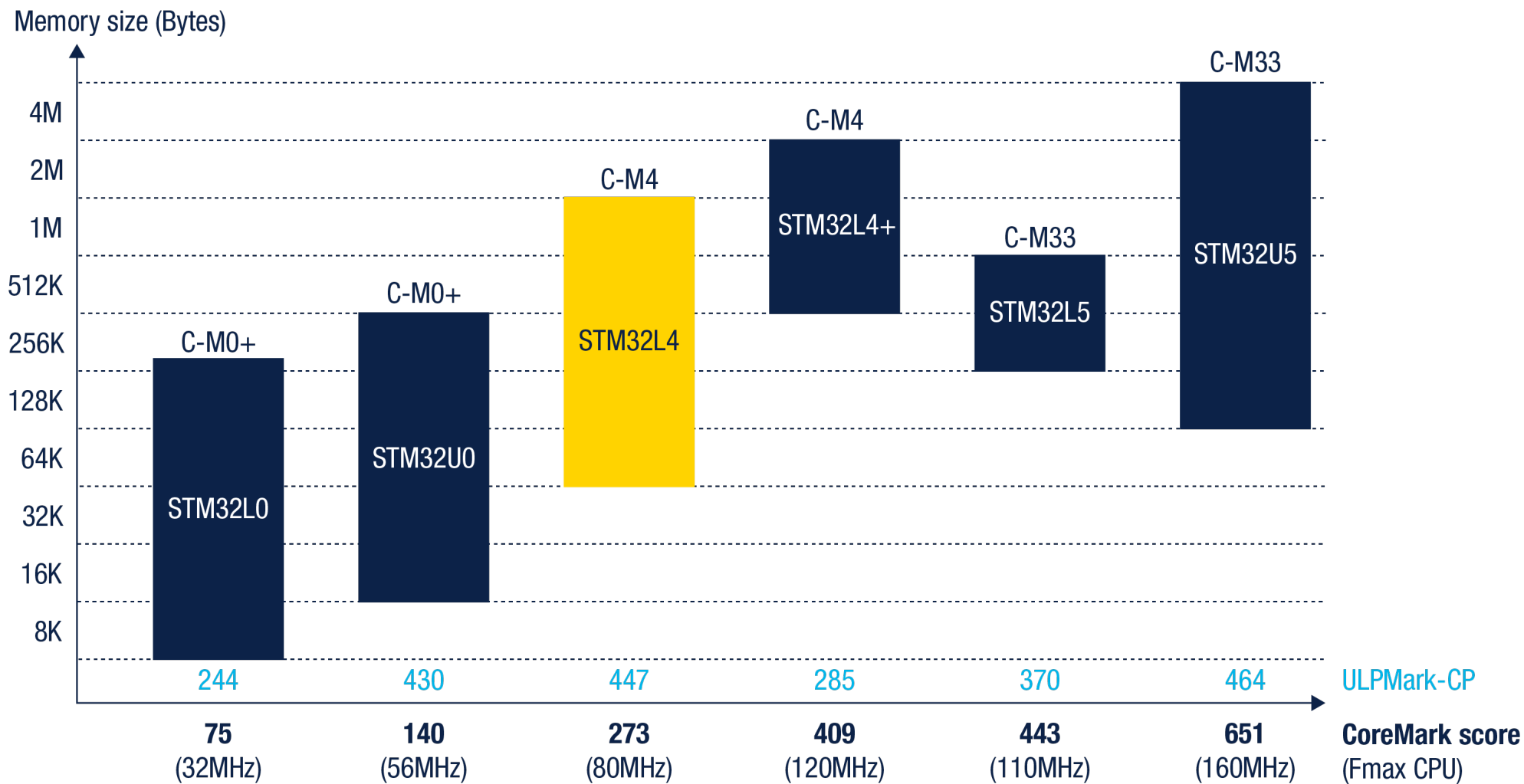


## Integrated safety and security features





# STM32L4 ultra-low-power benchmark





# STM32L4 series

## 10 Product lines

	Product line	Flash (KB)	RAM (KB)	Memory I/F FSMC	Op-Amp	CAN	Sigma Delta Interface	12-bit ADC 5 Msps 16-bit HW over-sampling	DAC	SAI	USB 2.0 OTG	USB Device	Segment LCD driver	Chrom-ART Accelerator
<ul style="list-style-type: none"> <li>• ART Accelerator™</li> <li>• USART, SPI, I²C</li> <li>• Quad-SPI</li> <li>• 16- and 32-bit timers</li> <li>• SAI + Audio PLL</li> <li>• SWP</li> <li>• 2x CAN</li> <li>• 2x 12-bit DACs</li> <li>• Temperature sensor</li> <li>• Low voltage 1.71 to 3.6V</li> <li>• V<sub>BAT</sub> mode</li> <li>• Unique ID</li> <li>• Capacitive Touch sensing</li> <li>• AES-128/256* and SHA-256**</li> </ul>	STM32L496**	512 to 1024	320	•	2	2	8x ch	3	2	2	•		Up to 8x40	•
	STM32L476*	256 to 1024	128	•	2	1	8x ch	3	2	2	•		Up to 8x40	
	STM32L475	128 to 1024	128	•	2	1	8x ch	3	2	2	•			
	STM32L433	128 to 256	64		1	1		1	2	1		•	Up to 8x40	
	STM32L452	256 to 512	160		1	1	4x ch	1	1	1		•		
	STM32L432	128 to 256	64		1	1		1	2	1		•		
	STM32L412	64 to 128	40		1			2				•		
	STM32L471 Access line	512 to 1024	128	•	2	1	8x ch	3	2	2				
	STM32L451 Access line	256 to 512	160		1	1	4x ch	1	1	1				
	STM32L431 Access line	128 to 256	64		1	1		1	2	1				

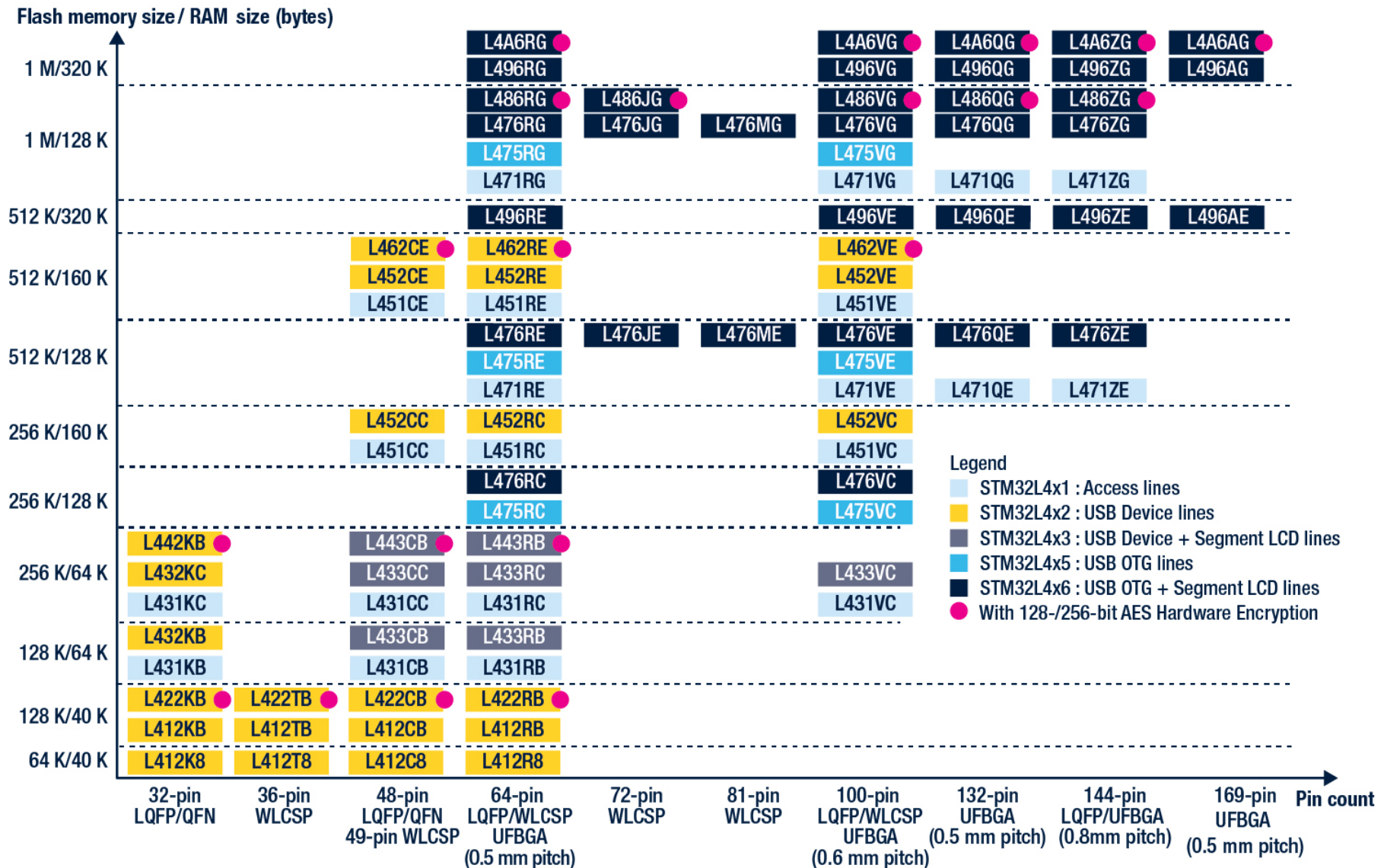
Note: \*HW crypto/hash functions are available on STM32L486, STM32L443, STM32L462, STM32L442 and STM32L422

\*\* on STM32L4A6





# STM32L4 portfolio



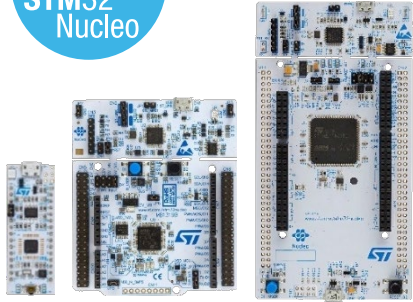




# STM32L4 ecosystem

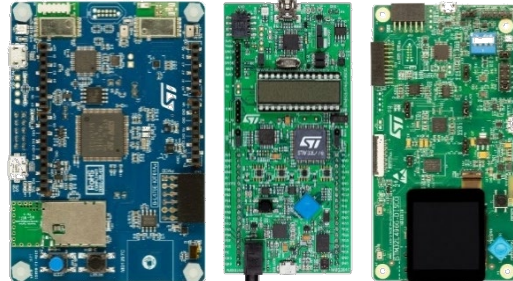
## HARDWARE TOOLS

## STM32 Programming Tool



### STM32 Nucleo boards

Flexible prototyping



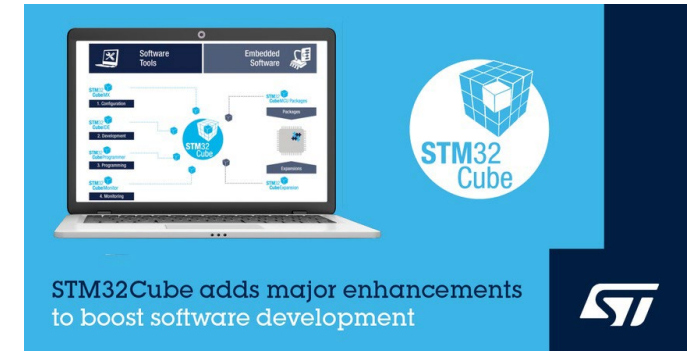
### Discovery kits

Key feature prototyping



### Evaluation board

Full feature evaluation



STM32  
CubeMX



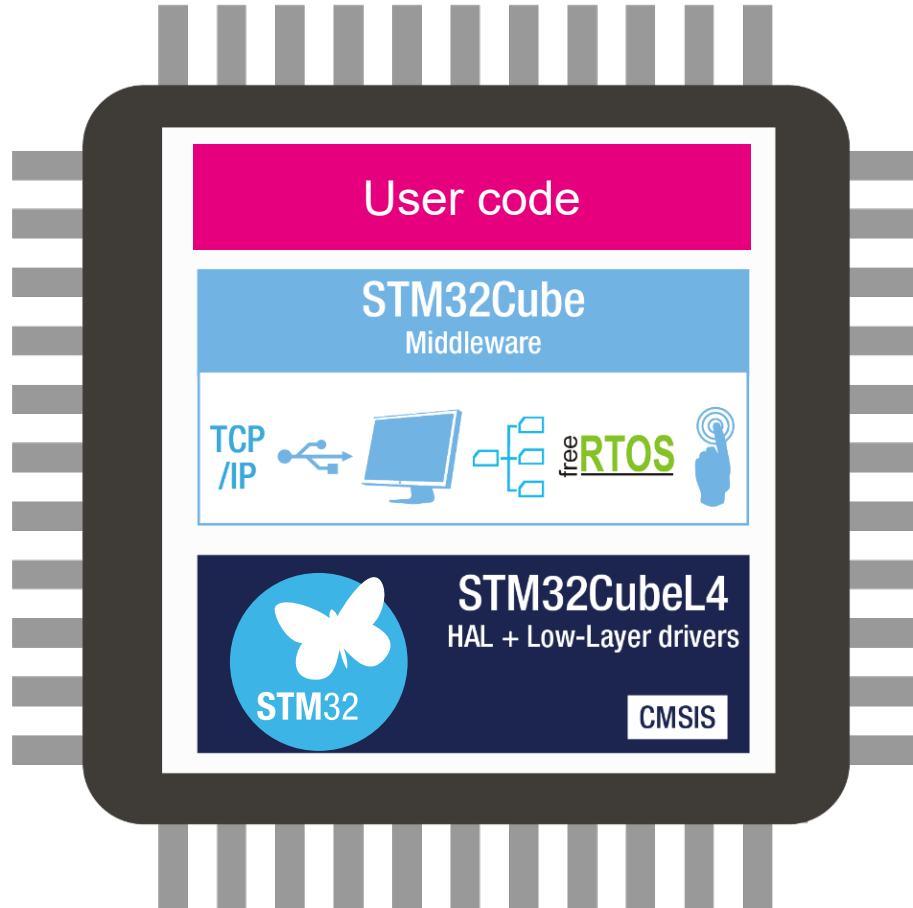
[wiki.st.com/stm32mcu](https://wiki.st.com/stm32mcu)



[github.com/STMicroelectronics](https://github.com/STMicroelectronics)



# STM32L4/L4+ ecosystem



## EMBEDDED SOFTWARE

- Open-source TCP/IP stack (lwIP)
  - USB Host and Device library from ST **Qualified HAL firmware**
  - STemWin graphical stack library from ST and SEGGER
  - Open-source FAT file system (FatFs)
  - Open-source real-time OS (FreeRTOS)
  - Touch-sensing library
  - Dozens of examples
- 
- STM32L4 Hardware Abstraction Layer (HAL) portable APIs
  - **High-performance, light-weight low-layer (LL) APIs**
  - High coverage for most STM32 peripherals
  - Production-ready and fully qualified
  - Dozens of usage examples
  - Open-source BSD license

# Releasing your creativity



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[www.st.com/STM32L4](#)



[wiki.st.com/stm32mcu](#)



[github.com/stm32-hotspot](#)



[STM32 MCU Developer Zone](#)



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[STM32L4 MOOC](#)



# Our technology starts with You



Find out more at [www.st.com/STM32L4](http://www.st.com/STM32L4)

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