

STM32U3 ultra-low-power MCUs

Extending battery life and protecting data in cost-sensitive industrial, medical, and consumer devices.



The STM32 portfolio

Five product categories



Short- and long-range connectivity









32- and 64-bit microprocessors















32-bit general-purpose microcontrollers: from 75 to 3,360 CoreMark score



Scalable security







STM32U3 microcontroller series



Industry-leading energy efficiency



Robust security to safeguard sensitive applications

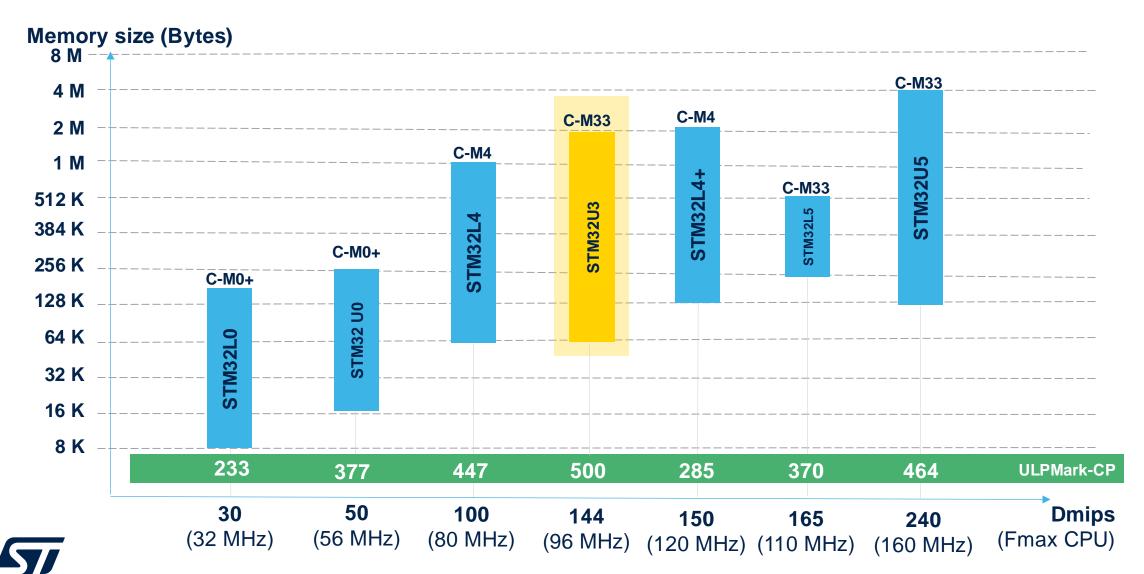


Versatile peripheral offering without compromising cost efficiency

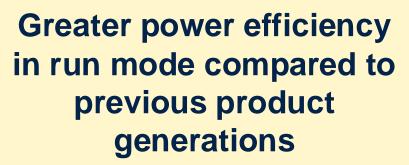




Expanding the STM32 ultra-low-power family



Enabling more efficient battery-powered devices





First STM32 with near-threshold design

Significantly reduces dynamic consumption

Market-leading efficiency with 117 CoreMark/mW

Five times more efficient vs previous product generation

Standard & extended industrial temperature support

• -40 up to +85°C and +105°C



Smaller, more energy efficient devices



A sustainable technology product expanding device battery lifetime.

Activity tracking devices



Gas and water metering devices



Industrial GPS tracking devices



Versus STM32L4 MCU





Enhancing device longevity and making products more sustainable



Industrial & smart home

Thermostats, smoke detectors, heat cost allocators, door locks, asset trackers, sensors

Medical

Insulin pumps, glucose meters





Smart metering

Water, gas, electricity meters, smart home gateways

Consumer

Activity trackers, GPS, ecigarettes, headphones, PC accessories





Leveraging near-threshold technology to boost power efficiency

A key enabler to significantly reduce dynamic consumption

What is near-threshold technology?

- Operates transistors within an MCU below their traditional power supply voltage level (1.20 to 0.9 V)
- STM32U3 core logic can operate at a minimum voltage of 0.65 V

How this technology makes a difference

Reduces dynamic consumption (when the MCU is in run mode).

Dynamic consumption is a major part of the application energy usage.



80% energy consumed in run mode



99% energy consumed in run mode

Reducing dynamic consumption impacts overall energy efficiency.





Optimizing power consumption with adaptive voltage scaling (AVS)

The adaptive voltage scaling (AVS) technique adjusts the supply voltage to the minimum in test production

Dynamic voltage adjustment for optimal performance

AVS minimizes the core voltage, enabling significant power savings.

A patented technique utilized during the production phase to precisely define AVS thresholds

API-free implementation of the AVS technique

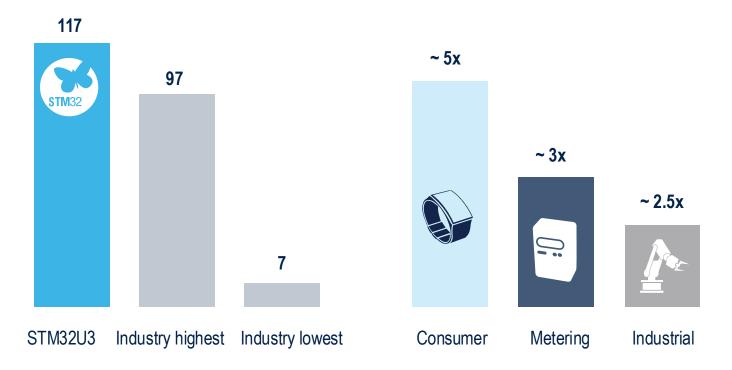
Preprogramming the optimal voltage value during in-house test production eliminates the need for an API during product configuration, saving time and costs.

Superior efficiency against industry references

CoreMark/mW* (the higher the better)

STM32U3 benchmark per application area (average ratio to STM32U3, the lower the better).

Above 1 means higher consumption than STM32U3



Outperforms best-in-class industry products

The most power efficient product in all application areas

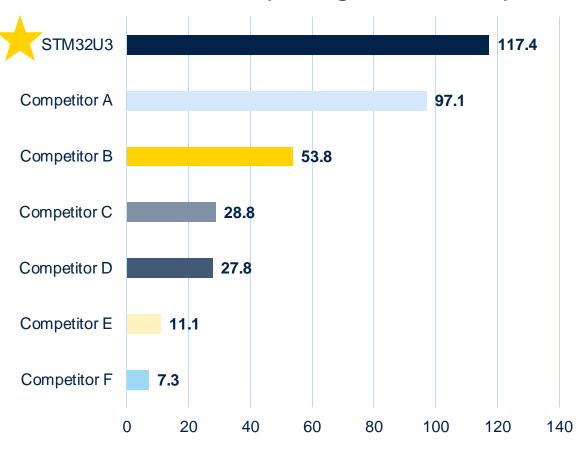
Competition values are expressed with their best-in-class products

*CoreMark/mW is a relevant metric to compare different cores and different voltage ranges



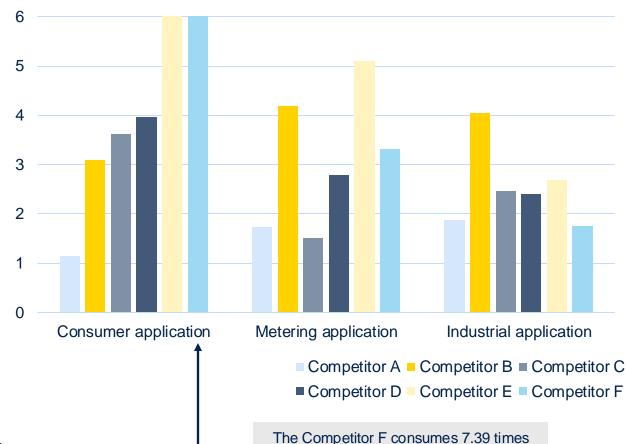
Benchmarking superior energy efficiency against competition

CoreMark/mW* (the higher the better)



STM32U3 benchmark (ratio to STM32U3, the lower the better)

Above 1 means higher consumption than STM32U3



more than STM32U3 in consumer

applications.

Competition values are expressed with their best-in-class products.

*CoreMark/mW is a relevant metric to compare different cores and different voltage ranges.

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STM32U3 ultra-low-power modes at 3.3 V

Shutdown 300 nA		5x Tamper detection, 22 I/Os, RTC, I3C reset pattern							
Standby 330 nA		Wake-up sources: + BOR, IWDG							
Stop 3 + 8 Kbytes		.6 μA Wake-up sources: + same as standby							
Stop 3 (full retention)		2.2μΑ	vvake-u	Sources. + Same as Standby					
Stop 2 + 8KB		3.8 μΑ		Wake-up sources: + all I/Os, PVD,					
Stop 2 (full retention)		4.5 μA		COMPs, I ² C, LPUART, LPTIM					
Stop 1 (full retention)		65 μ	A	Wake-up sources: + all I ² C, I3C, UART, SPI, USB, ADF					
Sleep		4.7 μ	A/MHz	Wake-up sources: any interrupt or event					
Run (while1) at 48 MHz		9.5 μA/MHz							
Run (Coremark) at 48 MHz		13 μA/MHz							
Run (Coremark) at 96 MHz		16 μA/MHz							



Robust security to safeguard sensitive and mission-critical applications

More locked doors to ensure device security

Enhanced device authentication and anticloning capabilities at a reduced cost

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The coupling and chaining bridge (CCB) HAL

- Key hardware protection by securely wrapping provisioning keys with an encryption algorithm.
- Keeping keys hidden even from the CPU and securely stored.



#1

In-factory provisioned attestation

Assigning a unique, secure identity to each device during manufacturing.



Side-channel resistant hardware protection

Ready for the cyber resilience act (CRA)

PSA Certified L3 & SESIP3 targets

Two new mechanisms on STM32U3 in standalone







STM32U3 security overview

Increasing security with digital identities

Memory protections

against illegal access control

OTP, HDP, WRP, RDP, MPU Secure Debug 5x Tamper pins, & V/T

Platform protection

during product lifecycle

RDP: 4 protection level states Password based regression

Cryptography

for hardware robustness

Side channel AES, PKA
Additional AES, SHA, TRNG, HUK
(Hardware unique key)
Attestation keys

Code isolation

for runtime protection

4 isolation stages Arm® TrustZone® technology

Security services

STM32Trust TEE TF-M

Secure boot & secure updates

Secure firmware install

NIST - CAVP certified CryptoLib

State-of-the-art security assurance level





Versatile peripheral offering without compromising cost efficiency





STM32U375/385 block diagram

Timers

16 timers including:
2 x 16-bit advanced motor
control timers
4 x ULP timers
5 x 16-bit-timers
3 x 32-bit timers

I/Os

Touch sensing controller

Arm[®] Cortex[®]
-M33
96 MHz FPU
MPU
TrustZone[®]
ETM

ART Accelerator

Up to 1 MB Flash Dual

256 KB RAM

Bank

Connectivity

USB Host/Device
1x SD/SDIO/MMC, 3x SPI,
3x I²C, 2 x I3C, CAN FD,
1 x octo-SPI,
4x USART + 1x ULP UART

Digital

AES (256-bit), SAES, SHA-1, SHA-256, SPKA, HUK, 1x SAI, 1x ADF

Analog

2x 12-bit ADC 2.5 MSPS 2 x DAC, 1x comparators, 2x op amp 1 x temperature sensor **Numerous integrated peripherals**

Large memory



Many options available to streamline costs

100% pin to pin compatible with STM32U5
(Except WLCSP)





9 different packages per line

LQFP48 (7 × 7 mm) LQFP64 (10 × 10 mm) LQFP100 (14 × 14 mm) UFBGA64 (5 × 5 mm) UFBGA100 (7 × 7 mm)

UFQFPN32 (5 x 5 mm) UFQFPN48 (7 x 7 mm) WLCSP52 (3.17 x 3.11 mm) WLCSP68 (3.11 x 3.17 mm)

Product types	Flash memory (Kbytes)	RAM (Kbytes)	ULP comparators	ULP timers	I3C	Coupling and chaining bridge (CCB)	Secure data storage with hardware unique key (HUK)	Public key accelerator (PKA)	AES 128/256 + S-AES
STM32U375	512 to 1024	256	2	4	2				
STM32U385	1024	256	2	4	2	•	•	•	•





STM32Cube framework

Tools and software supporting you during all your design steps

Evaluation, prototyping, and selection

Hardware and software configuration

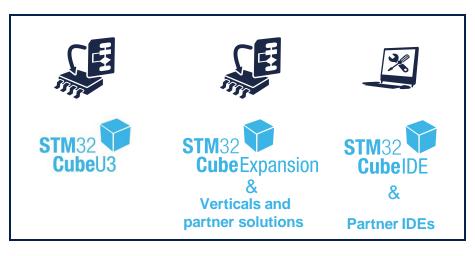
Application development and debug

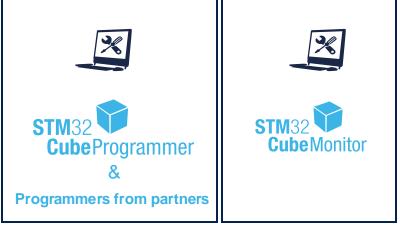
Code and hardware options programming

Run-time application monitoring









Worldwide support channels





Releasing your creativity



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community.st.com



st.com/STM32U3



wiki.st.com/stm32mcu



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STM32 MCU Developer Zone

Our technology starts with You



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