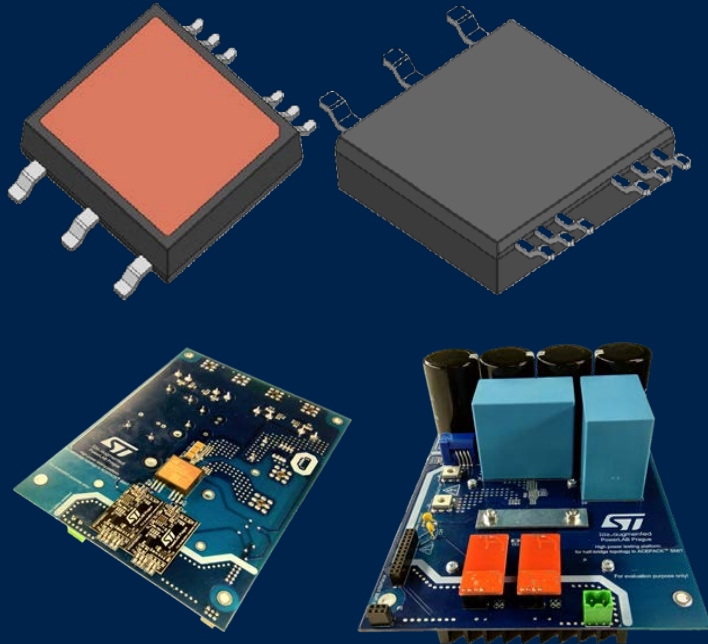




## ACEPACK™ SMIT



# New Molded Module for Industrial Power Applications

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v. 1.0 – March 2021



## **ACEPACK™ SMIT: Moulded Module for Power Applications:**

- **Technical Marketing**
  - Introduction
  - Motivation & Product Positioning
  - Planned portfolio & Application Examples
  - Promotion Material
- **Application Engineering**
  - Test Board dedicated to SMIT

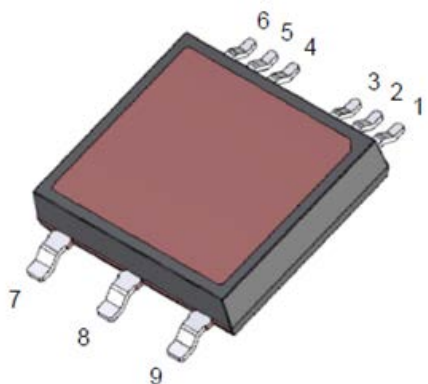
# Introduction



# ACEPACK™ SMIT: Introduction and Main Characteristics

Why SMIT? → Surface Mounted Isolated Top-Side Cooled Package

It looks like a discrete.... But it is a module!



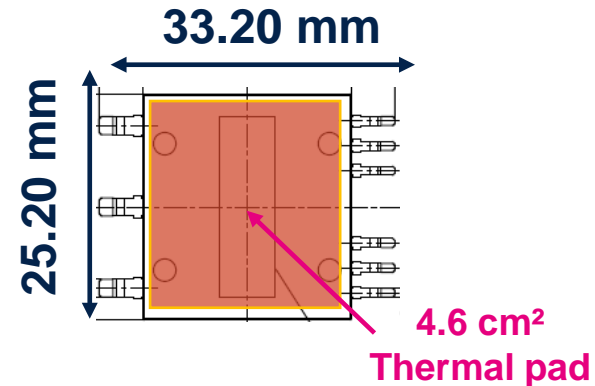
- It is molded
- It has a leadframe
- It is an SMD
- It is available in T&R\*

\* Tape and reel

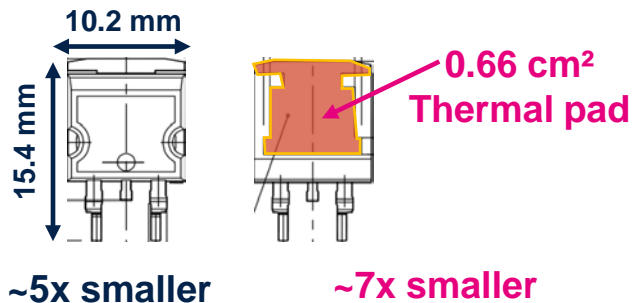
- It contains a DBC\*\*
- It has integrated dies forming simple topologies
- It has an isolated thermal pad

\*\* Direct Bond Copper

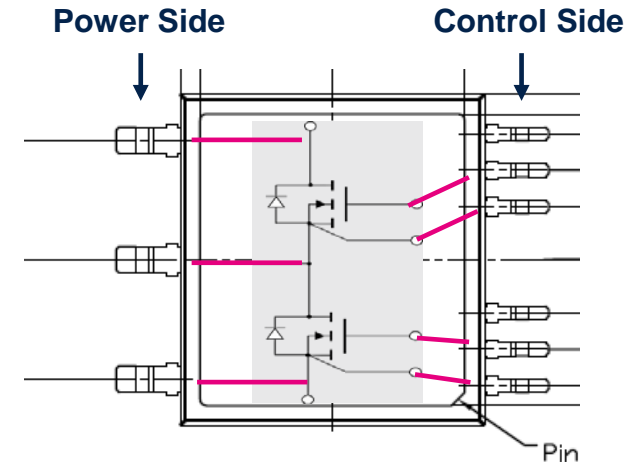
## Dimensions



For comparison: D2PAK



## Pin-out



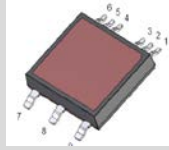
- This assembly is merely illustrative. Pin connections in real products may differ.
- In rectifiers the control pins might also be used for power.

# Motivation & Positioning

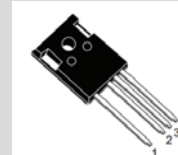


# Motivation: Why SMIT?

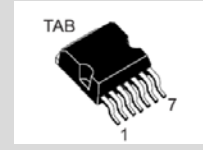
ACEPACK™ SMIT

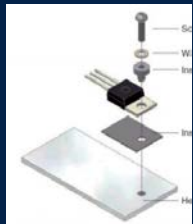


TO-247 (4pin)



H2PAK (7pin)



Isolation	Creepage pin-to-pin	7 mm ✓✓	2.4 mm ✗	5.9 mm ✓
	Isolated thermal pad	YES <sup>1</sup> ✓	NO ✗	NO ✗
Thermal Design	Isolation foil to heat-sink	Application dependent —	Required ✗	Required <sup>2</sup> ✗
	Dissipation capability (forced air)	60 to 100 W ✓✓✓	30 to 50 W ✓✓	5 to 10 W <sup>3</sup> ✓
Assembly	Automatized assembly	YES ✓	Every single device requires: ✗  Screw Washer Thermal foil Alignment to heat-sink Isolation test	YES ✓
	Production capability	>50 systems/day <sup>4</sup> ✓	<10 systems/day <sup>4</sup> ✗	N.A. —

<sup>1</sup> UL 2.5 kVrms granted  
4kVrms applied

<sup>4</sup> Based on customer feedback  
for OBC systems

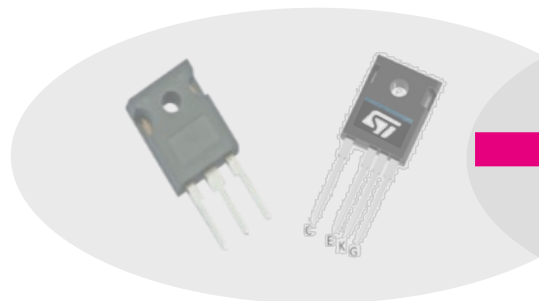
<sup>2</sup> can be avoided by Insulated metal  
substrate (IMS) board

<sup>3</sup> can be extended to 15W with IMS.

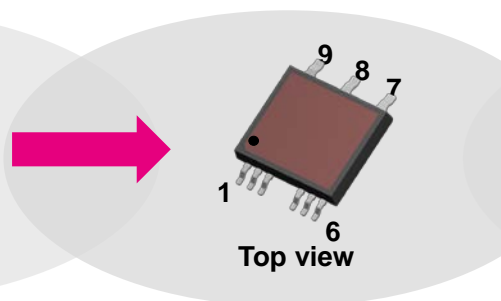


# Power Classes

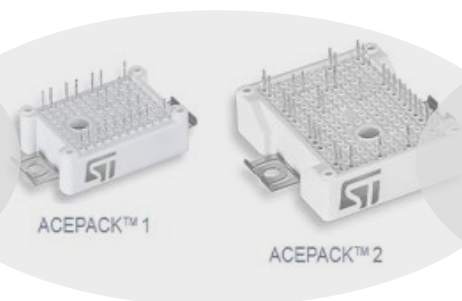
Discretes



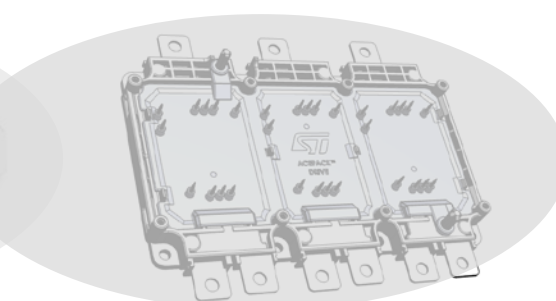
SMIT



Baseplate-less Modules



Baseplate Modules



1kW

10kW

50kW

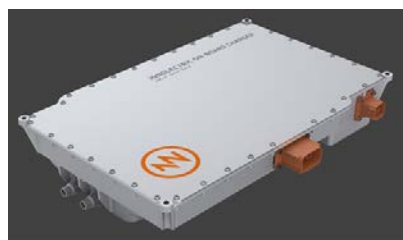
100kW

300kW

## Focus Applications



SMPS



On Board Charger  
(Automotive or Industrial)



EV Charger  
(Bi-directional)

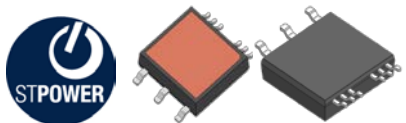


Storage Systems



Solar

# Product Portfolio



Automotive Grade

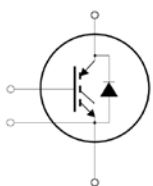
# Planned Product Portfolio

Topology	Part number		$R_{ds,on}$ / rated A* per chip	Semiconductor Technology	Samples	Planned SOP
Single Switch	STGSB200M65DF2AG		200A	650V IGBT	Available	In Production
3-ph Bridge	STPSC60H12-M3Y		60 A	1200V SiC Diodes	Available	Q2/21
Full-Bridge	STPSC40H12-M2Y		40 A	1200V SiC Diodes	Available	Q2/21
Full-Bridge	STTH120RQ06-M2Y		120 A	600V Fast recovery diodes	Available	Q3/21
Full-Bridge	STTH60RQ06-M2Y		60 A	600V Fast recovery diodes	Available	In production
SCR Half-Bridge	STTN6050H-12M1Y		60 A	1200V Thyristors	Available**	Q2/21
Mixed Bridge	STTD6050H-12M2Y		60 A	1200V Thyristors & Rectifiers	Available**	Q2/21
Half-Bridge	SH25M12W2		25 mΩ	1200V SiC MOSFETs	Available**	Q3/21
Half-Bridge	SH30M12W2AG		30 mΩ	1200V SiC MOSFETs	Available**	tbd
DuoPack	SD30M12WAG		30 mΩ	1200V SiC MOSFETs	Q1/21**	Tbd
DuoPack	SD42N60DM6AG		42 mΩ	600V MDmesh DM6 SJ MOSFET	Q2/21	Q4/21
Half-Bridge	SH32N65DM6AG		32 mΩ	650V MDmesh DM6 SJ MOSFET	Q2/21	Q4/21
Half-Bridge	SH63N65DM6AG		46 mΩ	650V MDmesh DM6 SJ MOSFET	Q2/21	Q4/21
Half-Bridge	SH68N65DM6AG		71 mΩ	650V MDmesh DM6 SJ MOSFET	Q2/21	Q4/21
Boost	SB49W65M5AG		49 mΩ + 20A Diode	650V MDmesh M5 SJ MOSFET + SiC Diode	Q3/21	Q1/22

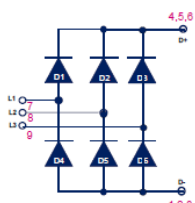
\*\* Engineering samples



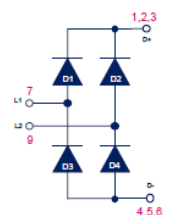
life.augmented



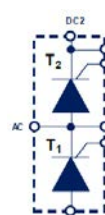
Single switch



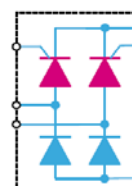
3-ph Bridge



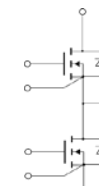
Full-Bridge



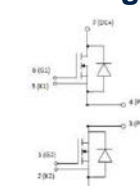
SCR Half-Bridge



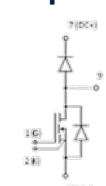
Mixed Bridge



Half-Bridge



DuoPack



PFC

# Application Examples

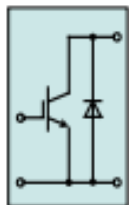


# Application Examples (1/3)

## Power Train

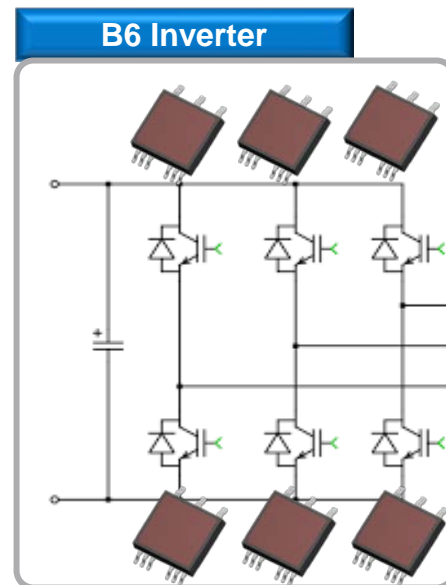
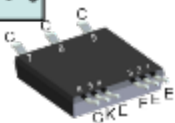
### STG5B200M65DF2AG

First SMIT in production



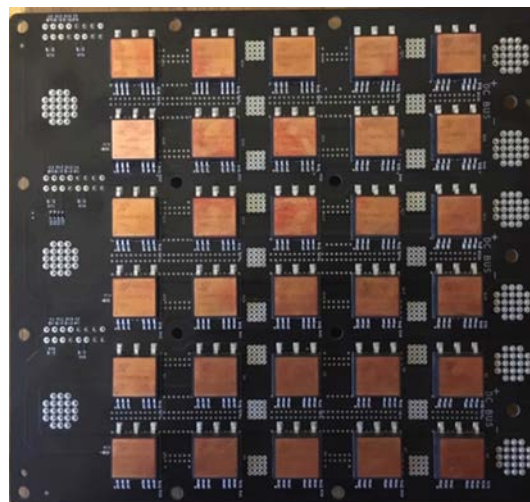
#### Product Features:

- 650V, 200A IGBT and freewheeling Diode
- Short circuit rated



Motor

105 kW Drive



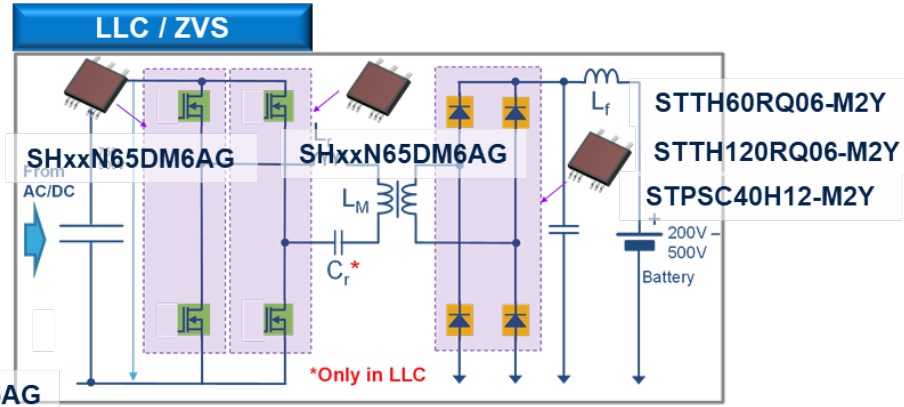
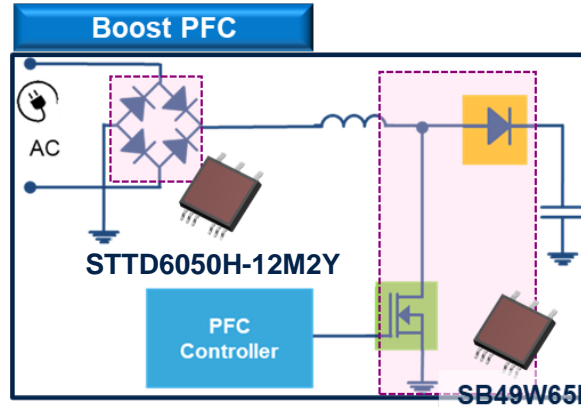
Power Train



# Application Examples (2/3)

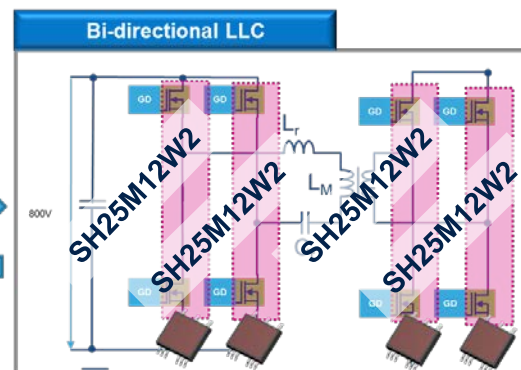
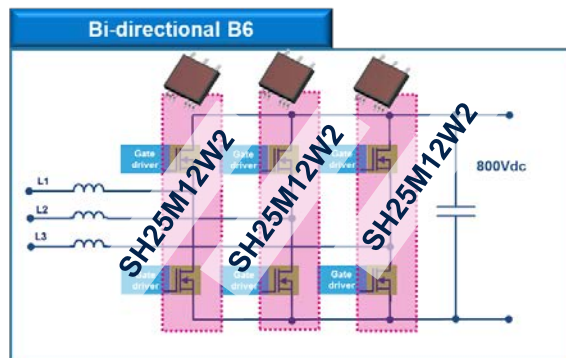
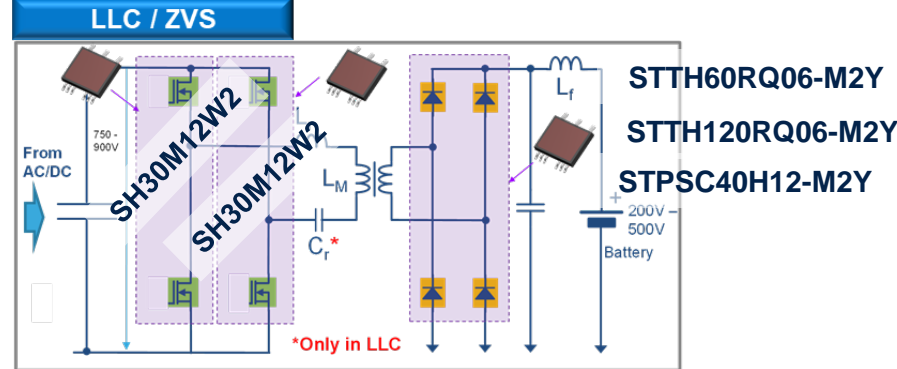
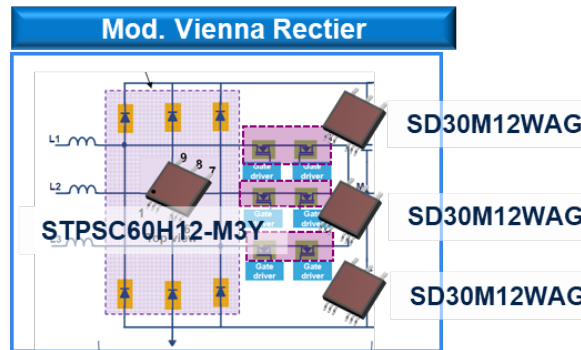
## Battery Chargers

1-phase



**On Board Charger**  
(Automotive or Industrial)

3-phase



**EV Charger**  
(Bi-directional)

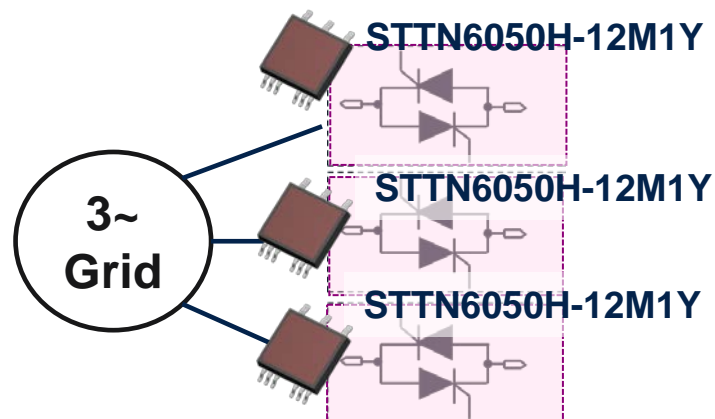


# Applications & Topologies (3/3)

## Solid State Relay

When compared to mechanical relay, power semiconductors are faster, more robust against mechanical stress, and suffer less degradation over time.

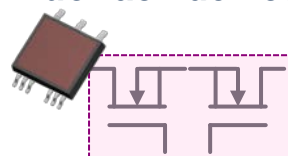
### SCRs



Industrial Drive

### MOSFETs

Back-to-back configuration, made out of DuoPack devices:



SD42N60DM6AG\* → for 400V Batteries  
SD30M12WAG → for 800V Batteries



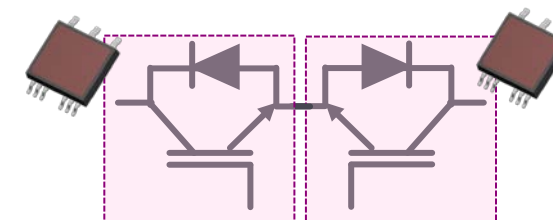
EV Charger



Electric Vehicle

### IGBTs

Back-to-back configuration, made with single switch devices:



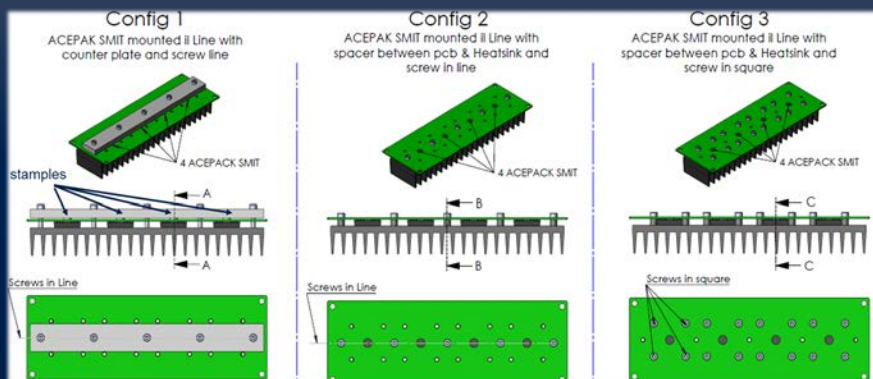
STGSB200M65DF2AG



Transformer protection

# Assembly of ACEPACK™ SMIT

## Assembly Possibilities



## Application Note + Presentation



AN5384

Application note

ACEPAK SMT module package guidelines for mounting and thermal management

### Introduction

STMicroelectronics is introducing a new module package called ACEPAK SMT. This package is a surface mount power module which is over-molded with an epoxy molding compound. The semiconductor chips are connected on a direct bonded copper (DBC) ceramic substrate. The DBC structure is composed of 3 layers: copper, alumina substrate and copper. The alumina layer provides the required insulation with a specified voltage of 3450 V RMS.

The behavior of a semiconductor device depends on the temperature of its silicon chip. This is the reason why the electrical parameters are given at a specified temperature. To achieve the performance of these devices, the temperature has to be limited by managing the heat transfer between the chip and the ambient atmosphere. The aim of this application note is to provide guidelines for the package mounting, handling and soldering, as well as thermal considerations linked to heatsink types and assembly methods.

The ACEPAK SMT is designed to be surface-mounted on a printed circuit board while having its opposite top side connected to an external heatsink in order to extract the maximum power dissipation out of the device and optimize thermal performance. This module will embed a range of power devices containing thyristors, rectifier diodes (silicon or silicon carbide) and transistors such as MOSFETs (Si or SiC) and IGBTs.

The main applications where the ACEPAK SMT fits are: battery chargers in on-board, off-board and industrial versions, power converters for data centers, UPS and renewable energy as well as motor drive systems.

Figure 1. ACEPAK SMT package overview



Note: ACEPAK SMT is a registered and/or unregistered trademark of STMicroelectronics International NV or its affiliates in the EU and/or elsewhere.

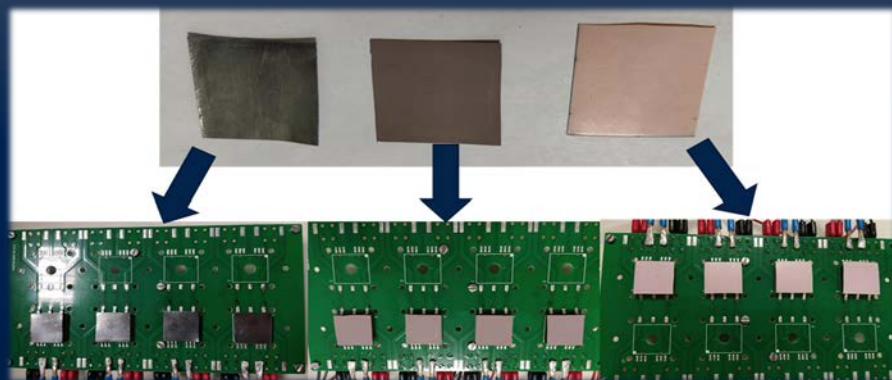
document: Rev 1 - November 2016

www.st.com

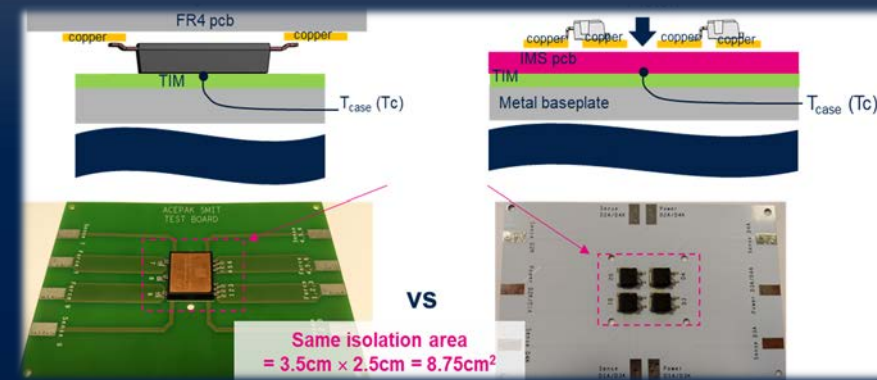
## Heat-sink Attachment



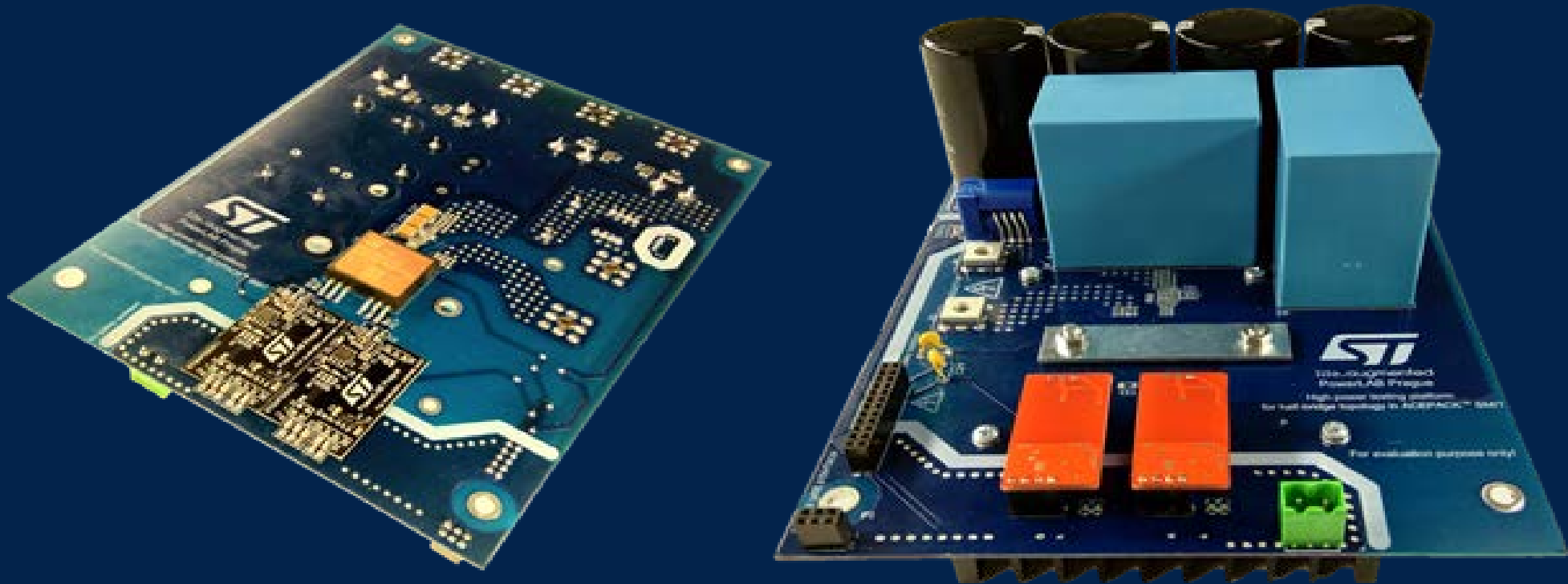
## Suggested TIM\*



## Comparison with D2PAK

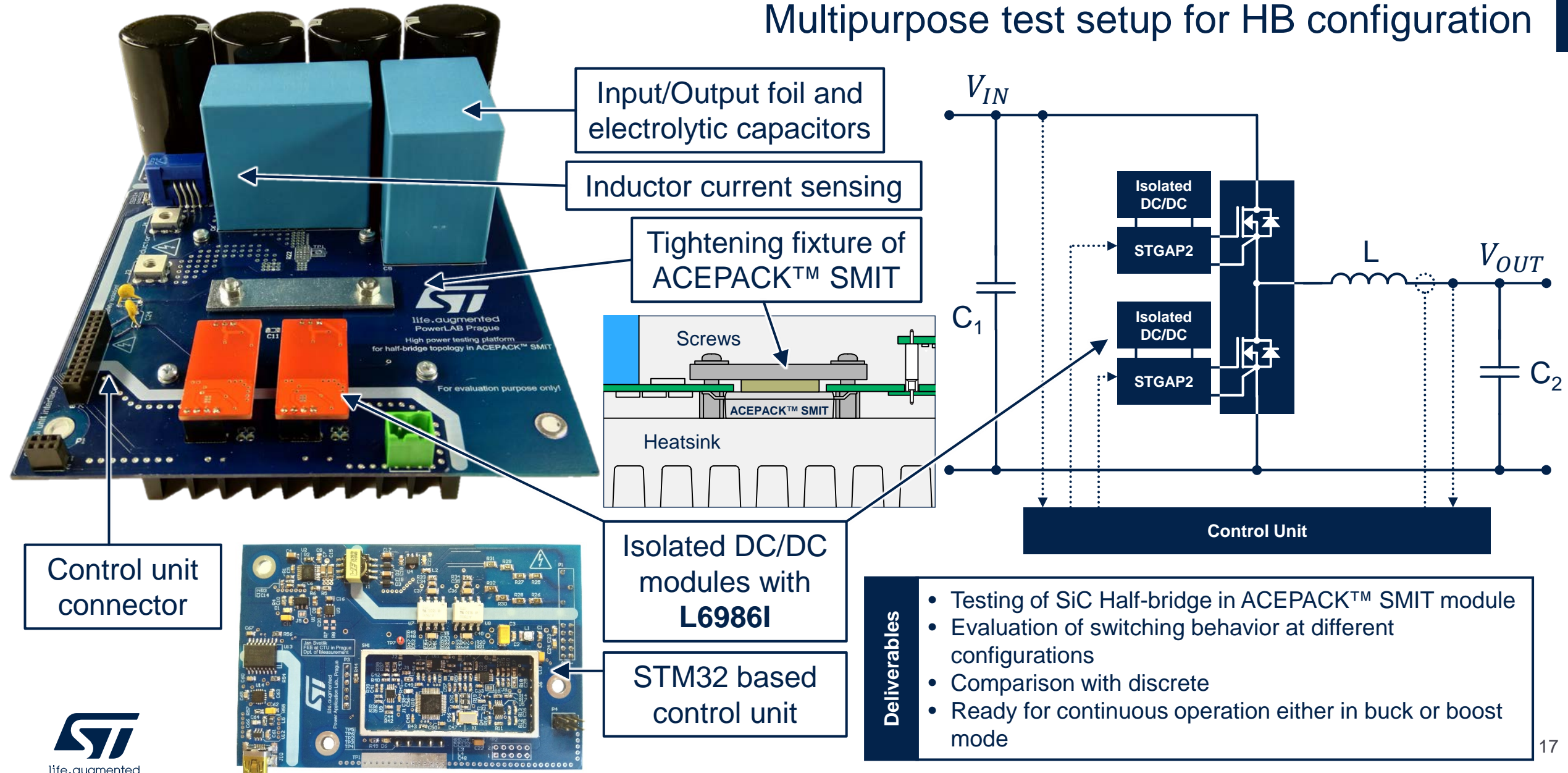


# ACEPACK™ SMIT Application Board



# ACEPACK™ SMIT testing platform (1/2)

## Multipurpose test setup for HB configuration

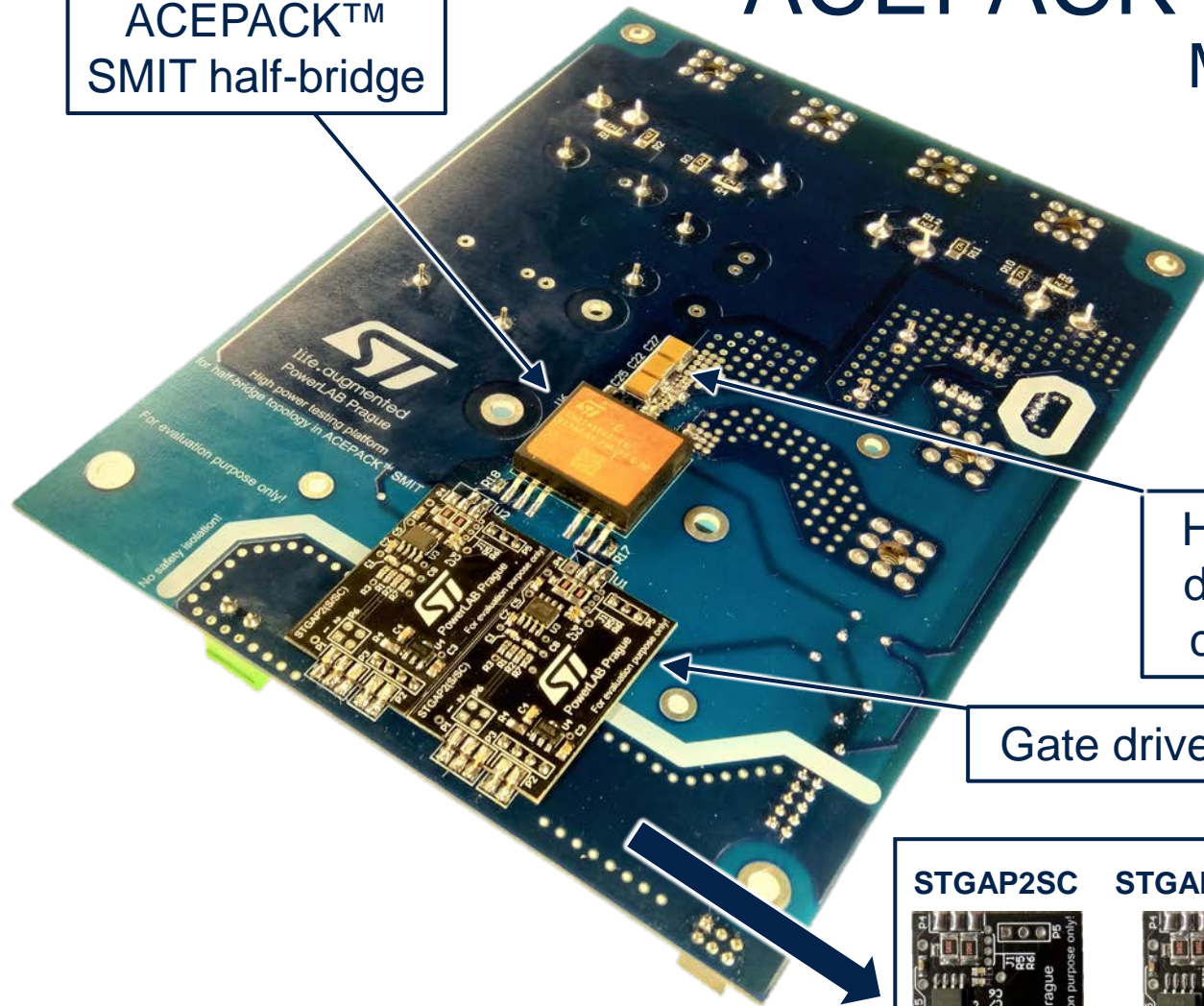


Bottom side view

ACEPACK™  
SMIT half-bridge

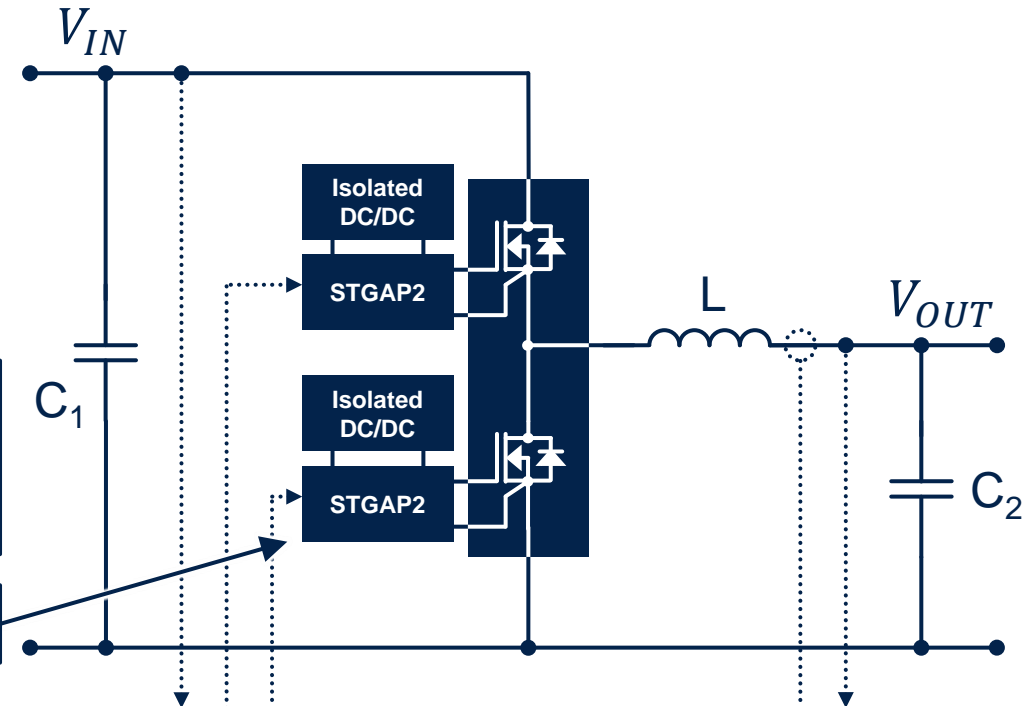
# ACEPACK™ SMIT testing platform (2/2)

Multipurpose test setup for HB configuration



Half-bridge  
decoupling  
capacitors

Gate driver modules



STGAP2SC

STGAP2SiCSC

STGAP2D

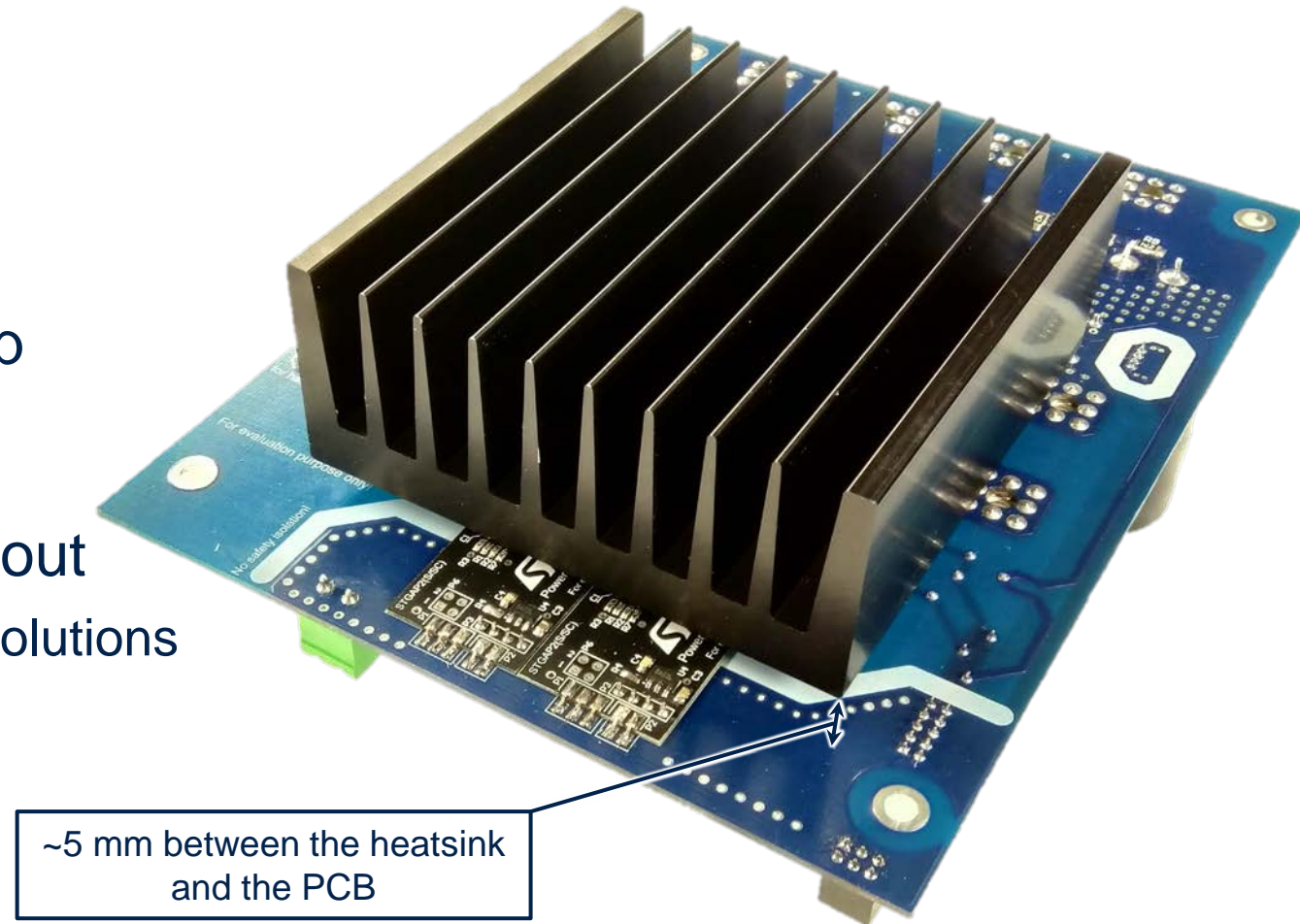


Control Unit

- Different driver modules with different gate drivers can be used

# Summary of the key points about ACEPACK™ SMIT

- Benefits of driving via Kelvin source
  - ⇒ Optimal driving
  - ⇒ Better switching performance
- SMD package with cooling pad on top
- Improved thermal performance
- Convenient pinout for easier PCB layout
  - Not a common property over all modular solutions
- Smaller commutation loop
  - ⇒ Faster switching



- Top layer
- Bottom layer
- Top components
- Bottom components

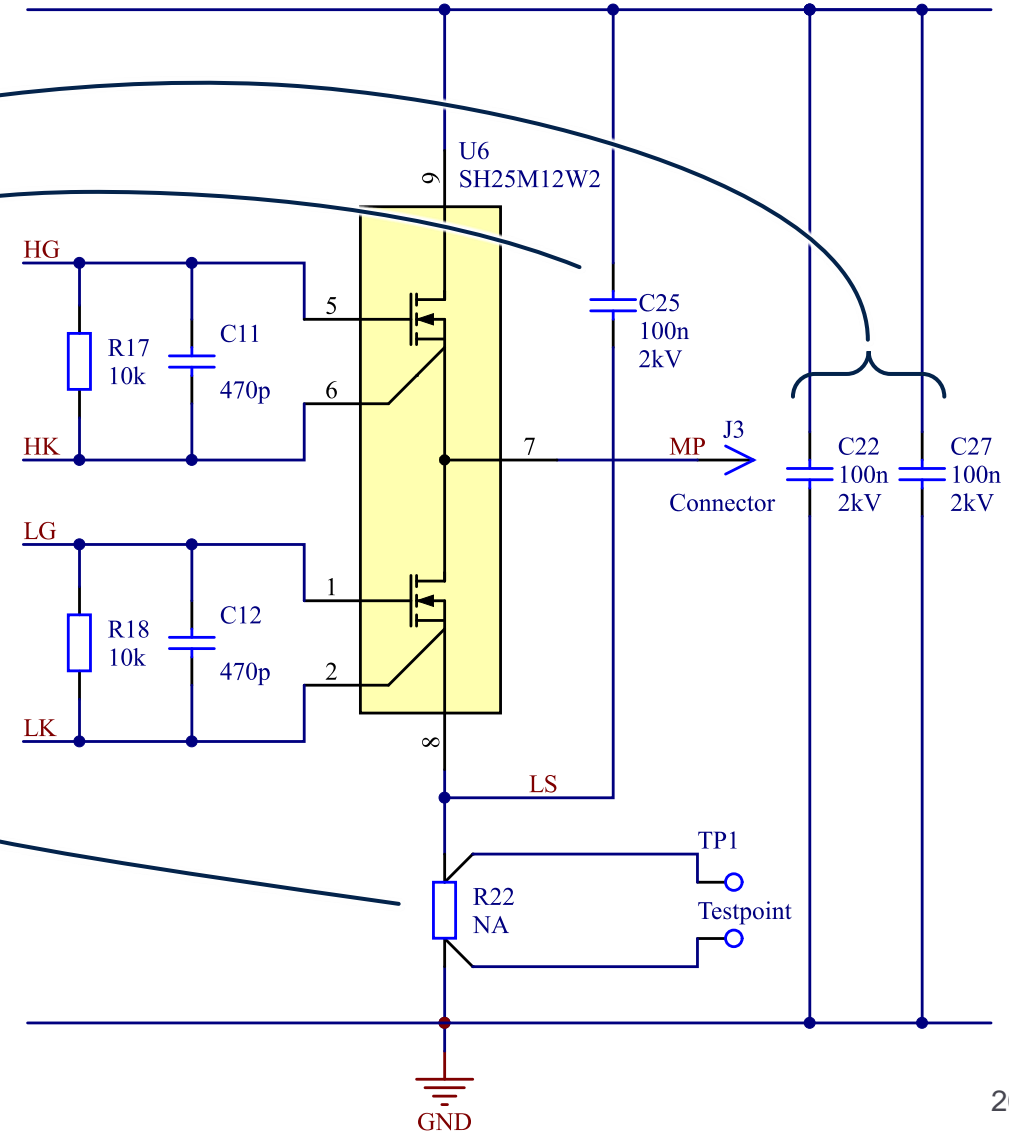
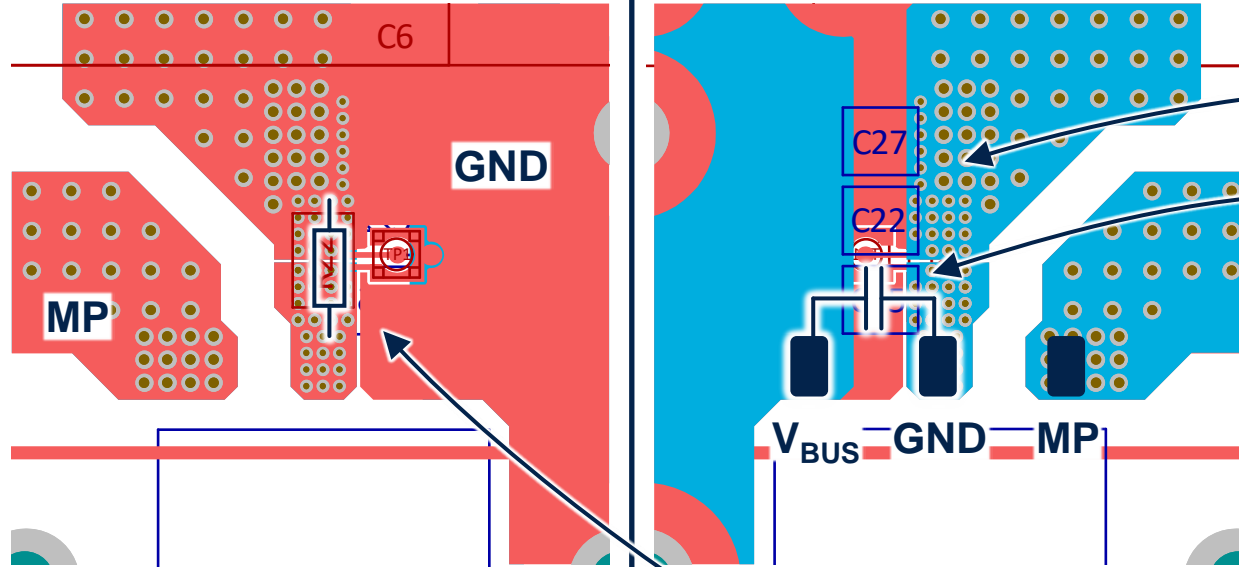
# Test platform PCB layout details (1/5)

## Commutation loop components

ACEPACK™ SMIT

Top side view

Bottom side view



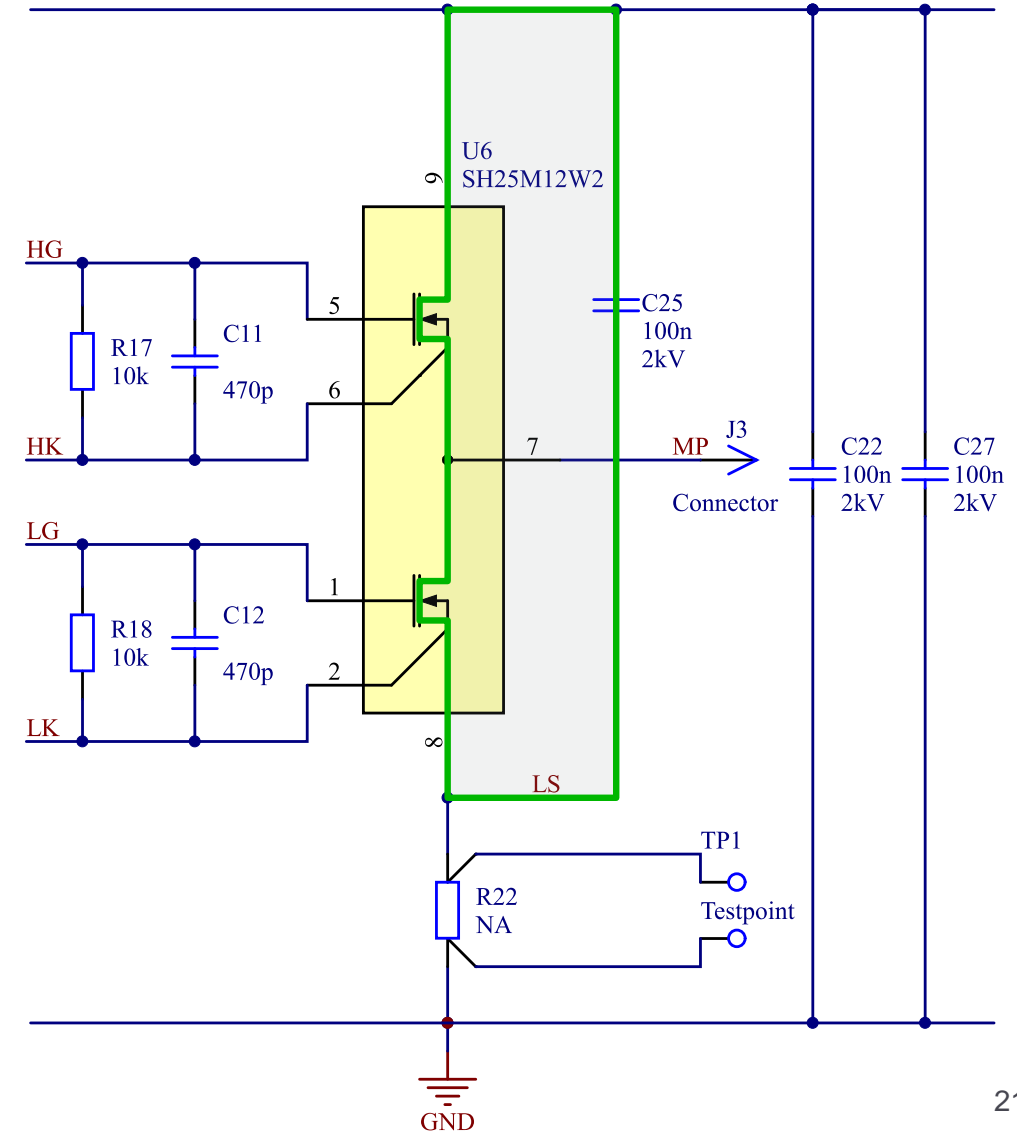
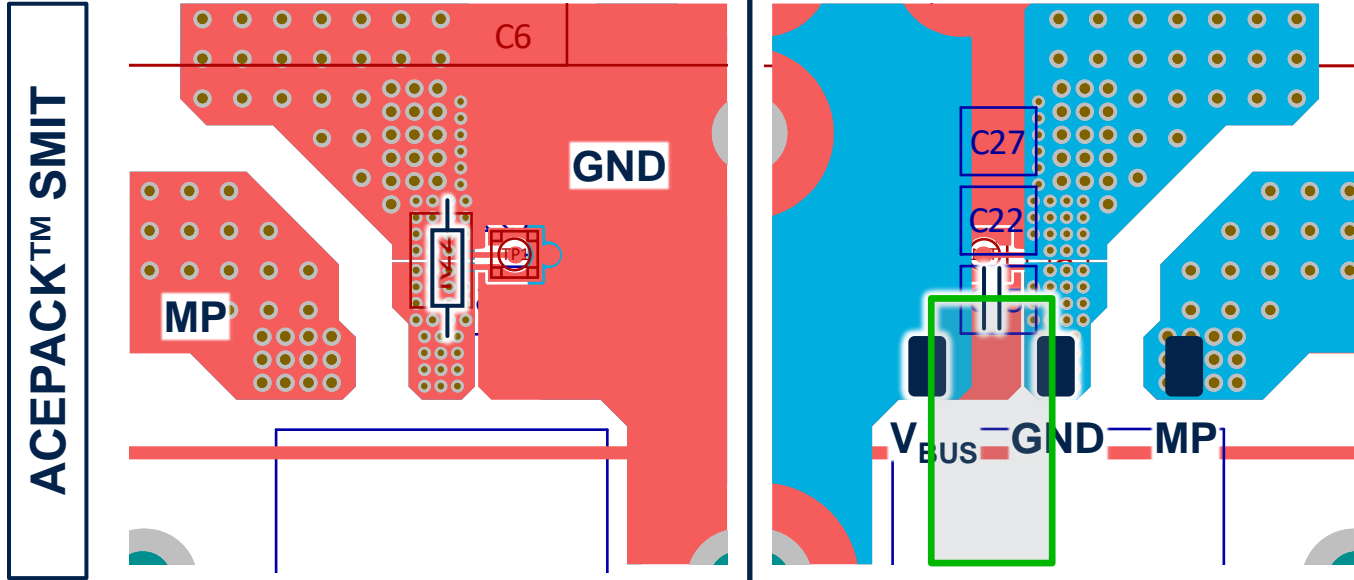
- Top layer
- Bottom layer
- Top components
- Bottom components

# Test platform PCB layout details (2/5)

## Size of the commutation loop

Top side view

Bottom side view



- Top layer
- Bottom layer
- Top components
- Bottom components

# Test platform PCB layout details (3/5)

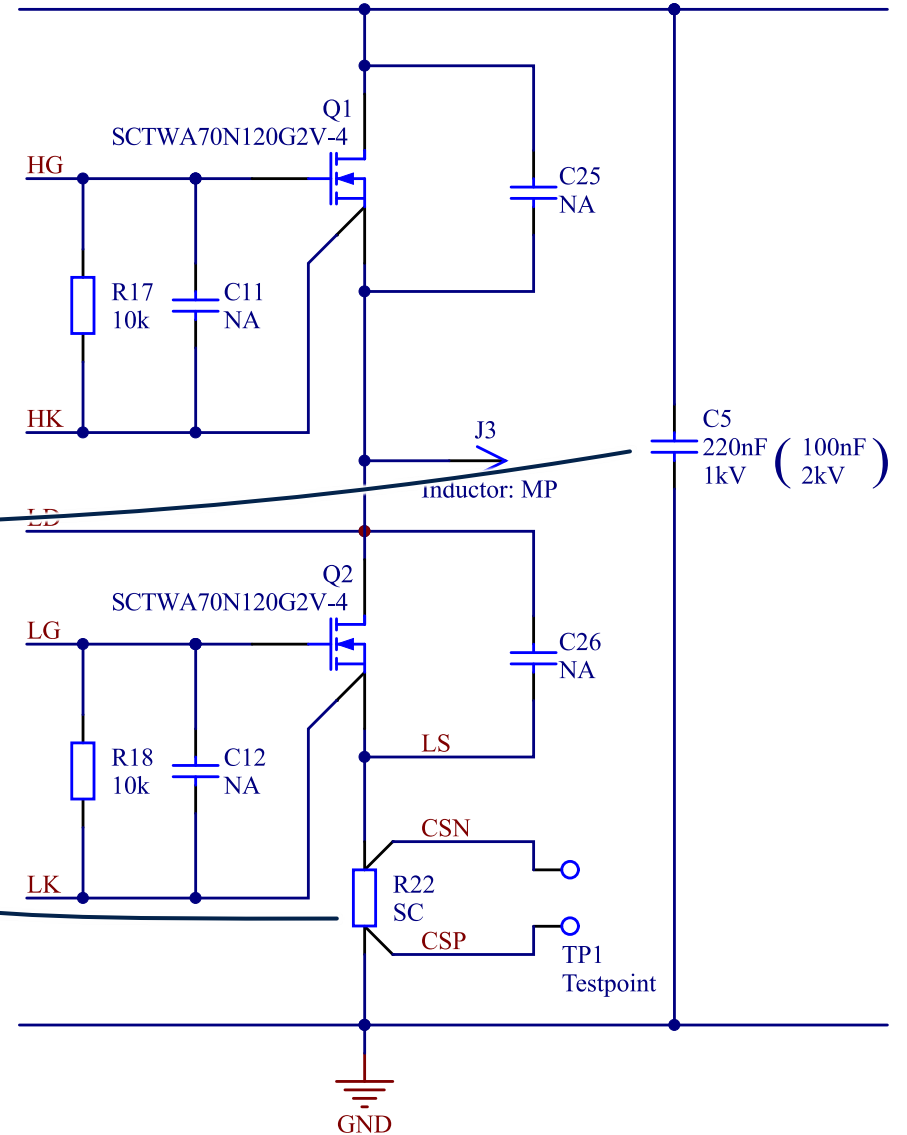
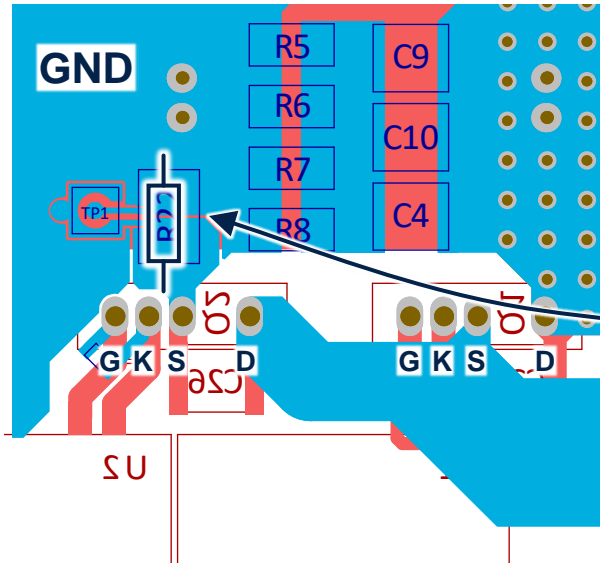
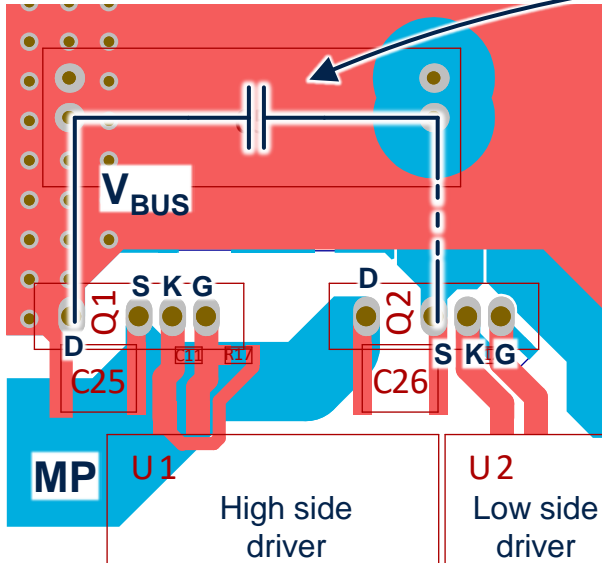
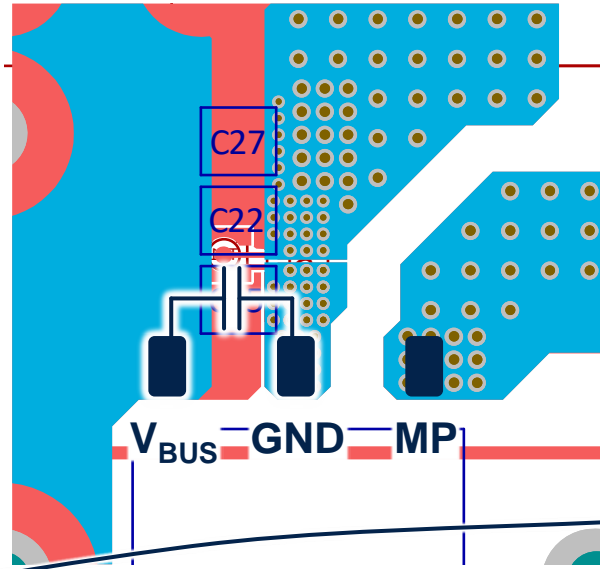
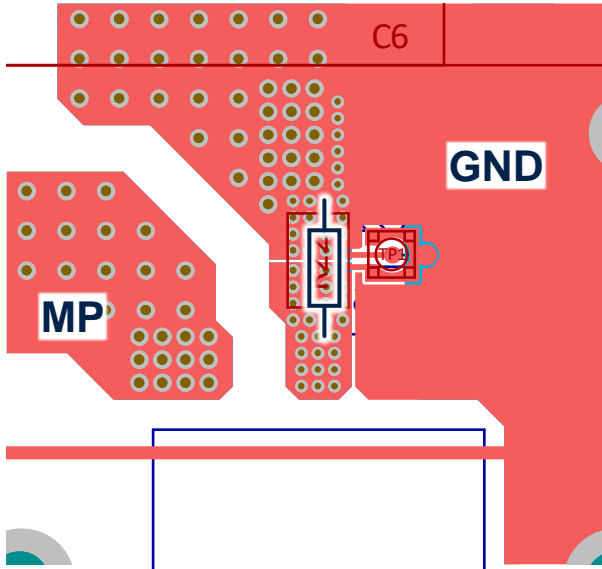
## Comparison with TO247-4 solution

Top side view

Bottom side view

ACEPACK™ SMIT

TO247-4



- Top layer
- Bottom layer
- Top components
- Bottom components

# Test platform PCB layout details (4/5)

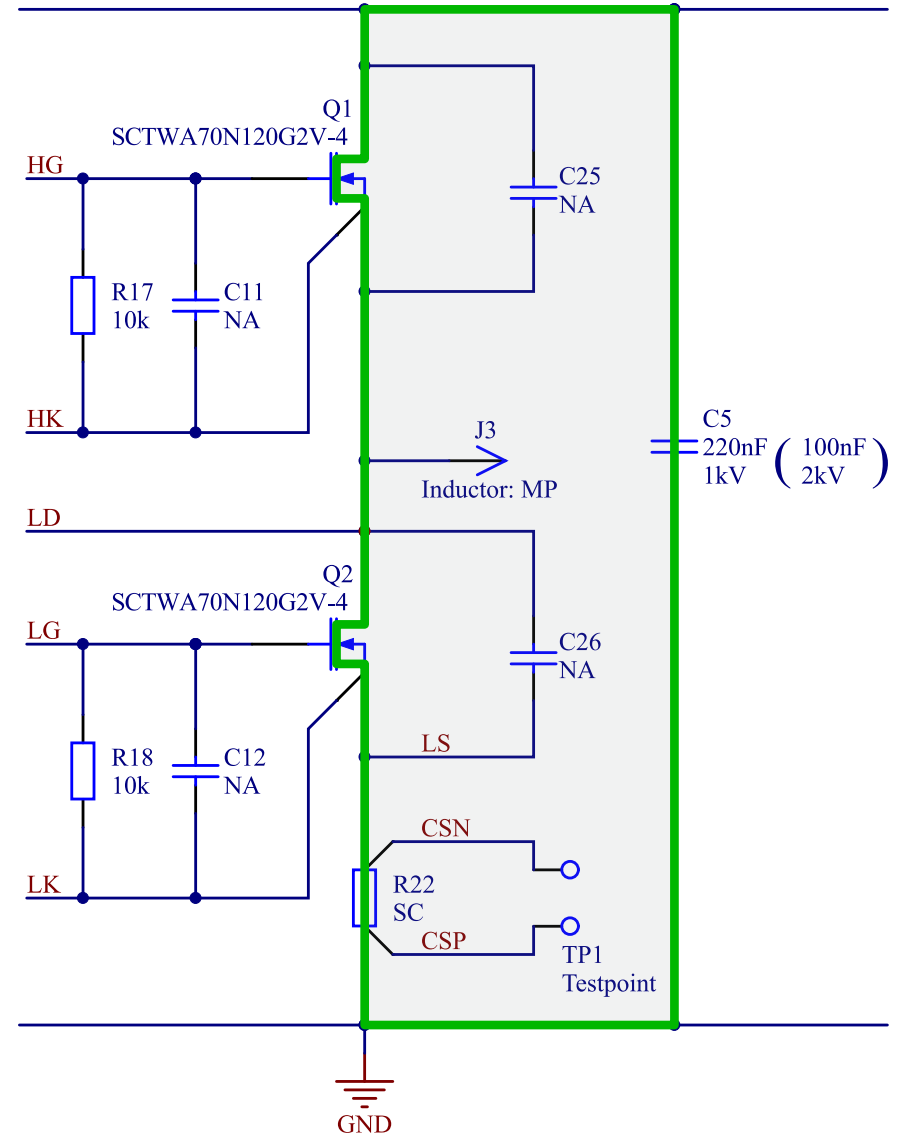
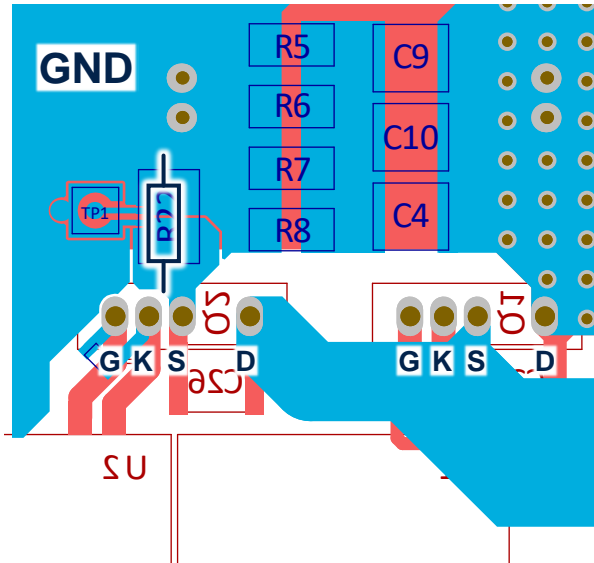
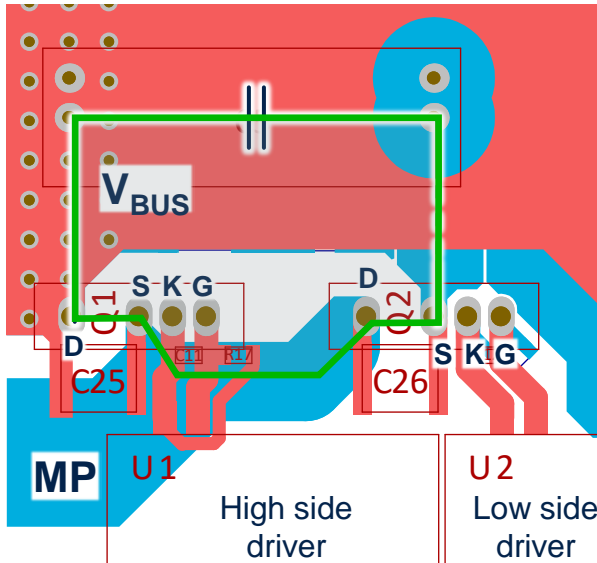
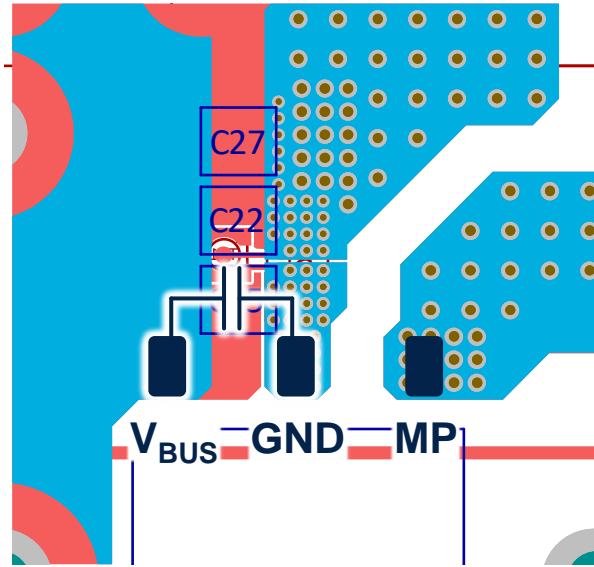
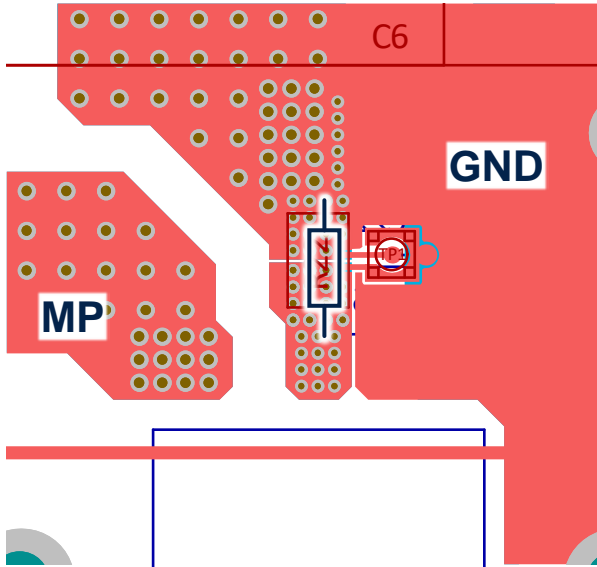
Size of the CL at TO247-4 platform

Top side view

Bottom side view

ACEPACK™ SMIT

TO247-4



■ Top layer      ■ Top components  
■ Bottom layer    ■ Bottom components

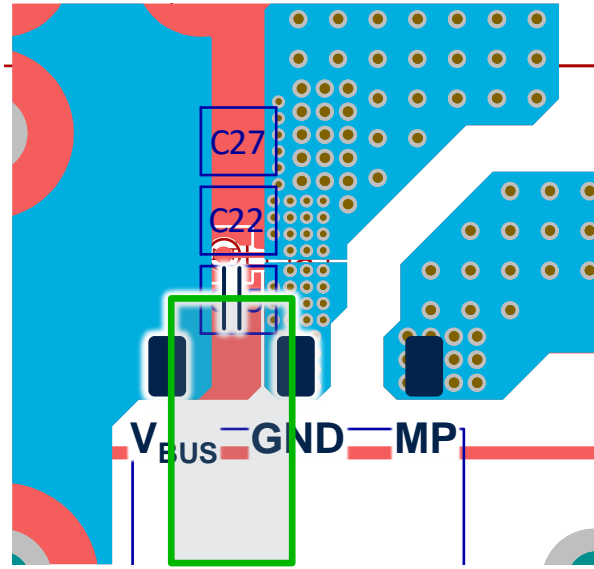
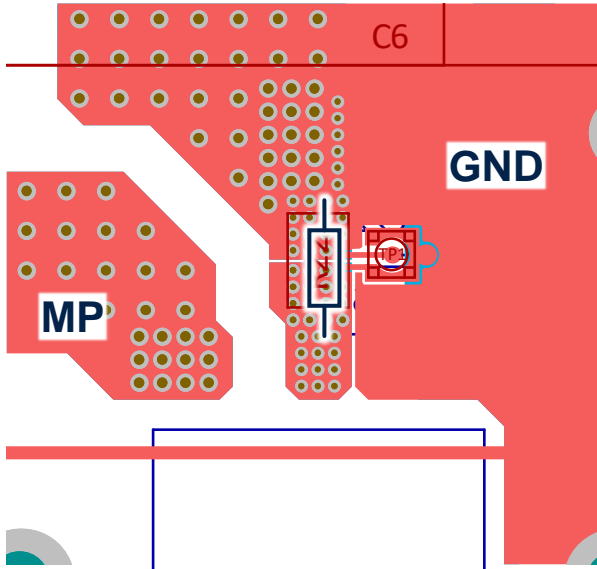
# Test platform PCB layout details (5/5)

## ACEPACK™ SMIT vs TO247-4

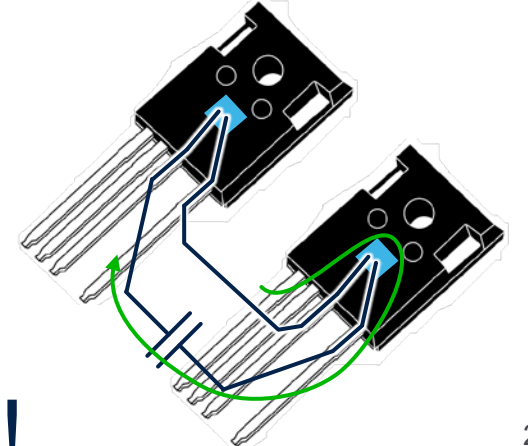
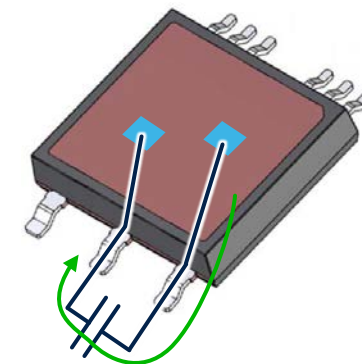
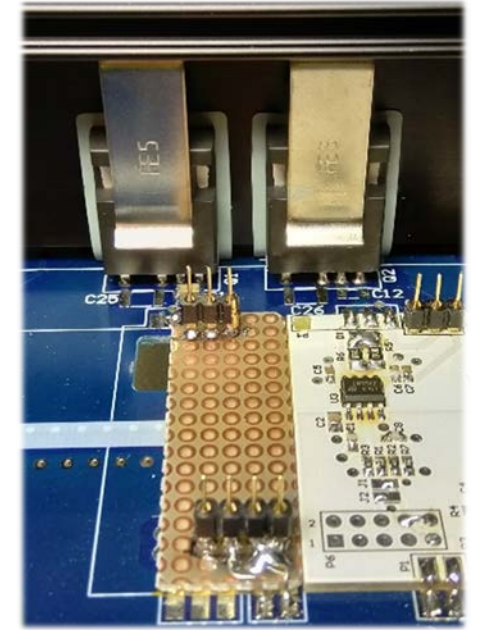
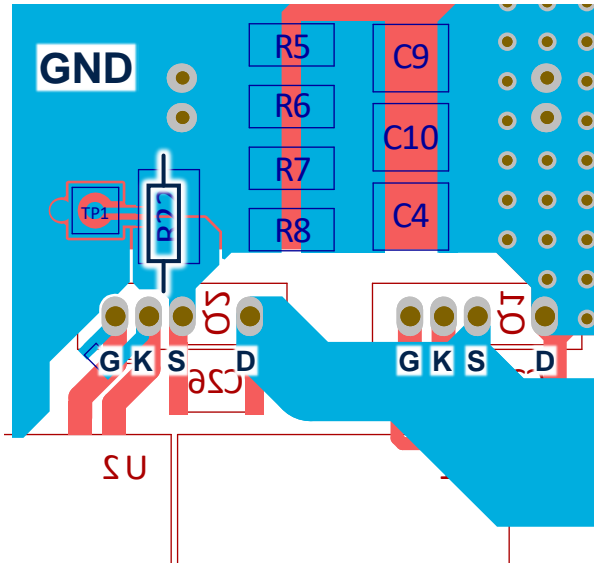
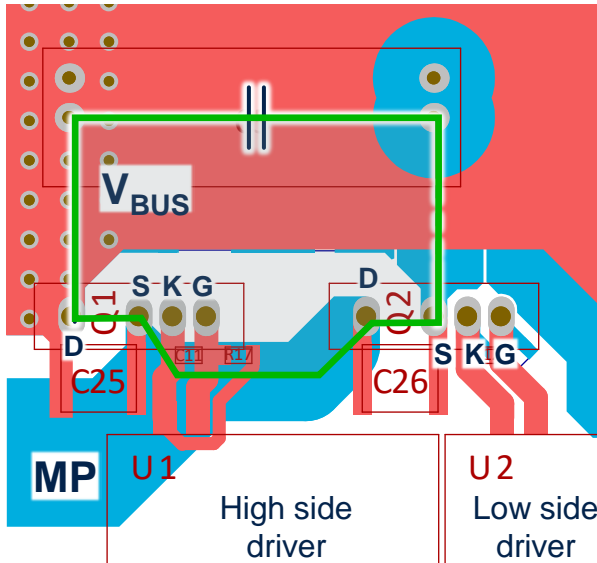
Top side view

Bottom side view

ACEPACK™ SMIT



TO247-4



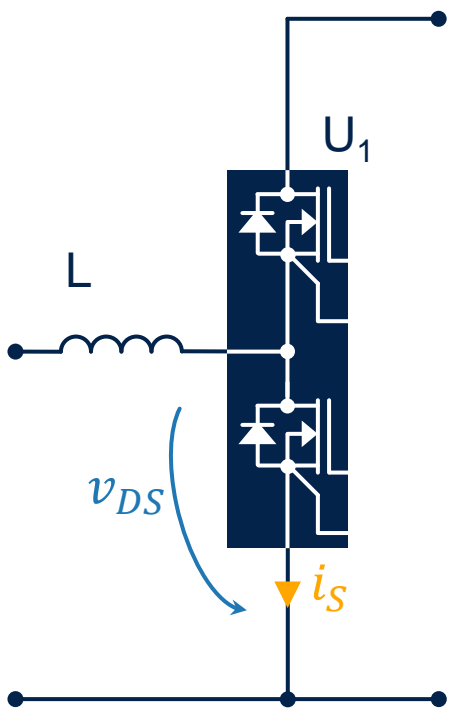
- 60% shorter path !
- 40% smaller area !

$V_{\text{BUS}}$	800 V
$I_{\text{PK}}$	50 A
$R_G$	22, 31, 41, 50 $\Omega$
$T_A \sim T_J$	25 °C

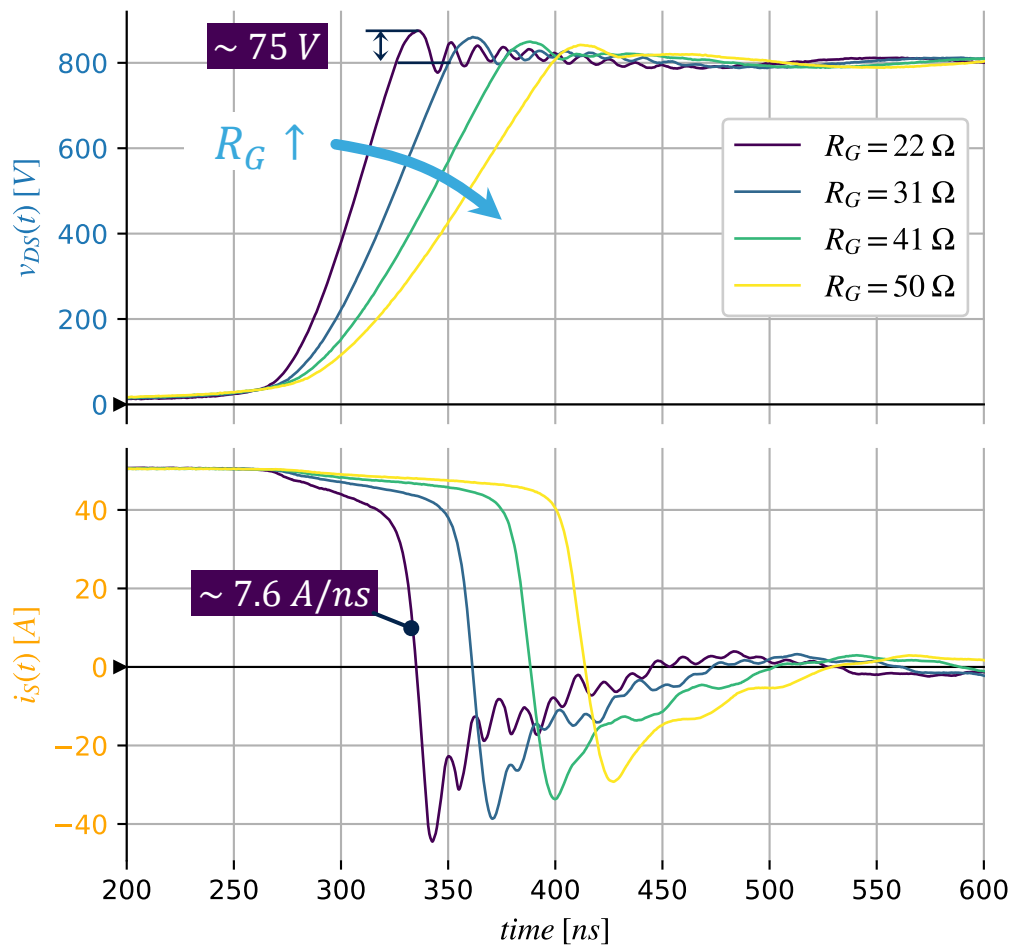
# Testing of a SH25M12W2 device (1/2)

## Switching waveforms at different conditions

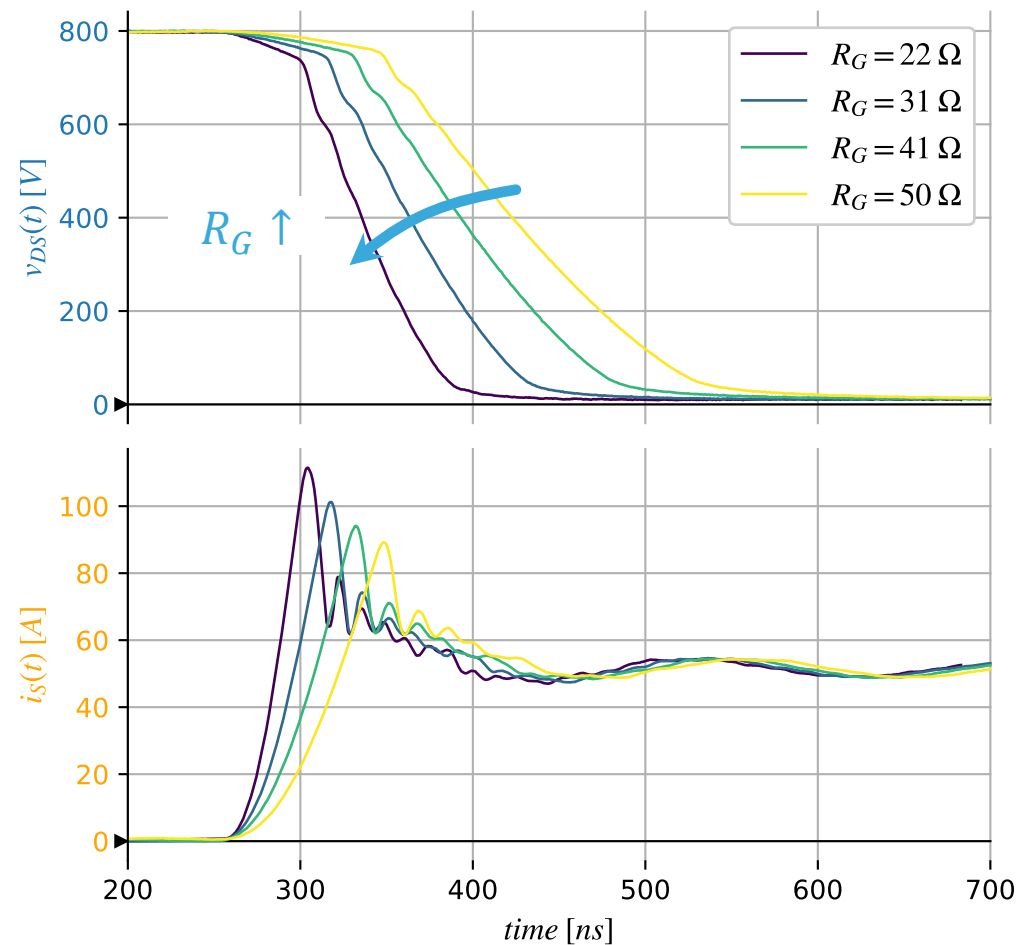
**U<sub>1</sub>: SH25M12W2**



Turn off transient waveforms



Turn on transient waveforms



$L_{CL} < 10 \text{ nH} \Rightarrow$  more than **80% less** compared to TO247 4-pin!



# Thank you

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