Schematic diagrams

Figure 1. AEK-MCU-C4MLIT3 circuit schematic (1 of 11)

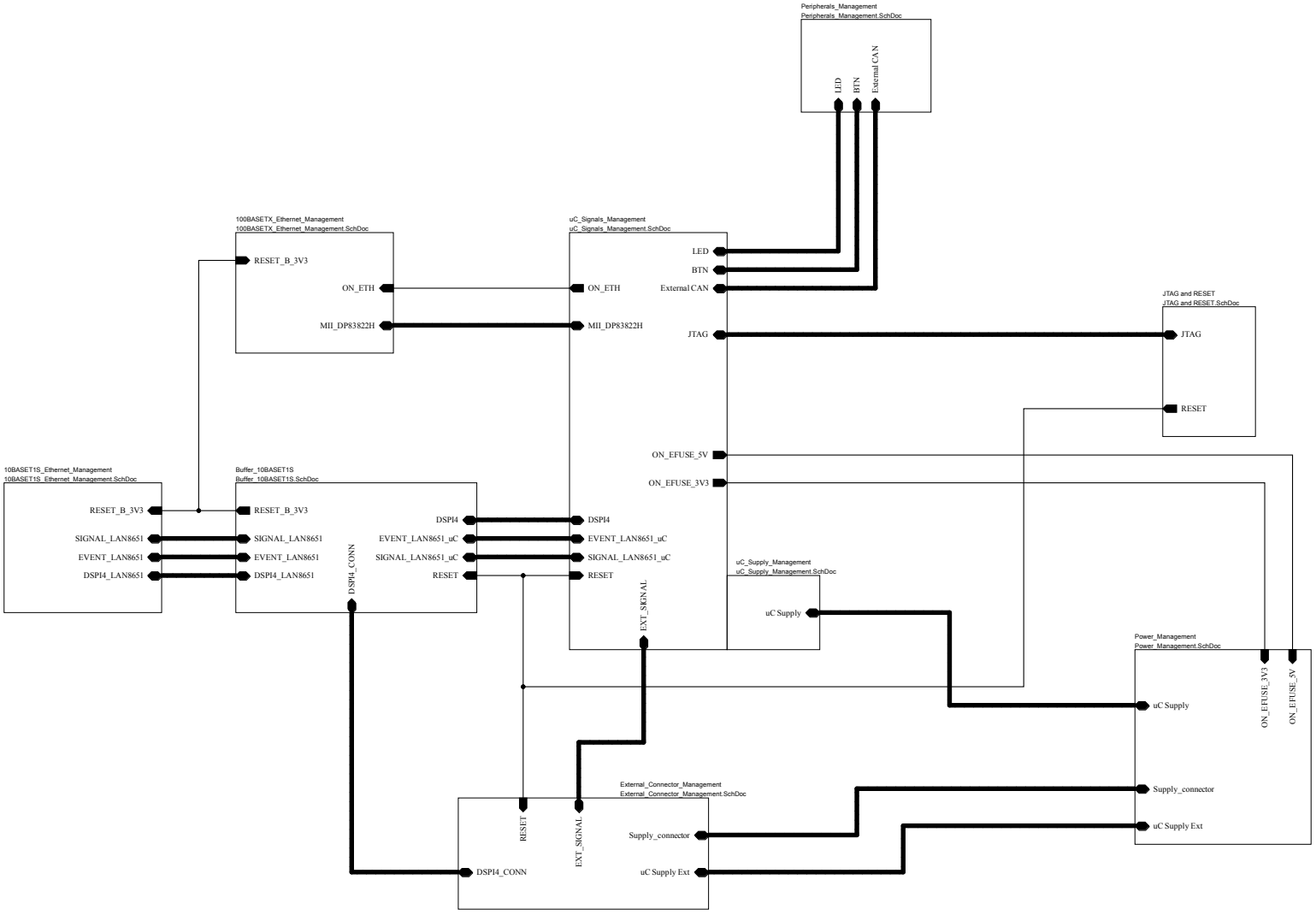


Figure 2. AEK-MCU-C4MLIT3 circuit schematic (2 of 11)

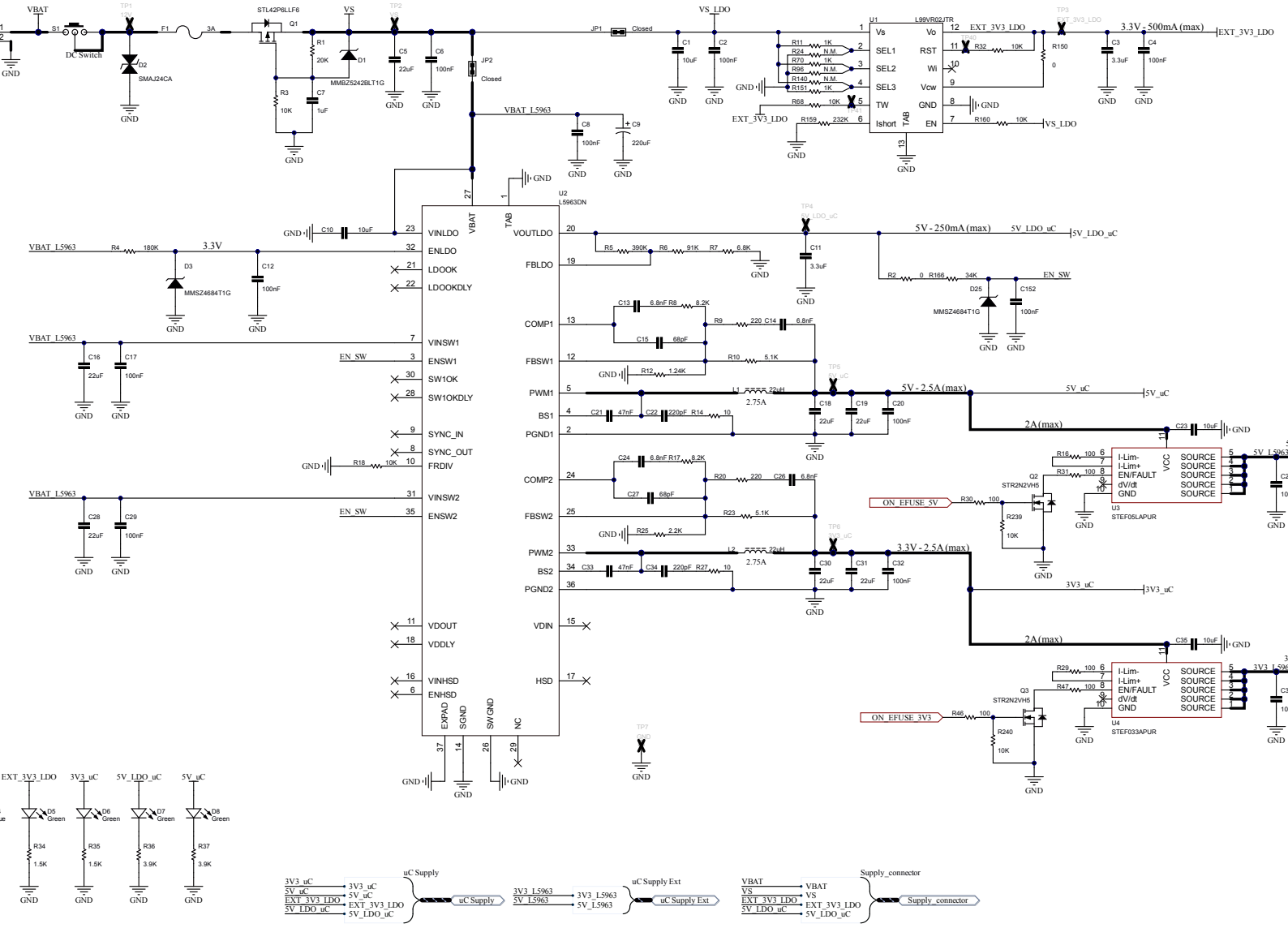


Figure 3. **AEK-MCU-C4MLIT3 circuit schematic (3 of 11)**

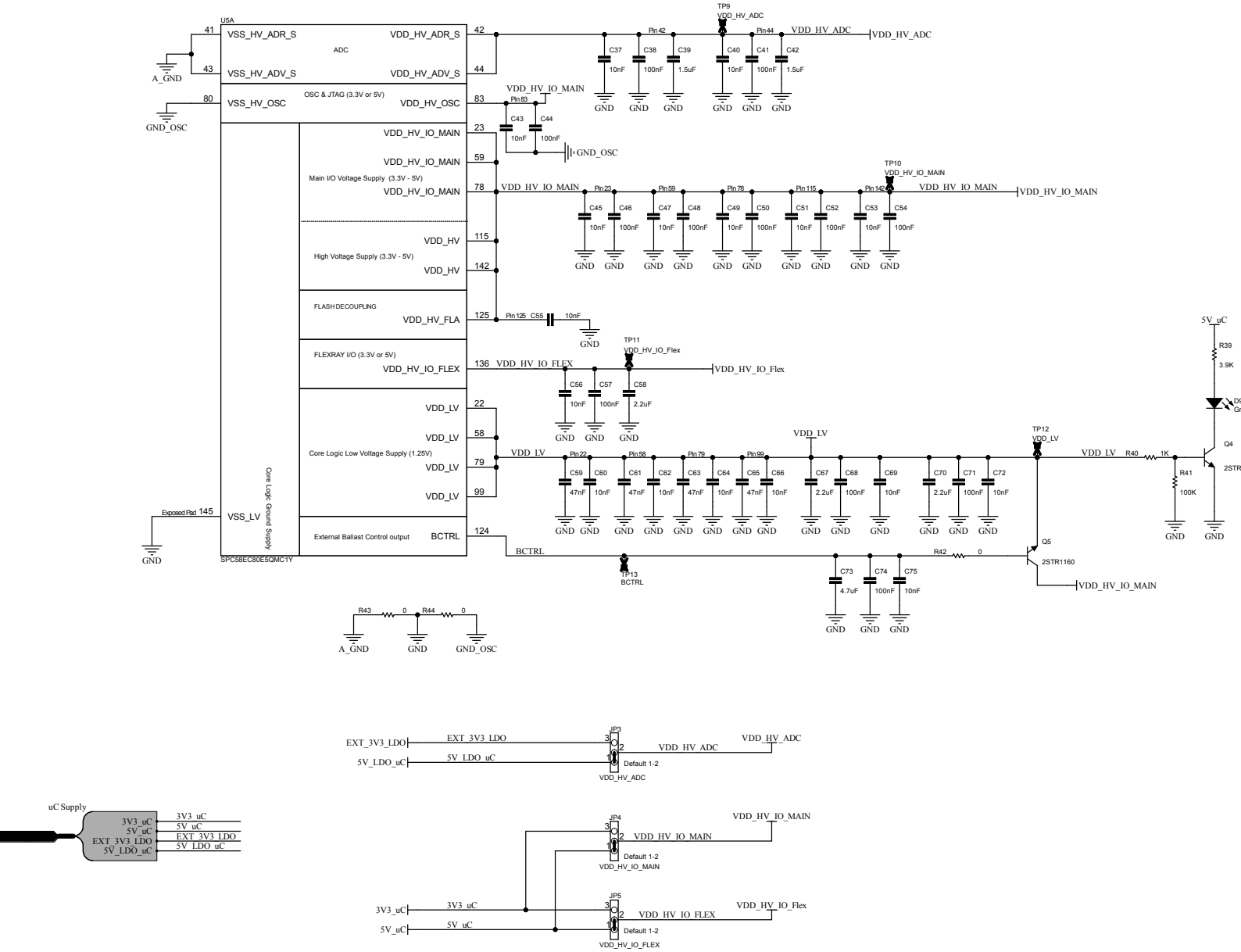


Figure 4. **AEK-MCU-C4MLIT3 circuit schematic (4 of 11)**

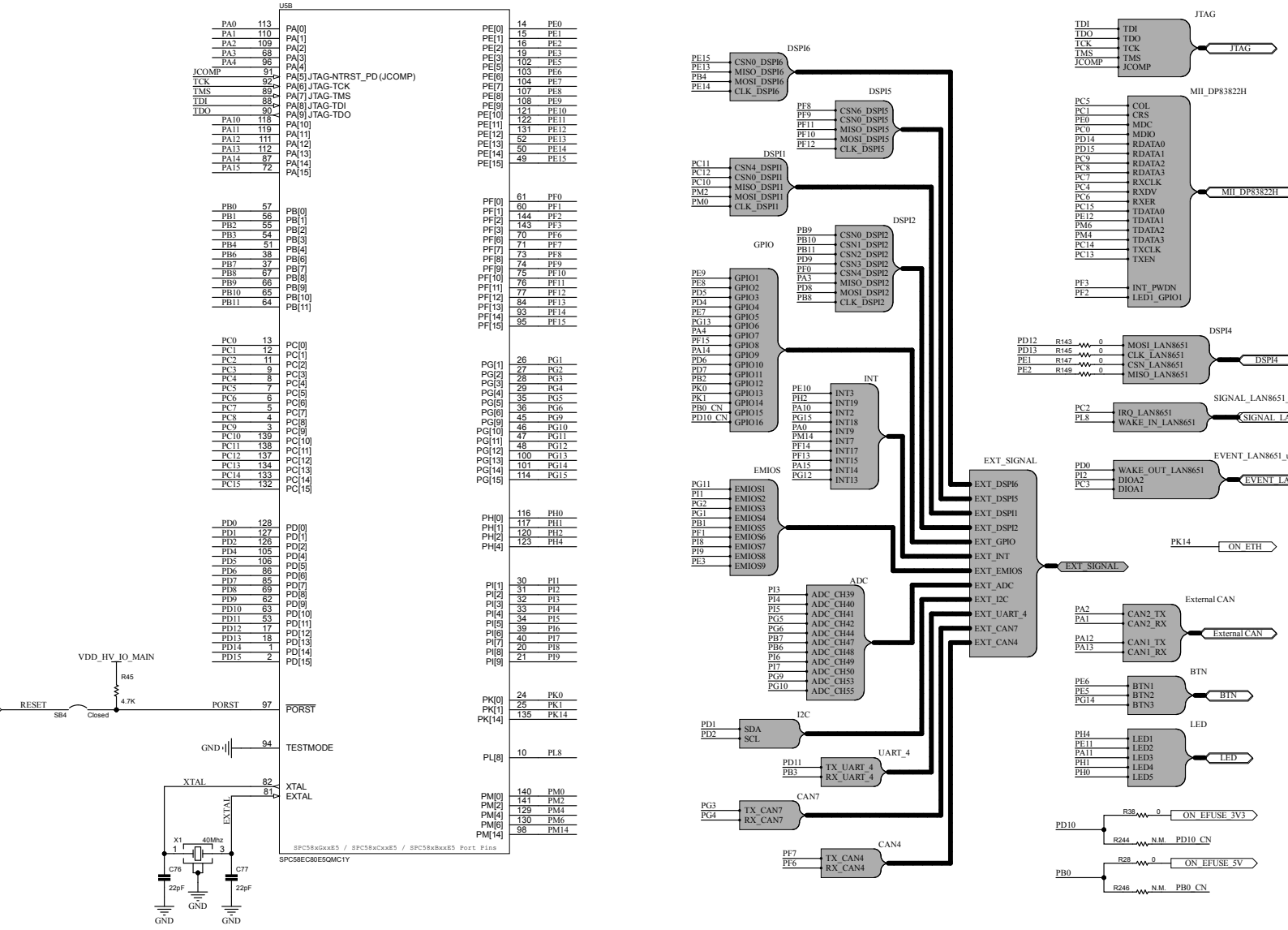


Figure 5. **AEK-MCU-C4MLIT3 circuit schematic (5 of 11)**

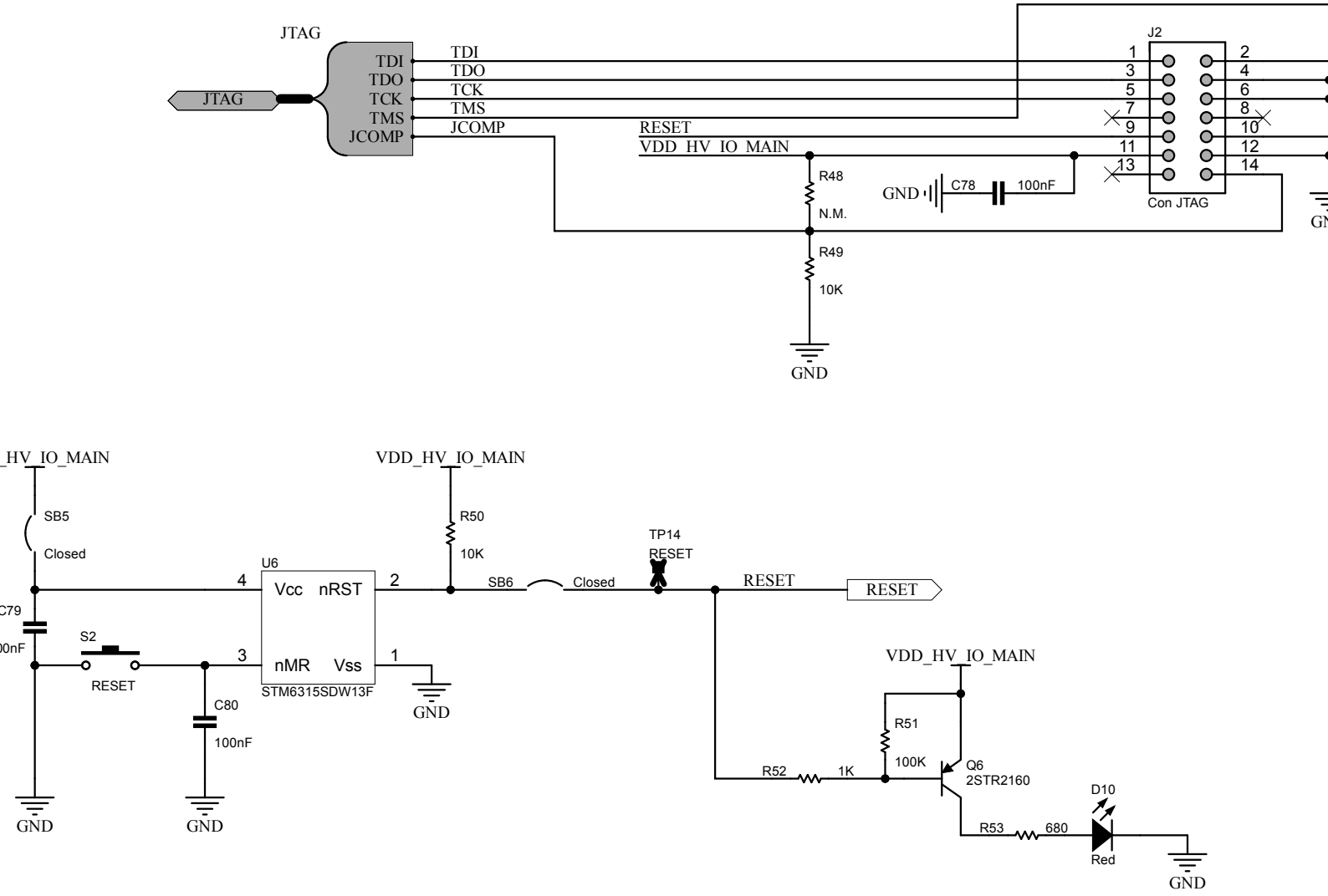


Figure 6. AEK-MCU-C4MLIT3 circuit schematic (6 of 11)

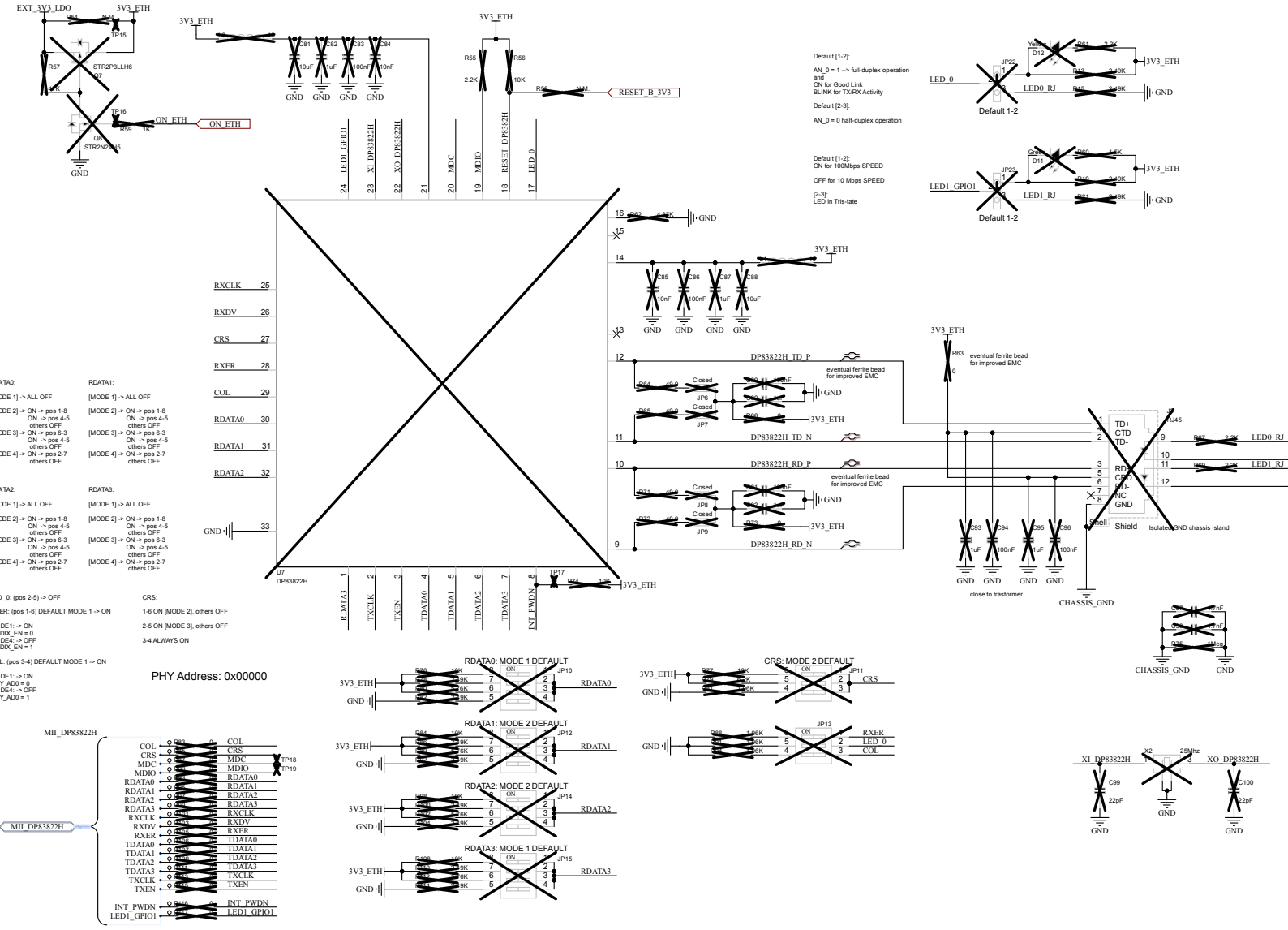


Figure 7. **AEK-MCU-C4MLIT3 circuit schematic (7 of 11)**

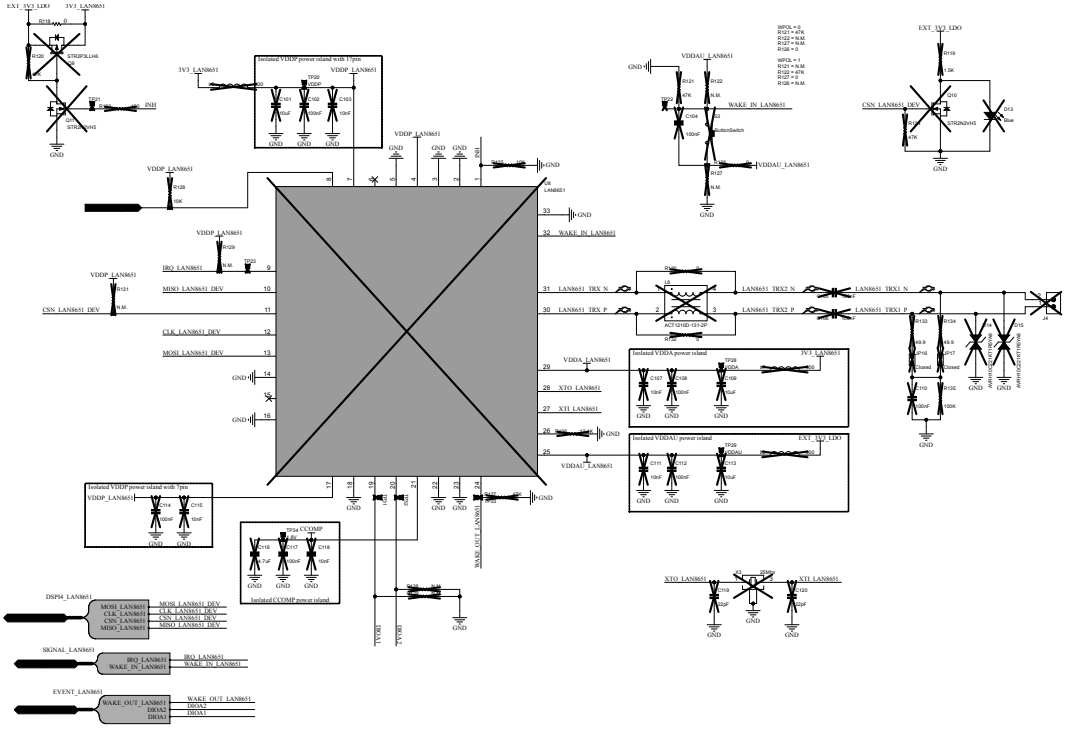


Figure 8. **AEK-MCU-C4MLIT3 circuit schematic (8 of 11)**

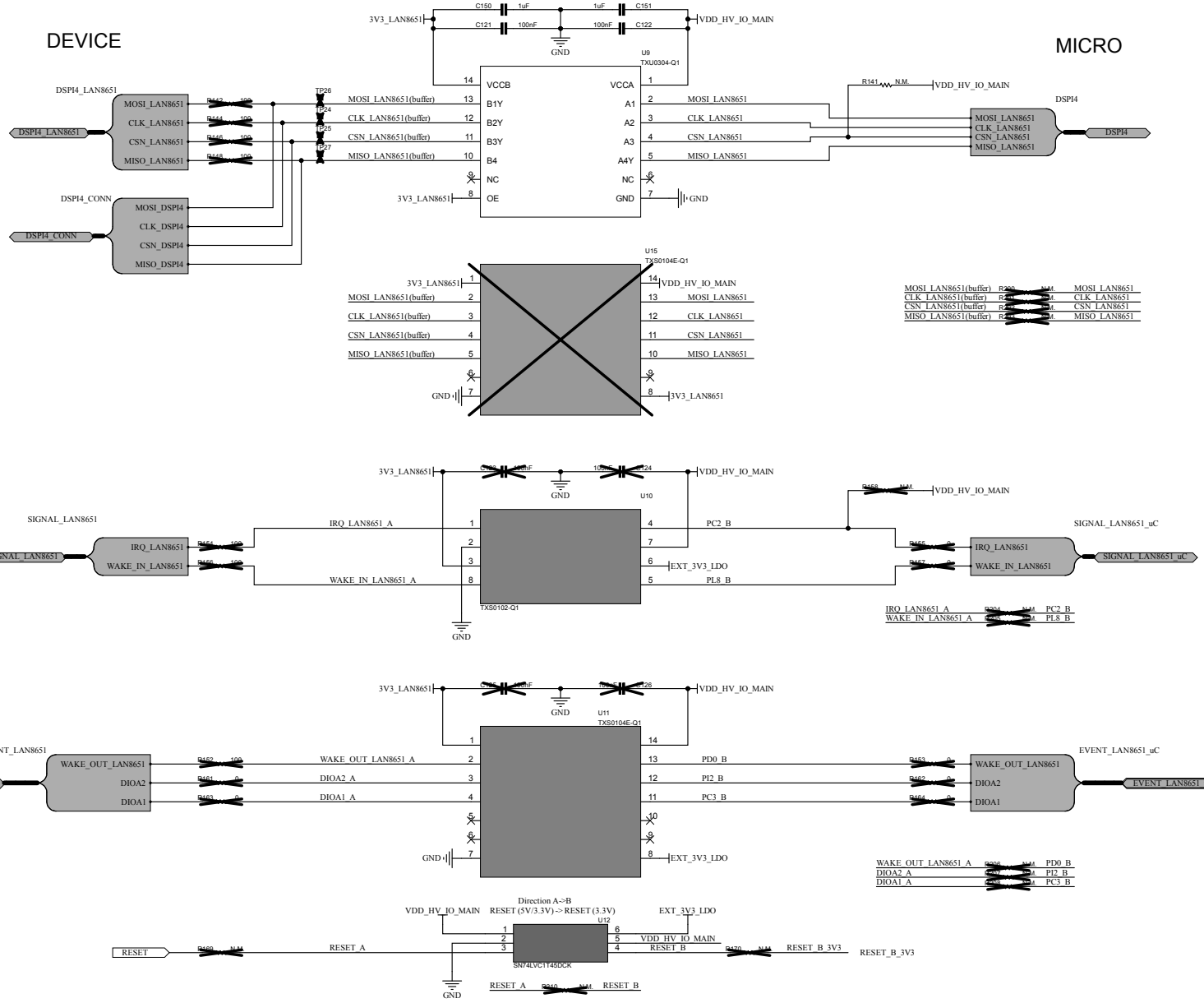


Figure 9. **AEK-MCU-C4MLIT3 circuit schematic (9 of 11)**

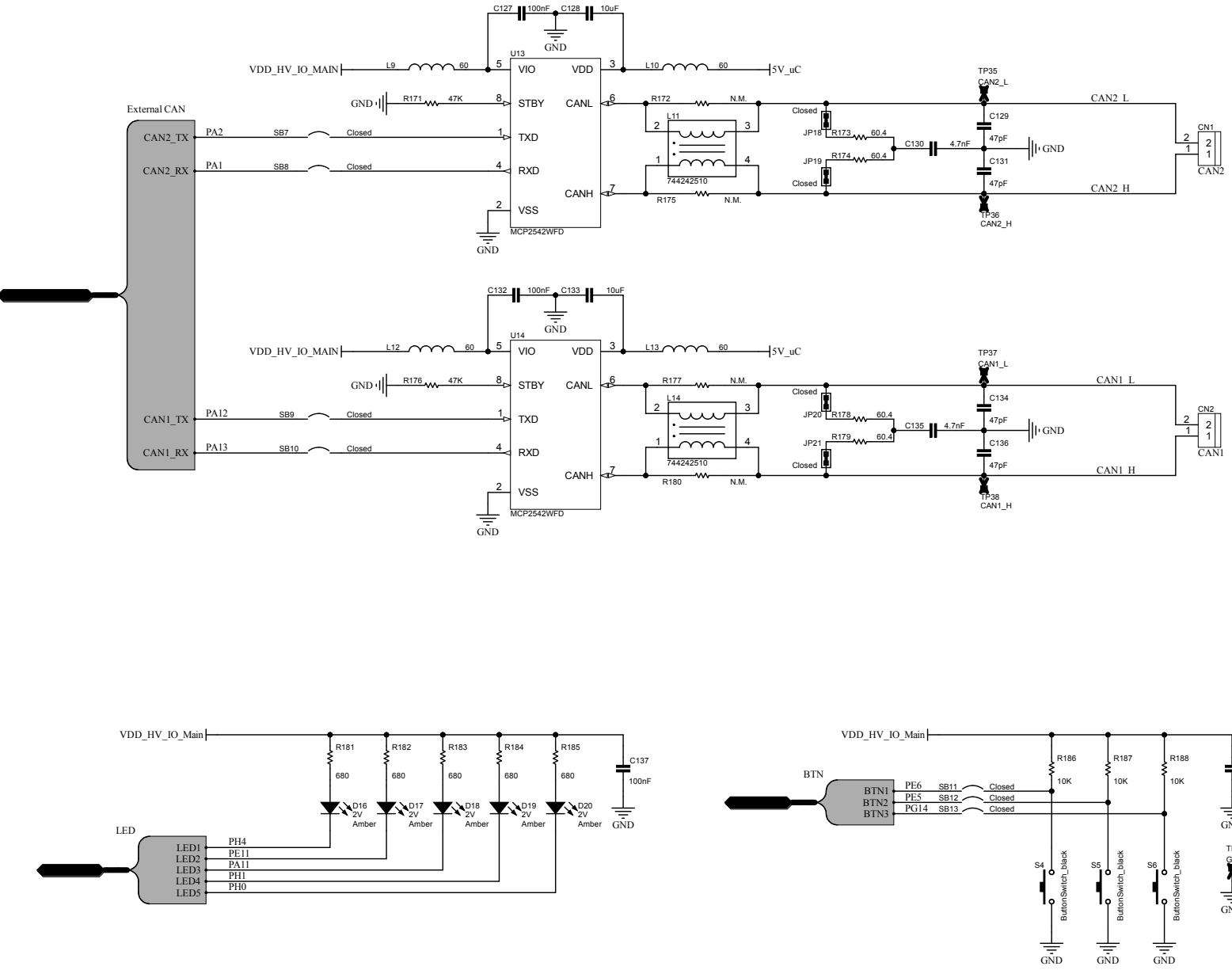


Figure 10. **AEK-MCU-C4MLIT3 circuit schematic (10 of 11)**

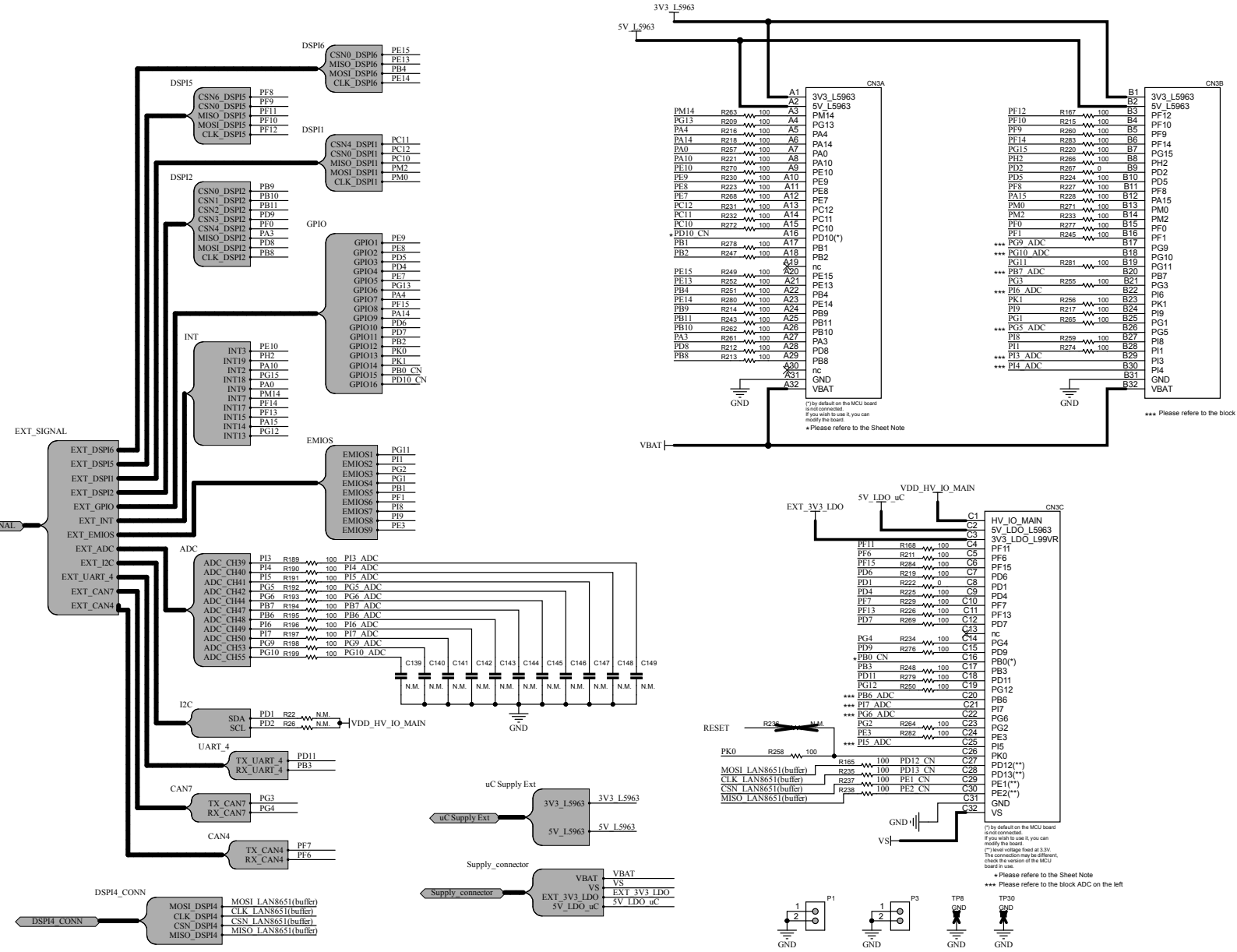
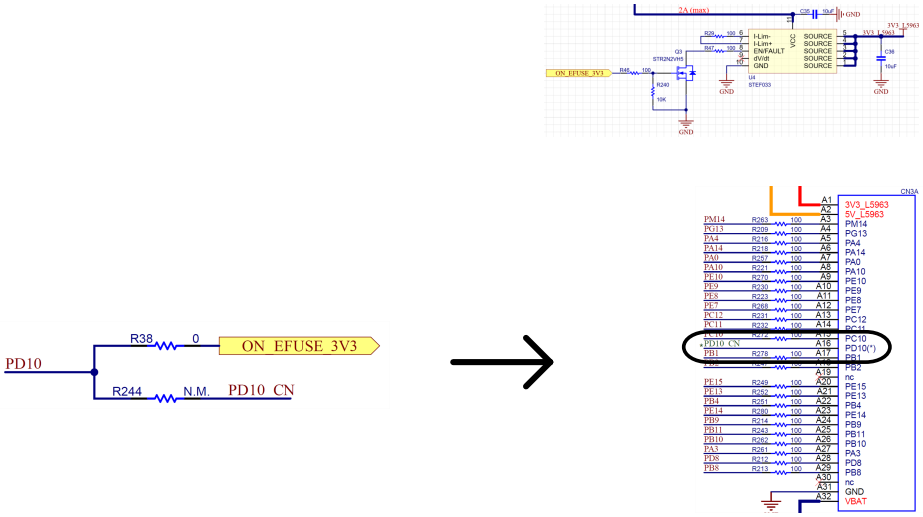
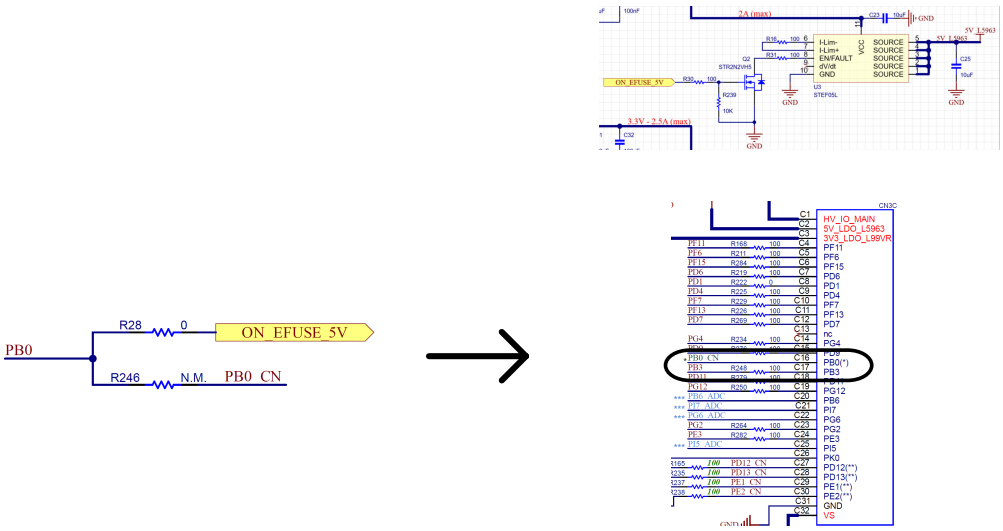


Figure 11. **AEK-MCU-C4MLIT3 circuit schematic (11 of 11)**



Pin not connected on the “96pin connector,” by default it is used to enable/disable the eFuse @3.3V (U4_STEF033).

To connect PD10 to the “96pin connector” remove R38 and mount a 0R on R244 (this way the eFuse always stays on and is no longer managed by the MCU).



Pin not connected on the “96pin connector,” by default it is used to enable/disable the eFuse @5V (U3_STEF05L).

To connect PB0 to the “96pin connector” remove R28 and mount a 0R on R246 (this way the eFuse always stays on and is no longer managed by the MCU)

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