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# DAS_HWB_EVB_ELE6A_C4_SCHEMATICS

**Schematics**

# Based on STA8610XA

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SHEET NAME


| | |
|---|---|
| PAGE 01 : Cover | REF DES: 0100 - 0199 |
| PAGE 02 : Bloc diagram | REF DES: 0200 - 0299 |
| PAGE 03 : UART/LED | REF DES: 0300 - 0399 |
| PAGE 04 : Front end | REF DES: 0400 - 0499 |
| PAGE 05 : JTAG/ANT SENS | REF DES: 0500 - 0599 |
| PAGE 06 : Power & Miscelianous | REF DES: 0600 - 0699 |
| PAGE 07 : Module | REF DES: 0700 - 0799 |
| PAGE 08 : Data log/MEMS | REF DES: 0800 - 0899 |
| PAGE 09 : MODFIED | REF DES: 0900 - 0999 |


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Board Title: DAS_HWB_EVB_ELE6A_C4_SCHEMATICS

Sheet Title: PAGE 01: Cover

| Doc No | Date | Rev | Sheet |
|---|---|---|---|
| 856387 | Thursday, February 19, 2026 | B1 | 1 / 9 |

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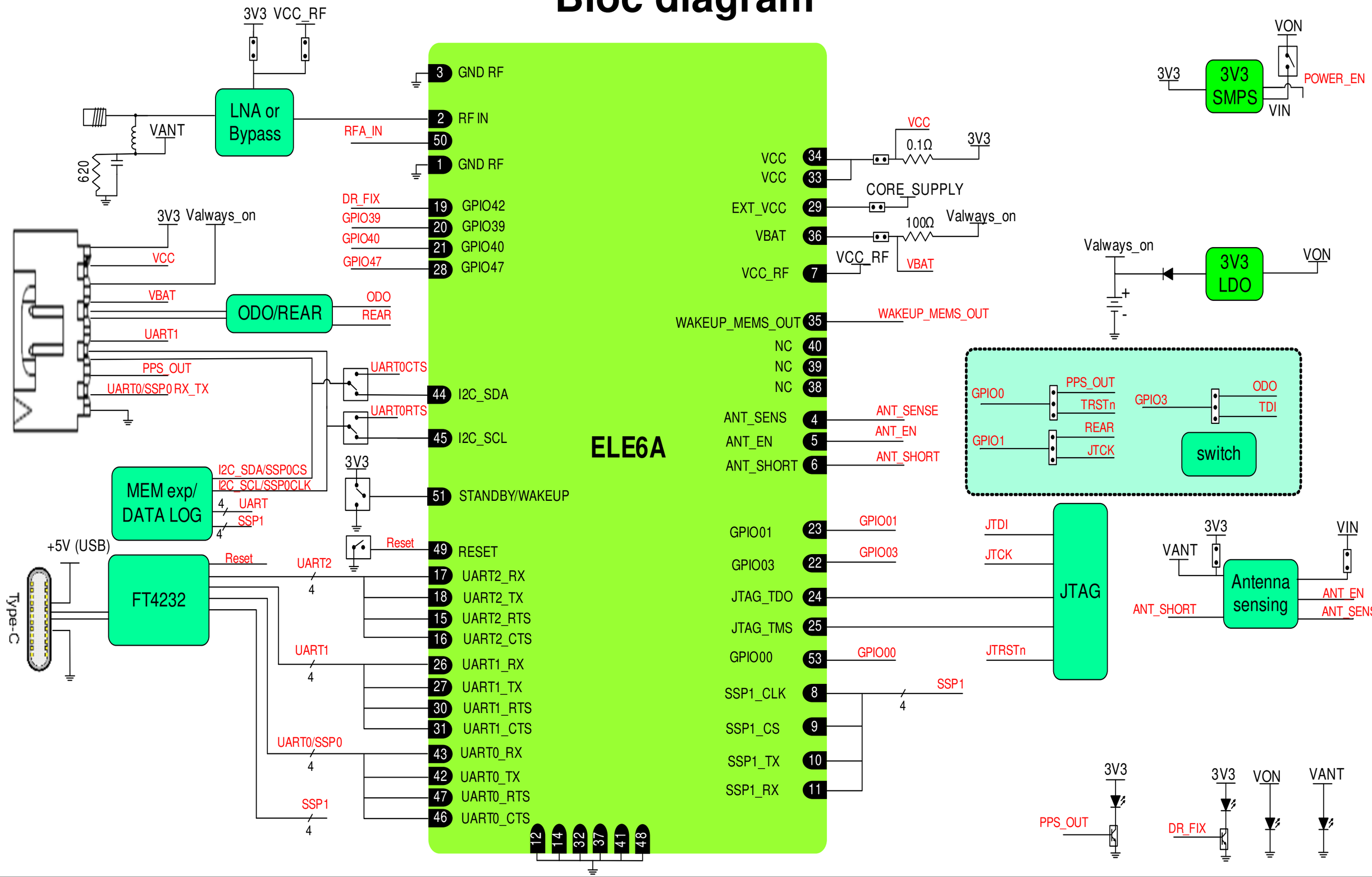
# Bloc diagram

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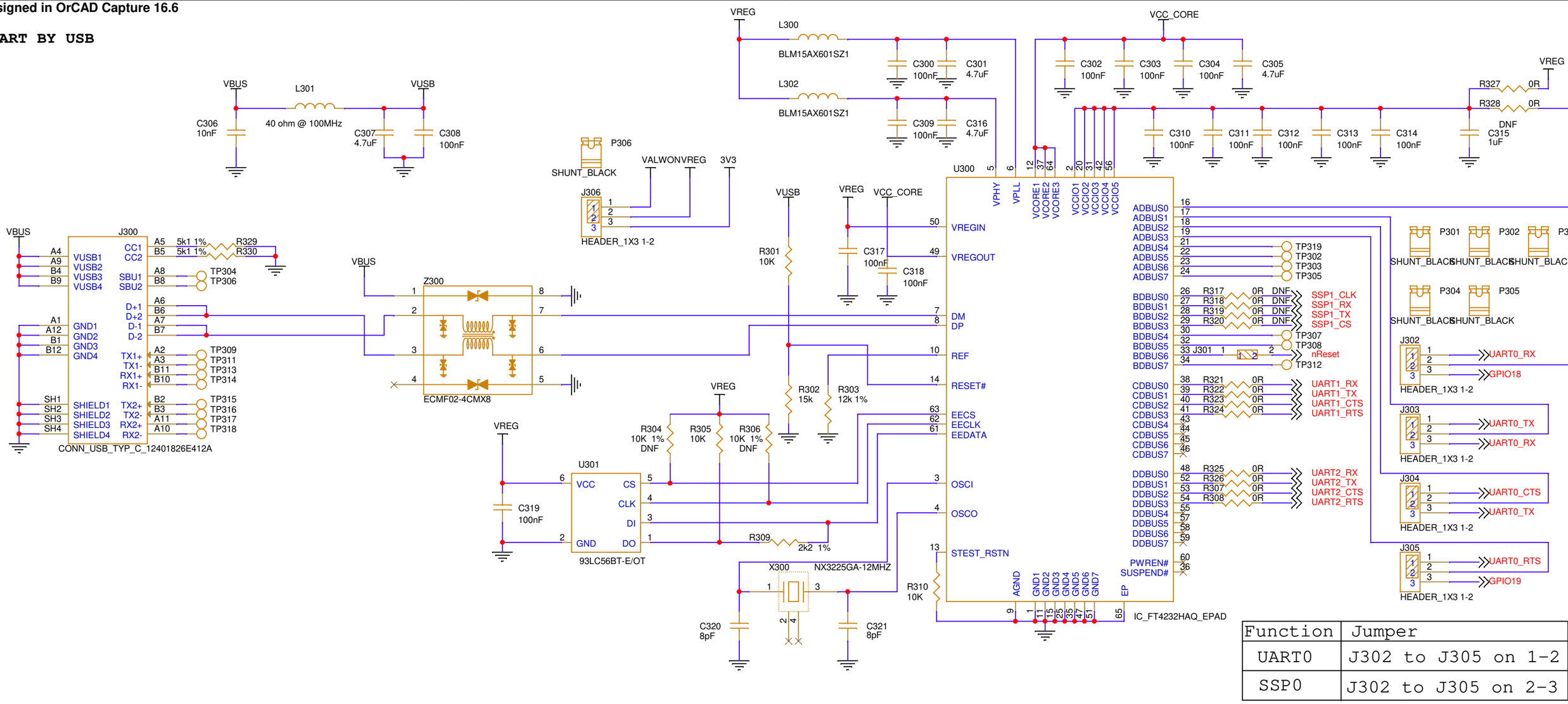
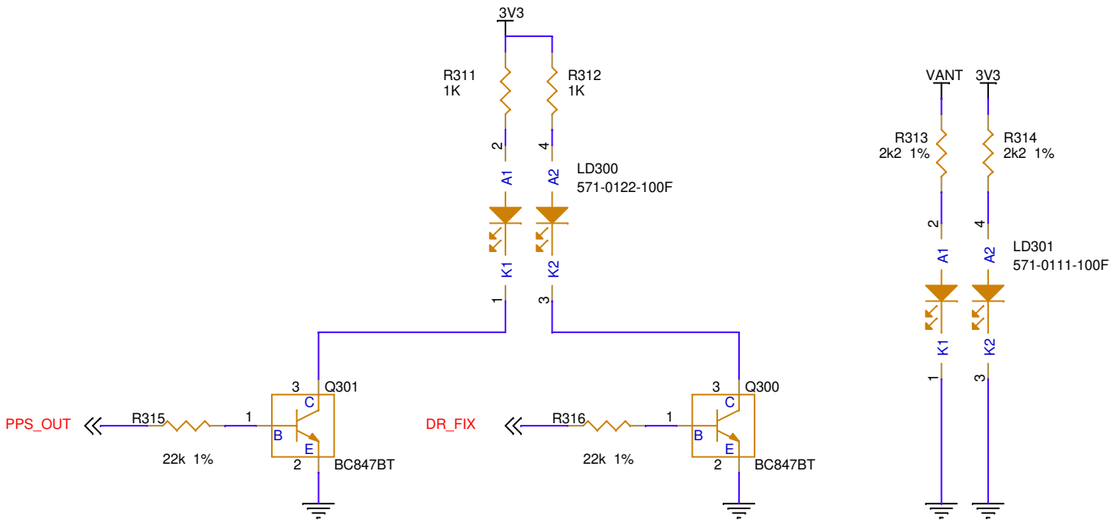
# UART BY USB




| Function | Jumper |
|---|---|
| UART0 | J302 to J305 on 1-2 |
| SSP0 | J302 to J305 on 2-3 |

## LEDs

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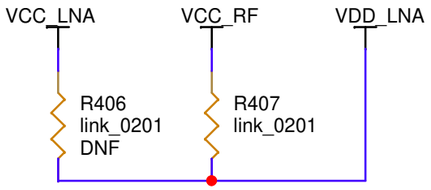
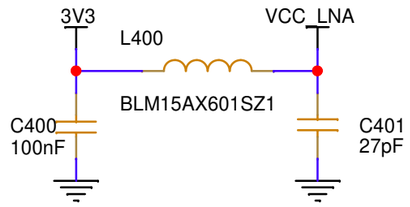
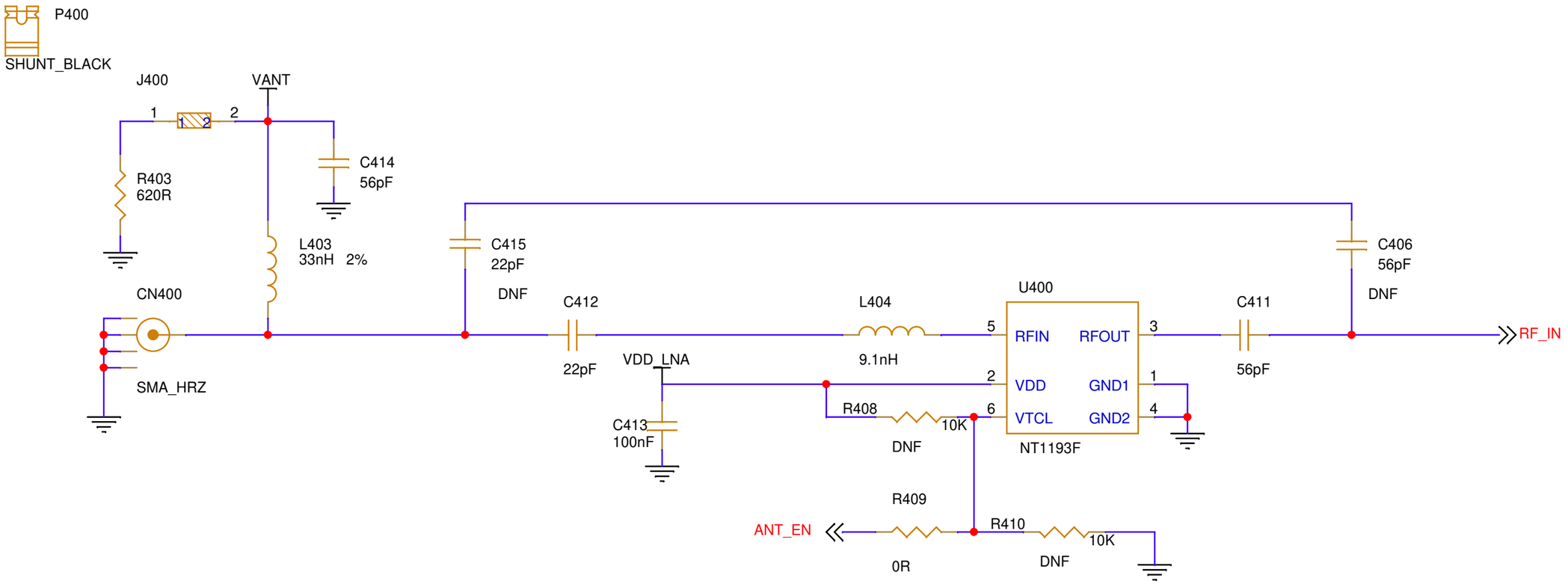



| COMP REF | CONFIG |
|---|---|
| R406 | MOUNTED |
| R407 | DNF |

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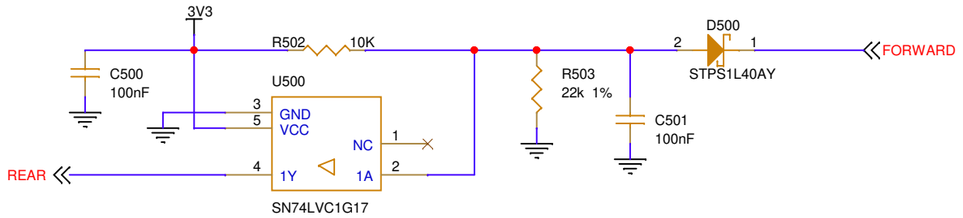
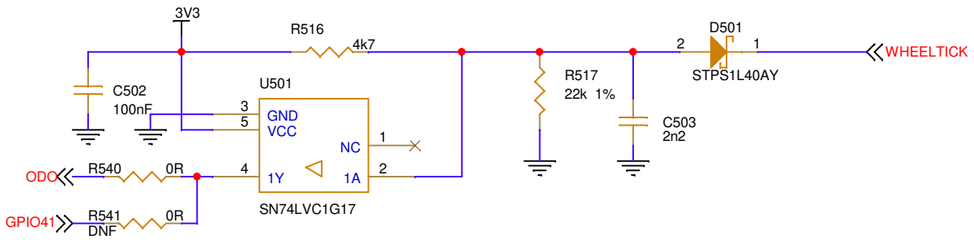
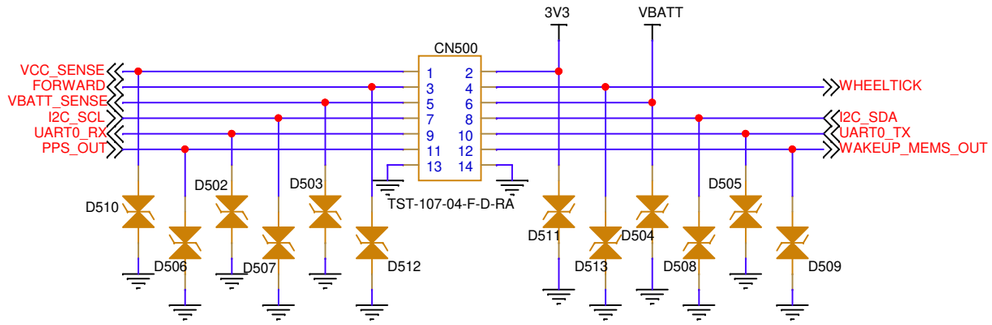
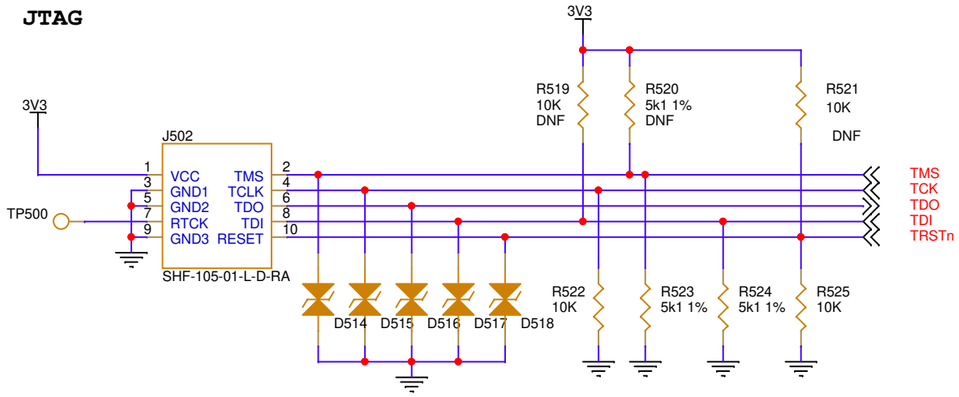
## REAR & ODOmeter



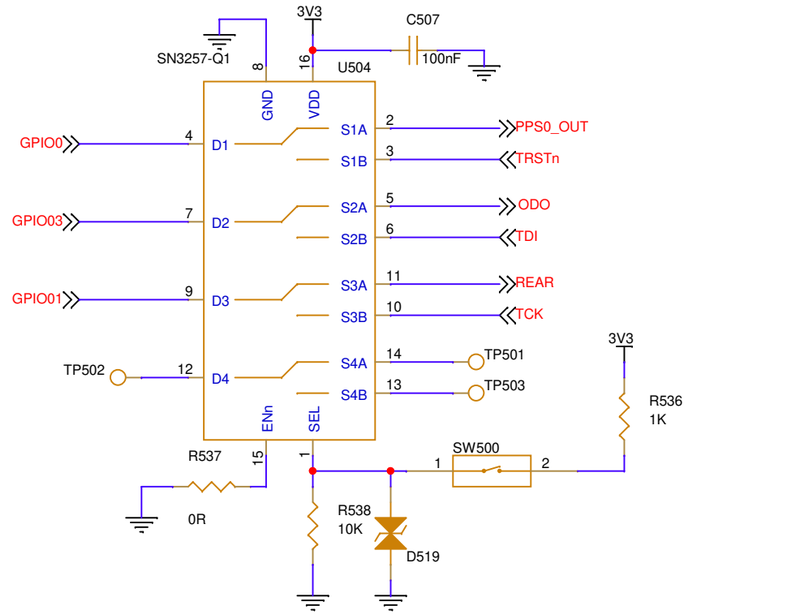



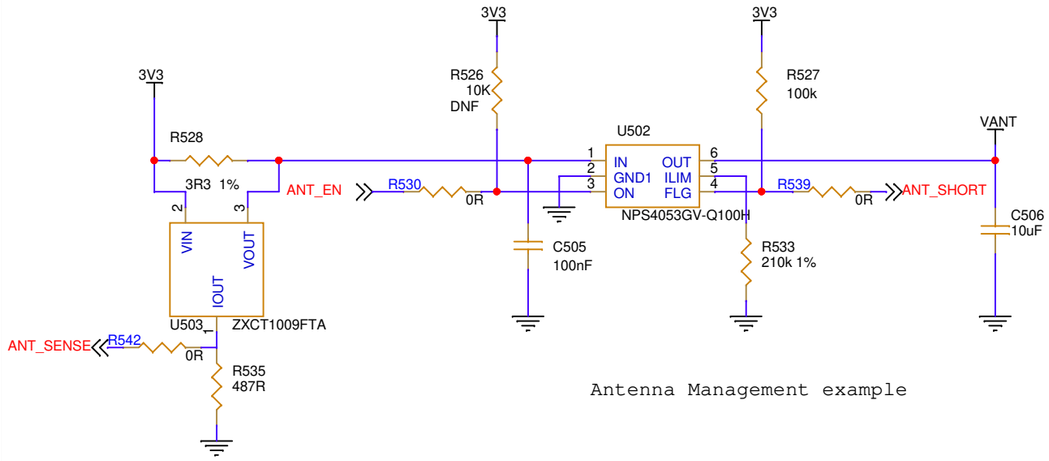

## JTAG




for 2 wire JTAG no PU on TRSTN




## Antenna sensing




Antenna Management example

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# POWER

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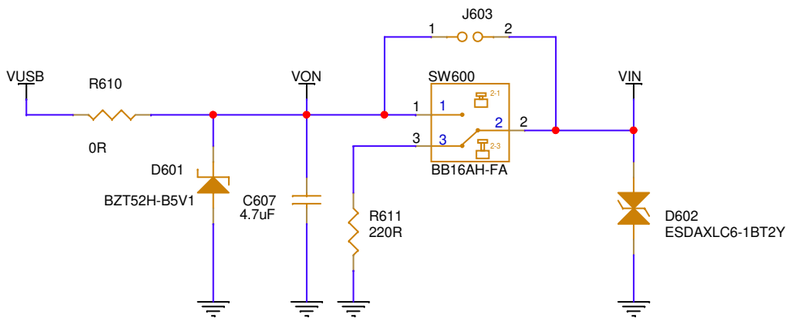
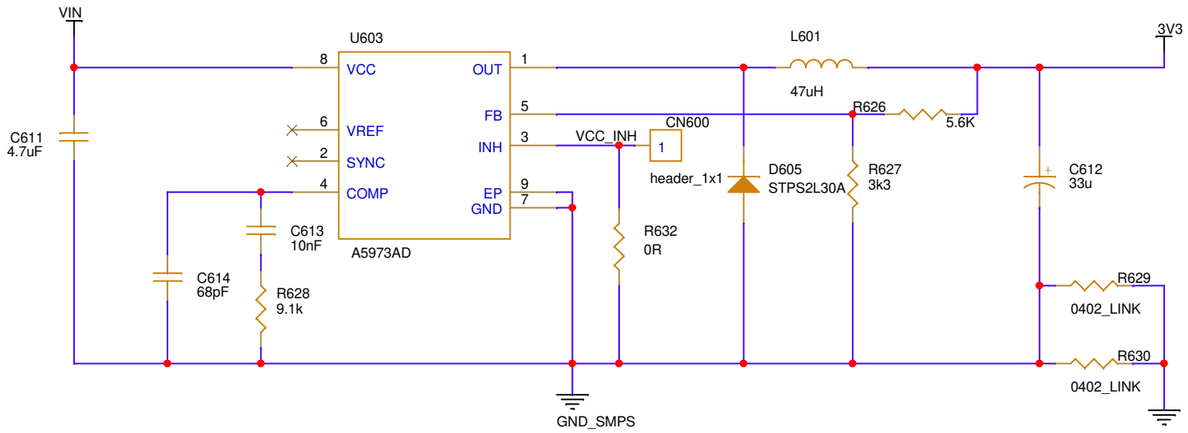
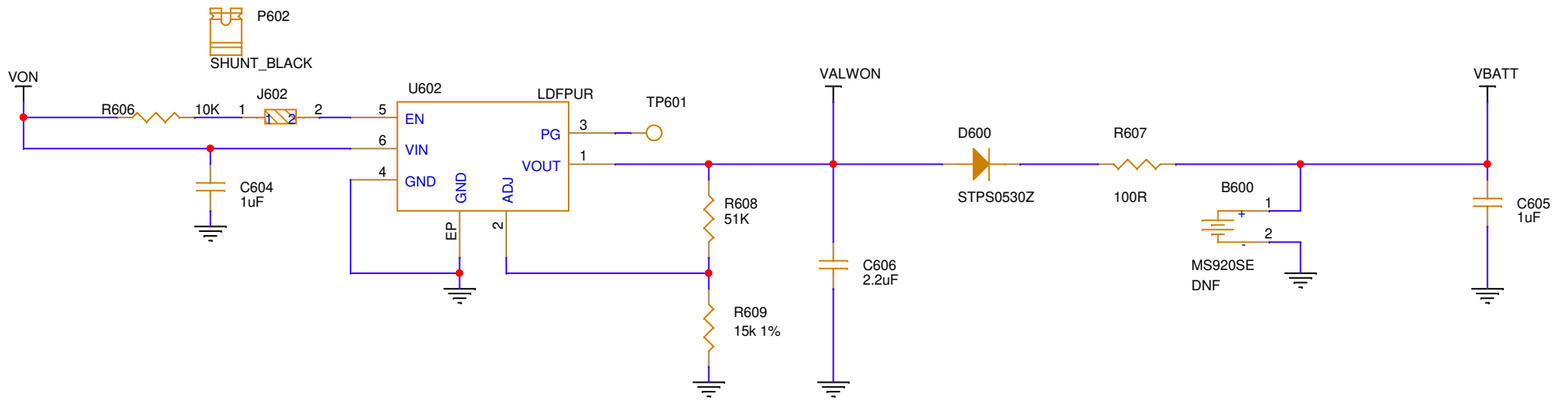
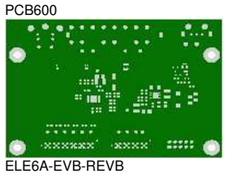



PCB600



ELE6A-EVB-REVB

HW600

XXXXY-AABBCC
12345678-123

HW_LABEL_SERIAL_NUMBER

| | | | |
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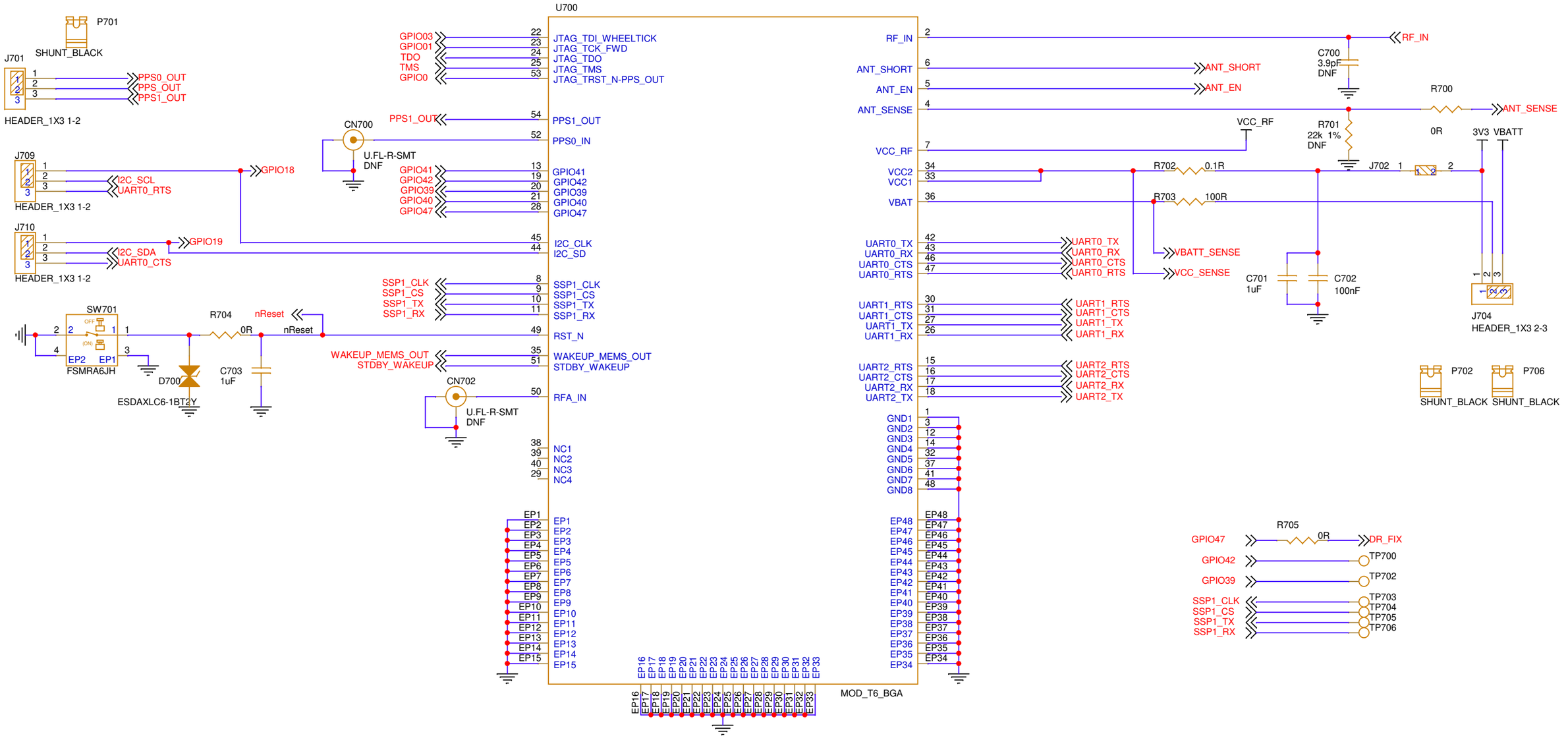





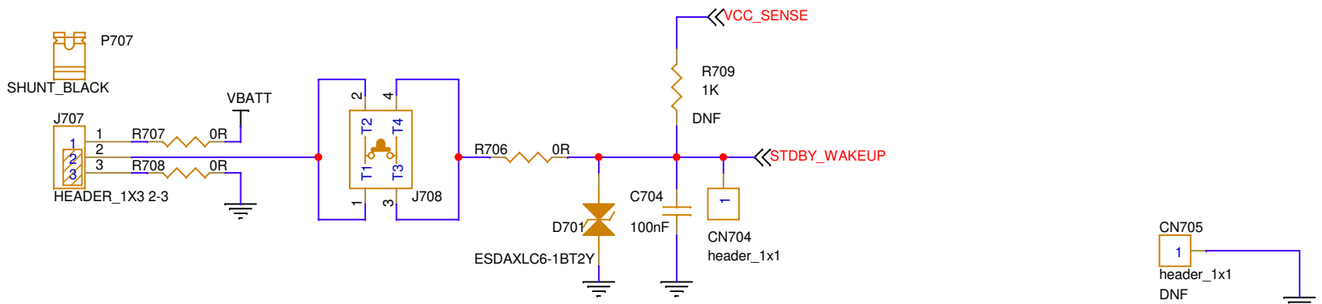


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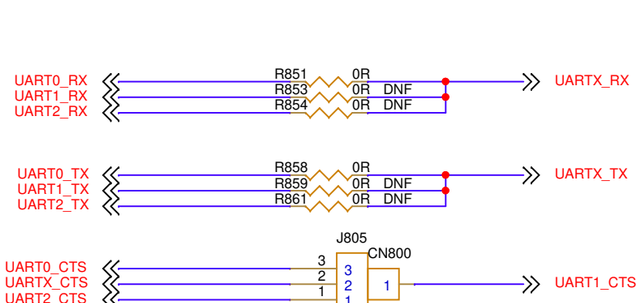


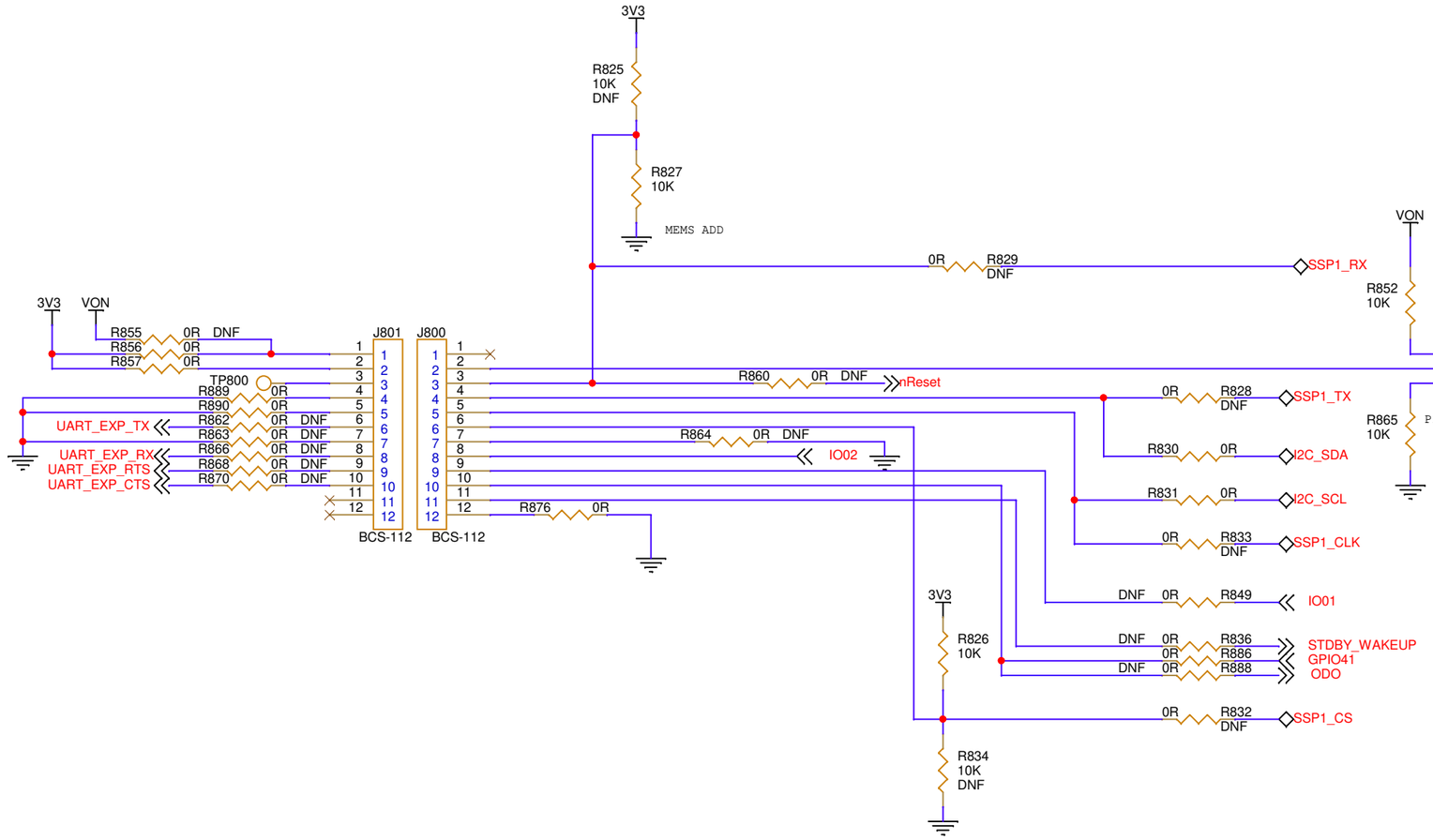


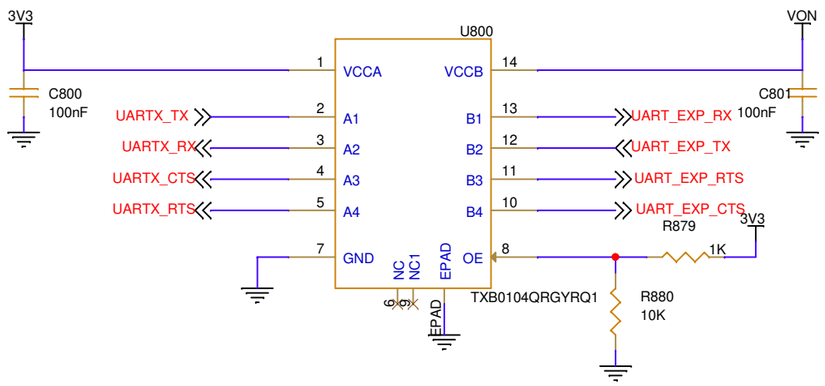


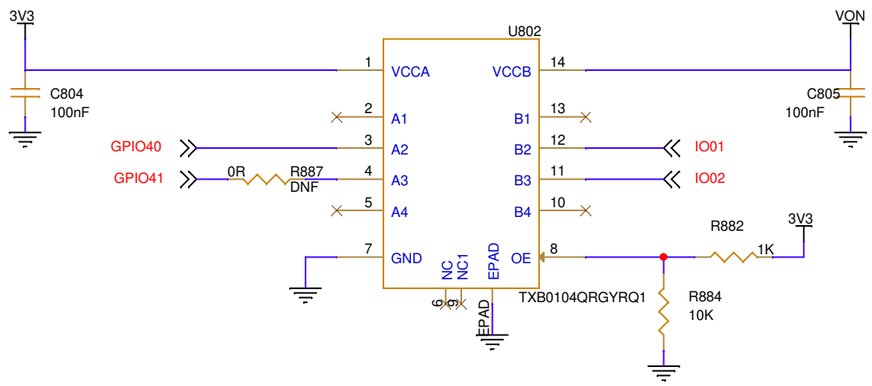


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ELE6A-EVB REV A1:

- Mount resistor for SSP test R872 and R873
- Update output 3,3V R626 3.3k and R627 5.6k
- Update output VBATT R608 51k and R609 15K
- Reset filtering issue change R704 by 0R
- USB issue change R329 and R330 by 0R

ELE6A-EVB REV B0:

- Remove LNA by pass ticket 161596
- Remove discret LNA ticket 160931
- Remove option smps for core supply ticket 161590
- JTAG issue correction ticket 161447
- USB connector assembly issue ticket 160882
- Connect INT2 of expansion (sensors in i2c) ticket 161307
- Disconnect INT 1 of MEMS expention ticket 160660
- Improve test coverage at Production EMS (For ex. RTS/CTS) ticket 160933
- Antenna current monitor update ticket 160928
- Update schematic to support binary download on SSP interface via FTDI ticket 160939
- Change module for REVC
- Add possibility to connecte VCC to Standby_in
- Connect PIN 4 and 5 of J801 to GND

ELE6A-EVB REV C1:

-LNA Bypass align with EVB T5
-Change PU by PD on EN of LNA fro current saving

ELE6A-EVB REV C3:

-Remove PU/PD on ANT_EN
-Change LNA power by VCC_RF

ELE6A-EVB REV C4:

-Update bloc diagram

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Sheet Title PAGE 09 : MODIFIED

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