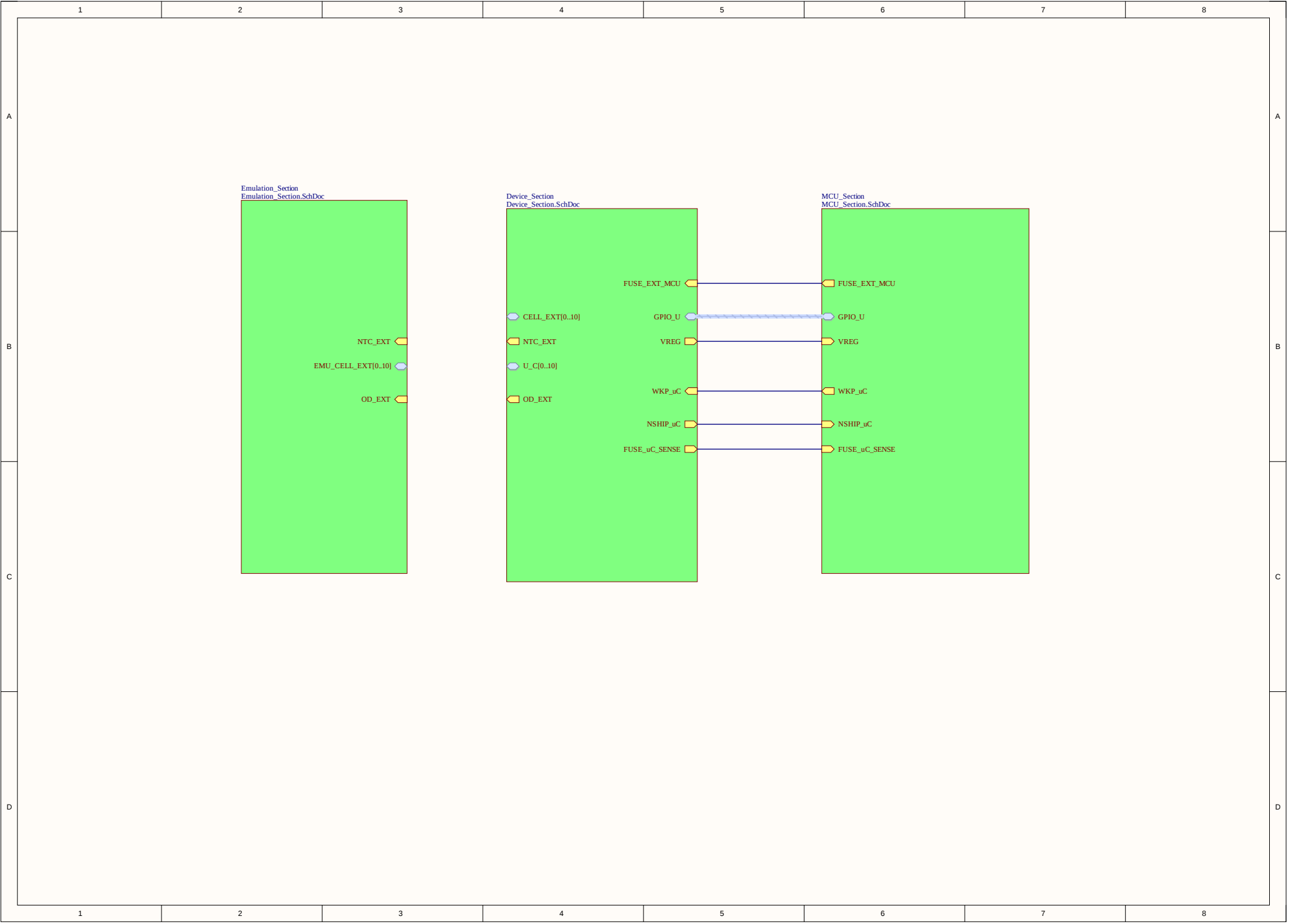
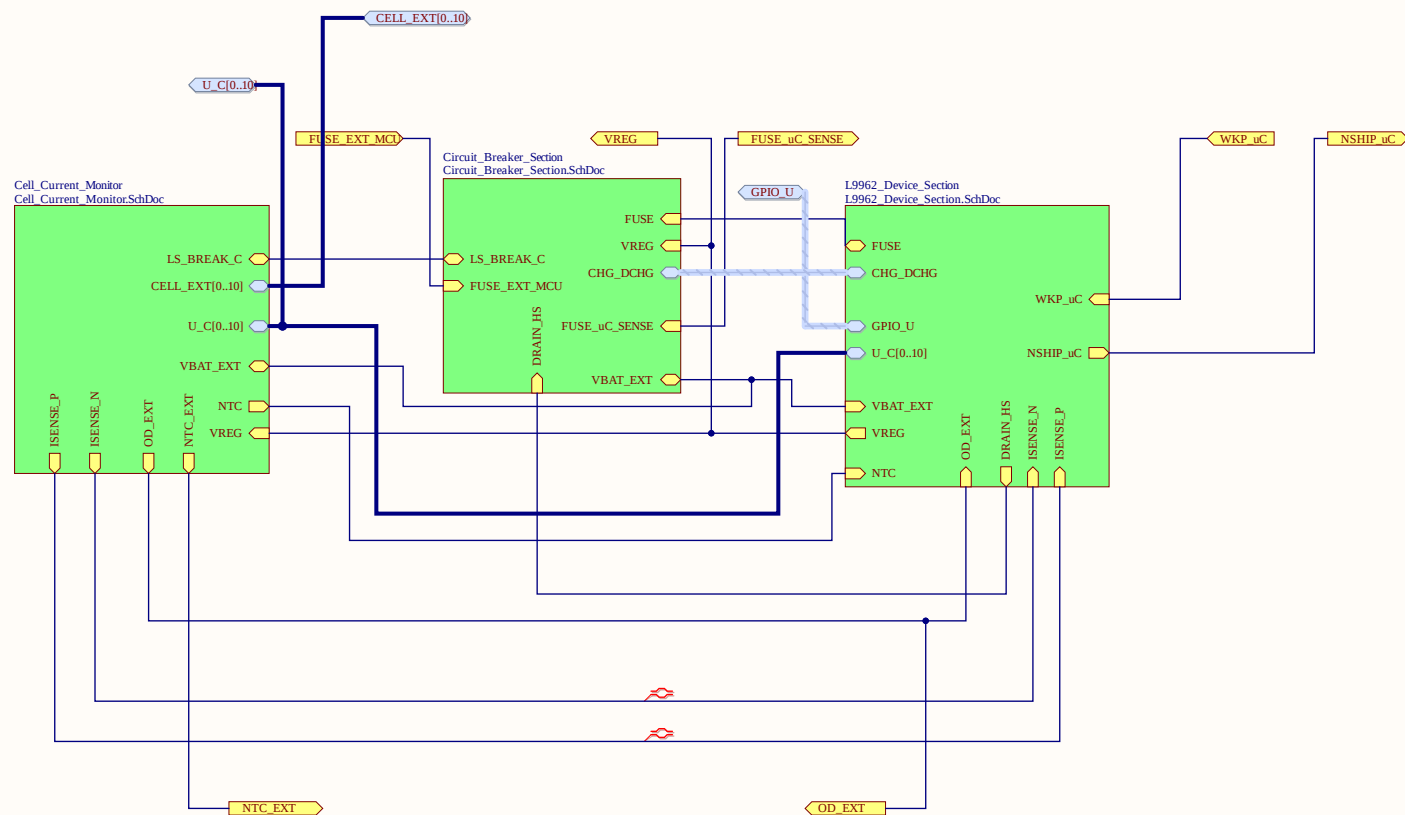






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Title: **Evaluation Board L9962**

Part number: **EVL9962**

Date: **07/23/26**

Document name: **A3-ST-APMS.SchDoc**

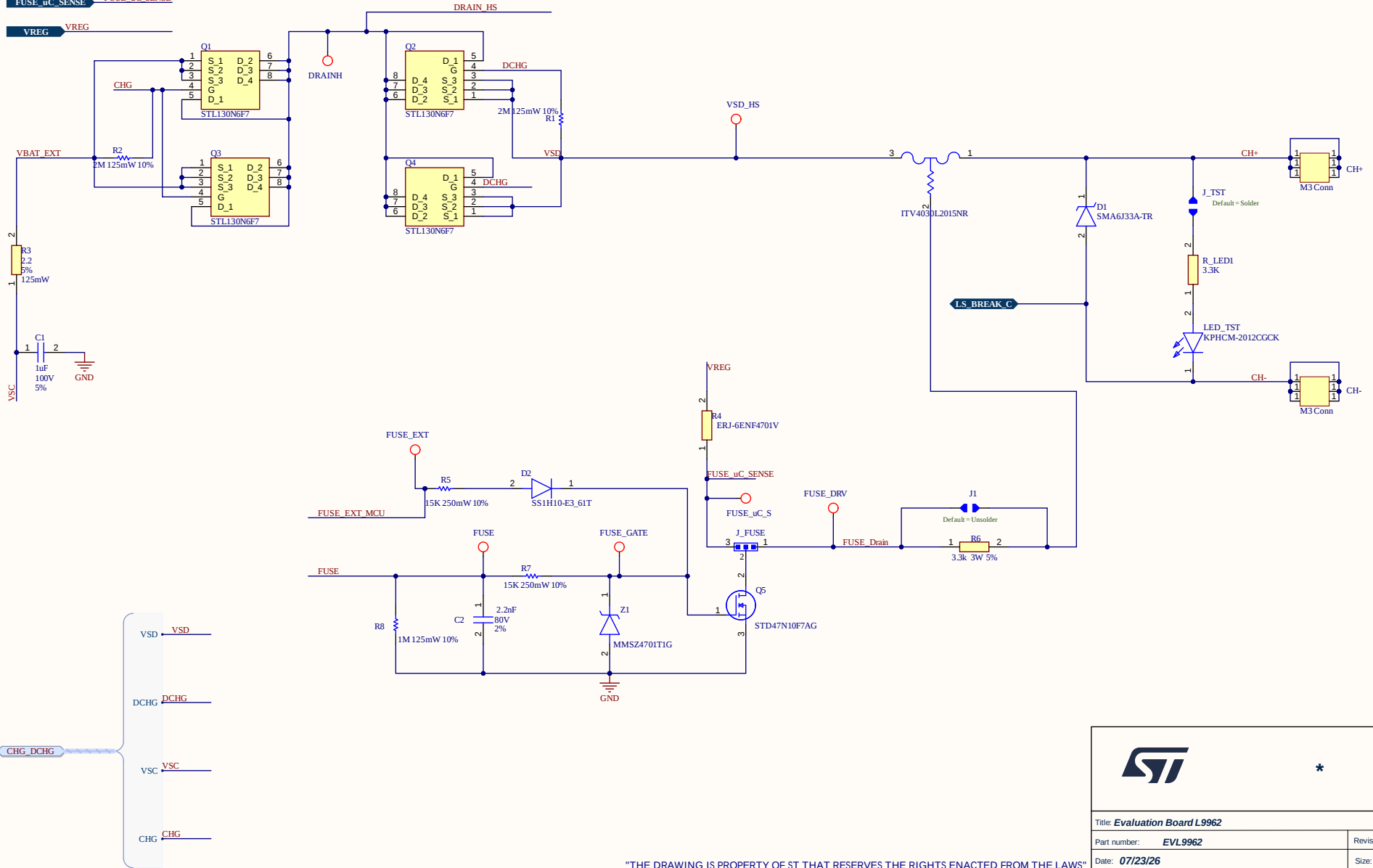
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Size: **A3**

Sheet 2 of 7

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 "STMicroelectronics assumes no responsibility for the consequences of the use of this board"

VBAT_EXT VBAT_EXT
DRAIN_HS DRAIN_HS
FUSE_EXT_MCU FUSE_EXT_MCU
FUSE FUSE
FUSE_uC_SENSE FUSE_uC_SENSE
VREG VREG



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Title: **Evaluation Board L9962**

Part number: **EVL9962**

Date: **07/23/26**

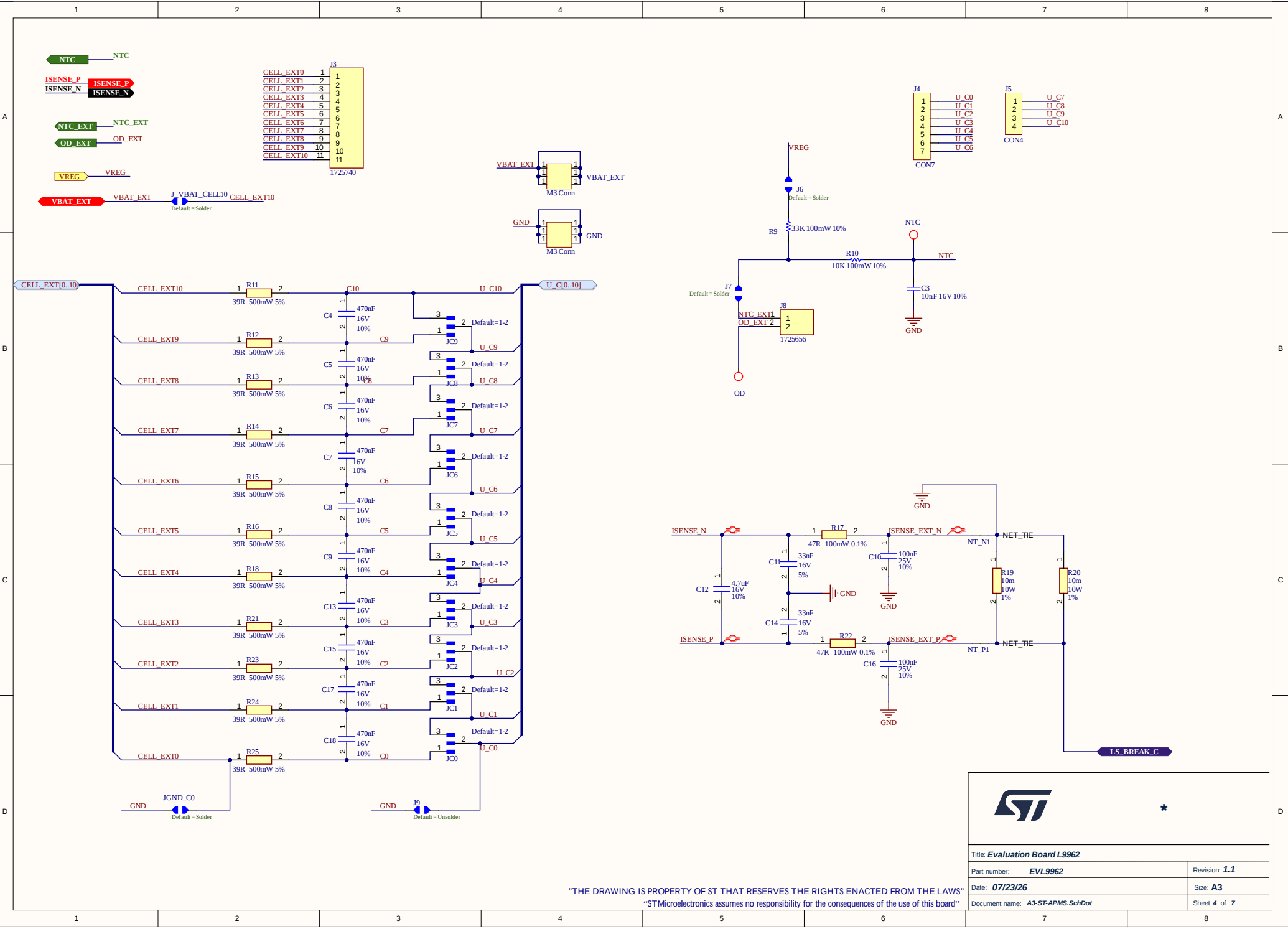
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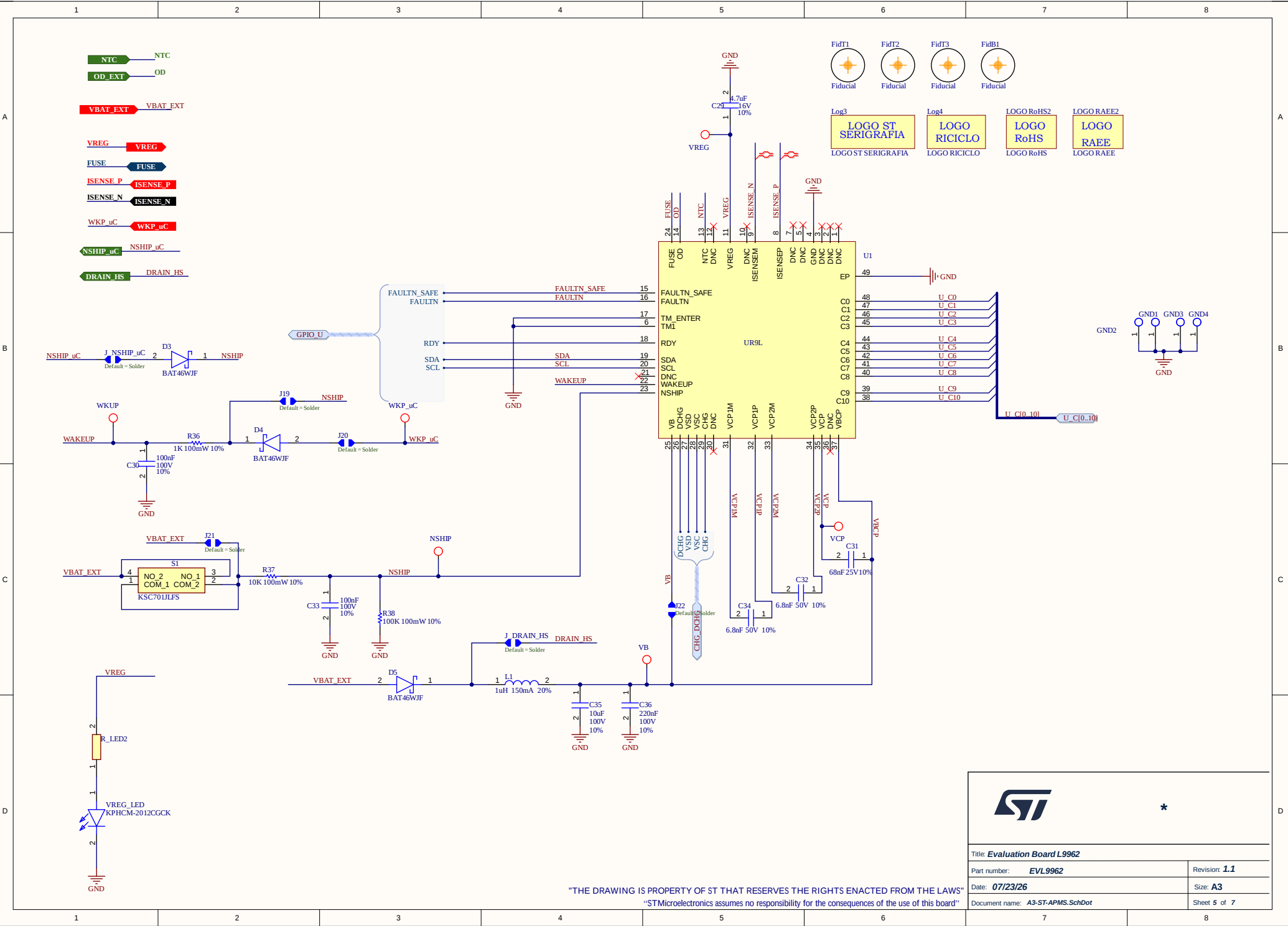
Revisior: **1.1**

Size: **A3**

Sheet **3** of **7**

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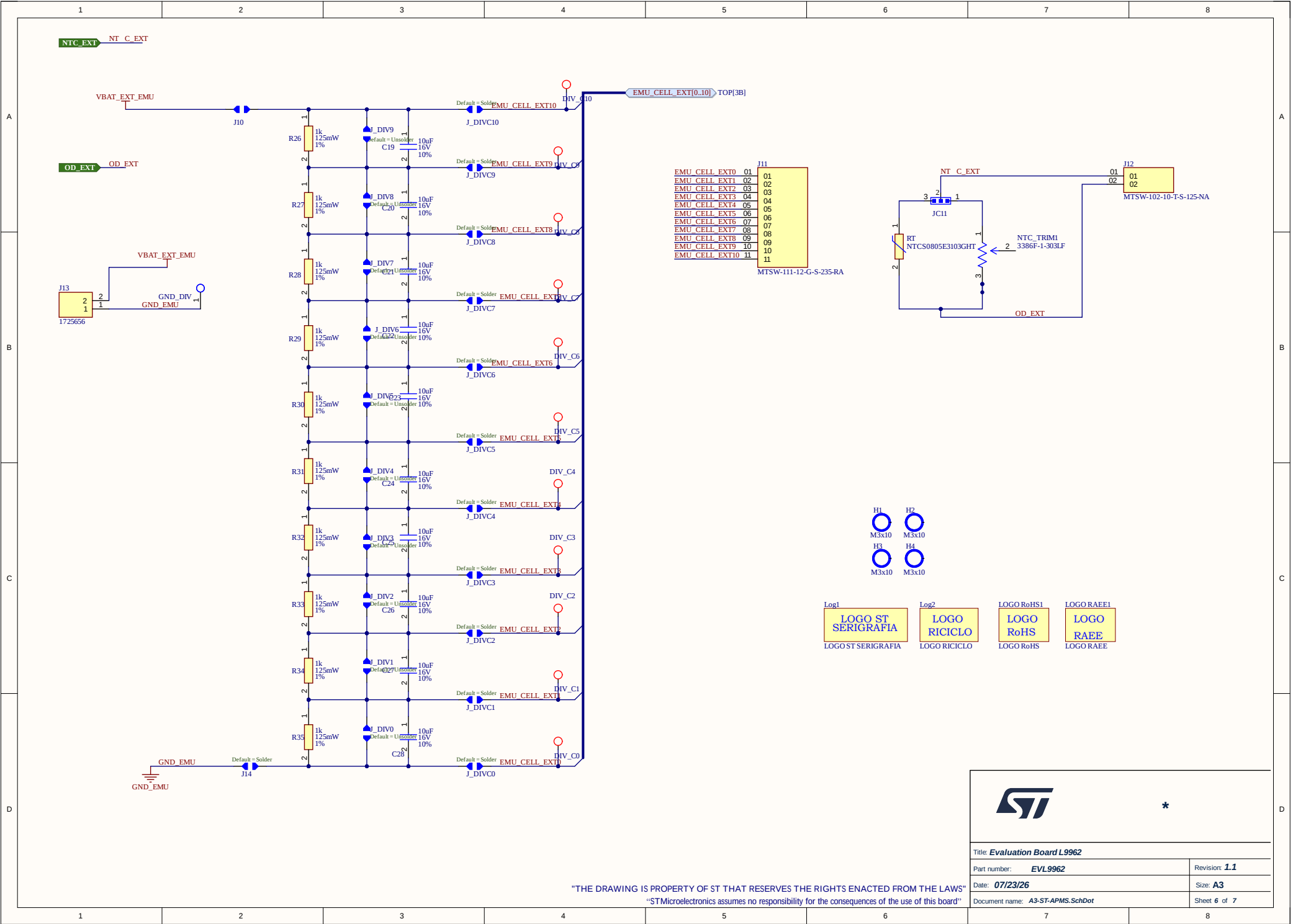


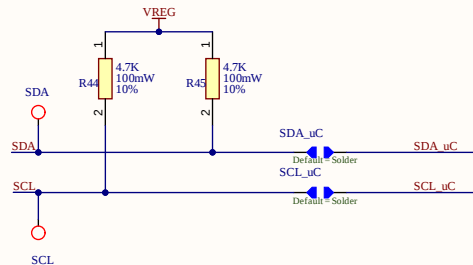
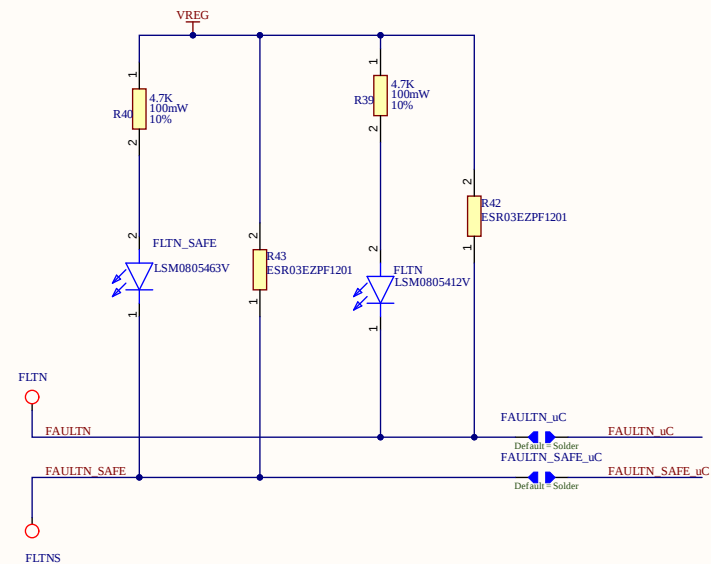
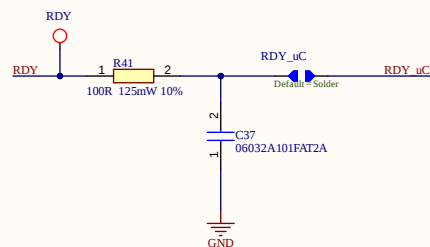
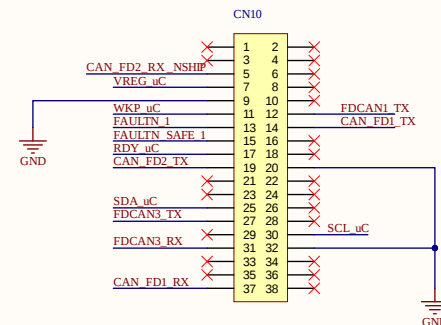


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Title: Evaluation Board L9962	
Part number: EVL9962	Revision: 1.1
Date: 07/23/26	Size: A3
Document name: A3-ST-APMS.SchDot	Sheet 5 of 7

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Title: **Evaluation Board L9962**

Part number: **EVL9962**Revision: **1.1**

Date: 07/23/26

Size: **A3**

Document name: A3-ST-APMS.SchDot

Sheet 7 of 7

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