

Schematic diagrams

The image displays three circuit diagrams, each representing a buck converter stage (IC1A, IC1B, and IC1C) with its associated components and PCB layout. Each stage includes an input capacitor, a buck converter IC, an inductor, and four output capacitors, all connected to a common output line that branches to individual outputs (VOUT1, VOUT2, VOUT3).

IC1A (BUCK1IN): The input capacitor is connected to VIN (pin 15) and GND. The buck converter IC (BUCK1IN) has pins 16 (VLX1) and 18 (VOUT1) connected to the output line. The inductor (LX1) is connected between pins 16 and 18. The output capacitors (CVOUT1A, CVOUT1B, CVOUT1C, CVOUT1D) are connected to the output line and GND. The output line is connected to VOUT1.

IC1B (BUCK2IN): The input capacitor is connected to VIN (pin 4) and GND. The buck converter IC (BUCK2IN) has pins 5 (VLX2) and 7 (VOUT2) connected to the output line. The inductor (LX2) is connected between pins 5 and 7. The output capacitors (CVOUT2A, CVOUT2B, CVOUT2C, CVOUT2D) are connected to the output line and GND. The output line is connected to VOUT2.

IC1C (BUCK3IN): The input capacitor is connected to VIN (pin 14) and GND. The buck converter IC (BUCK3IN) has pins 13 (VLX3) and 11 (VOUT3) connected to the output line. The inductor (LX3) is connected between pins 13 and 11. The output capacitors (CVOUT3A, CVOUT3B, CVOUT3C, CVOUT3D) are connected to the output line and GND. The output line is connected to VOUT3.

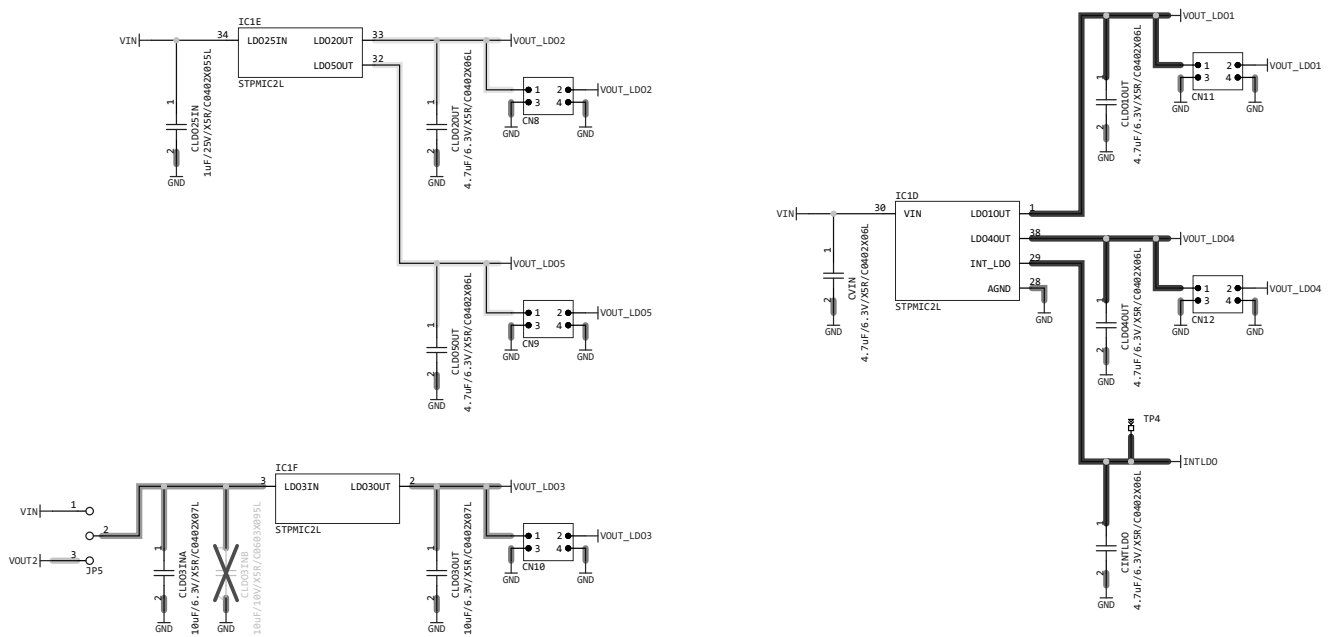
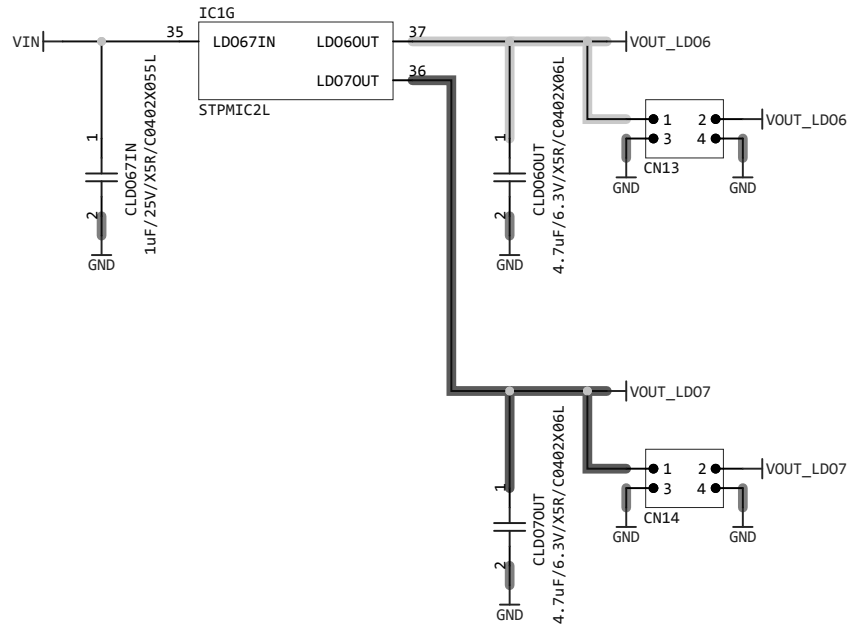
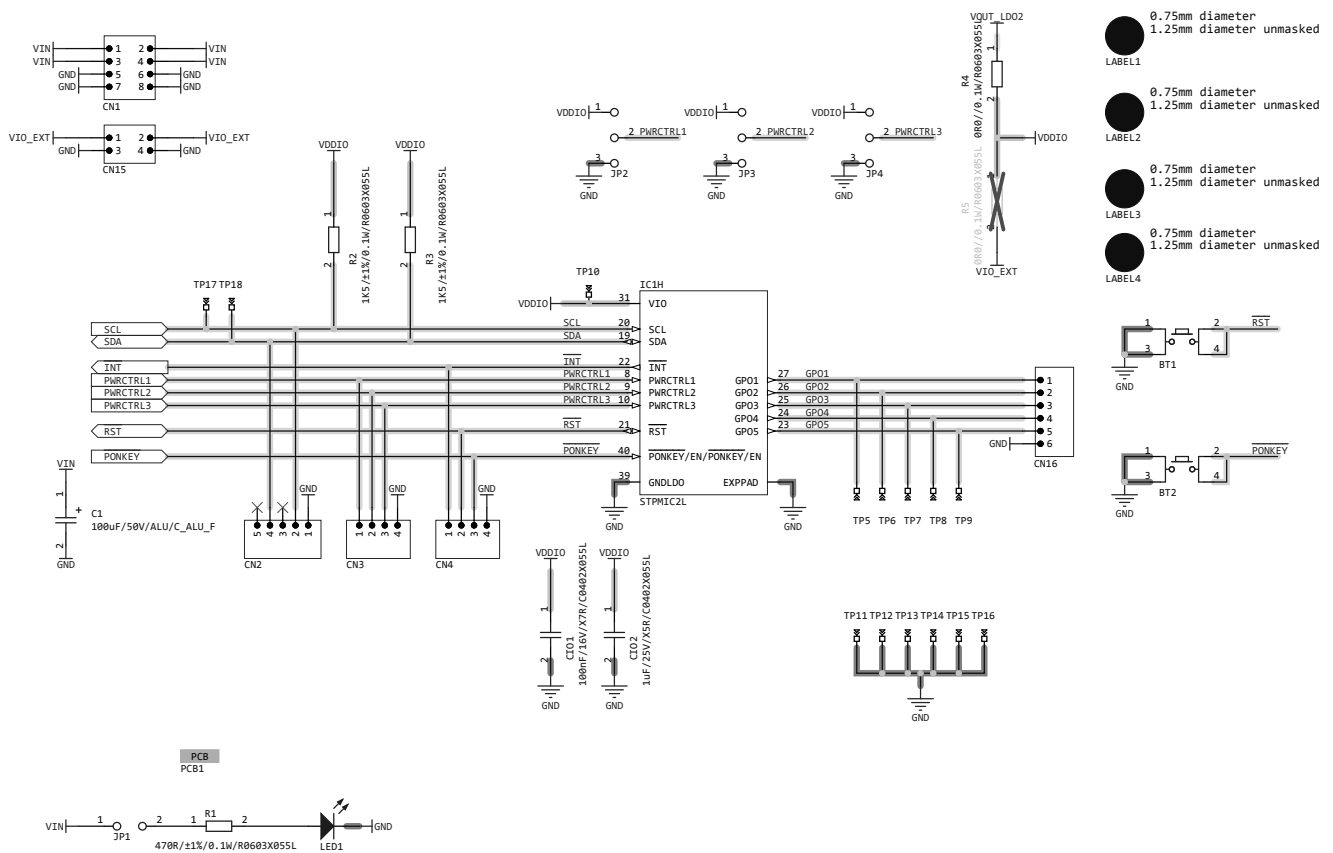
Figure 2. STEVAL-PMIC2LM1A circuit schematic (2 of 4)

Figure 3. STEVAL-PMIC2LM1A circuit schematic (3 of 4)


Figure 4. STEVAL-PMIC2LM1A circuit schematic (4 of 4)


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