

Reference device marking schematics for STM32 microcontrollers and microprocessors

Introduction

This document provides the most probable⁽¹⁾ device marking example for every⁽²⁾ package code encountered in STM32 general-purpose microcontrollers or microprocessors.

The package code is a unique package identifier that can be found:

- in this document, between parenthesis, at the end of each section title
- in this document, as a fragment of the index located at each figure bottom-right corner
- in the product datasheet, as a fragment of the index located at the bottom-right corner of package outline figures.

(1): Some slight package marking variations may be encountered between different products using the same package, and even between two identical products (in case of multiple assembly plants for instance). Nevertheless, the pin1/ballA1 localisation instructions provided in this document apply invariably.

(2): Except wafer level chip scale packages (WLCSP).

Table 1. Applicable products

Type	Sub class
Microcontrollers	STM32 Wireless MCUs STM32 Ultra Low Power MCUs STM32 Mainstream MCUs STM32 High Performance MCUs
Microprocessors	STM32 Arm Cortex MPUs

1 General information

This document applies to STM32 Arm[®]-based general-purpose microcontrollers or microprocessors.

Note: Arm is a registered trademark of Arm Limited (or its subsidiaries) in the US and/or elsewhere.

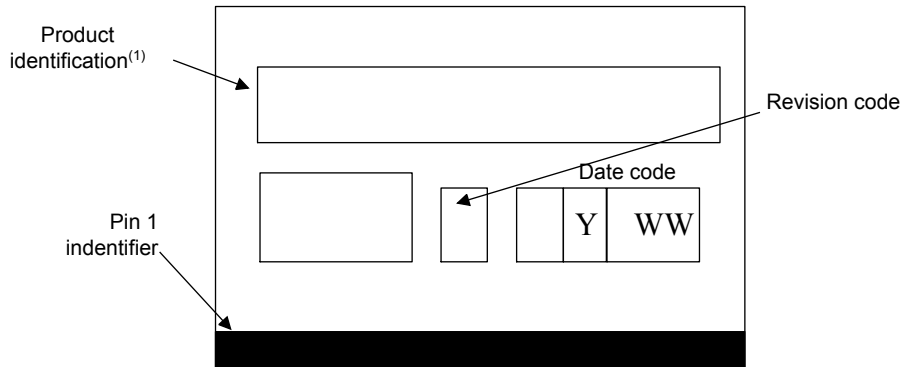


2 Device marking for SO8N (O7)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 1. SO8N marking example (package top view)



O7_SO8N_DM_V4

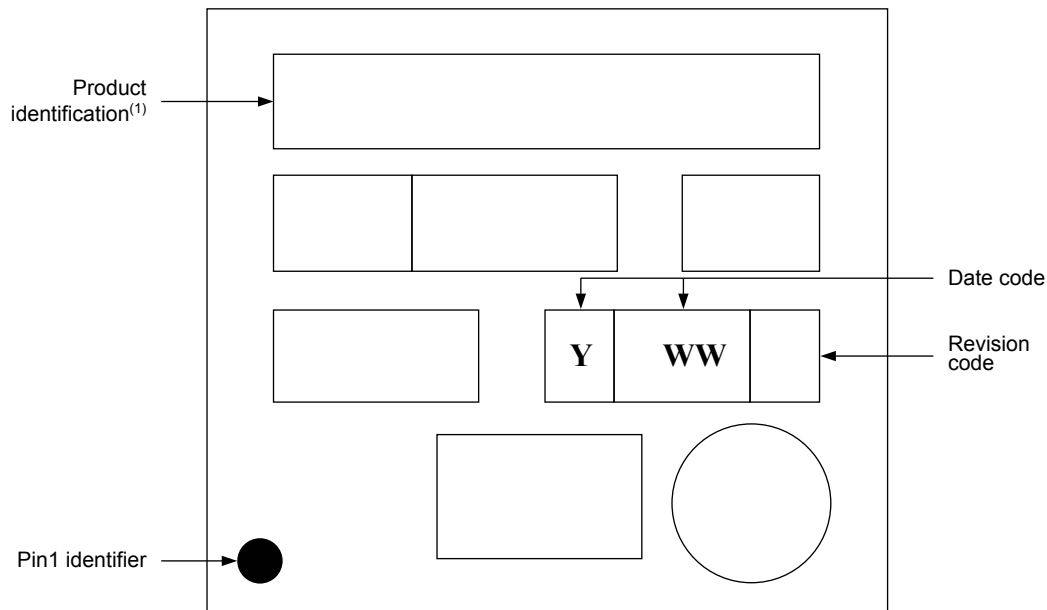
1. Parts marked as ES or E or accompanied by an Engineering Sample notification letter are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event will ST be liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

3 Device marking for TSSOP14 (6R)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 2. TSSOP14 marking example (package top view)



6R_TSSOP14_DM_V2

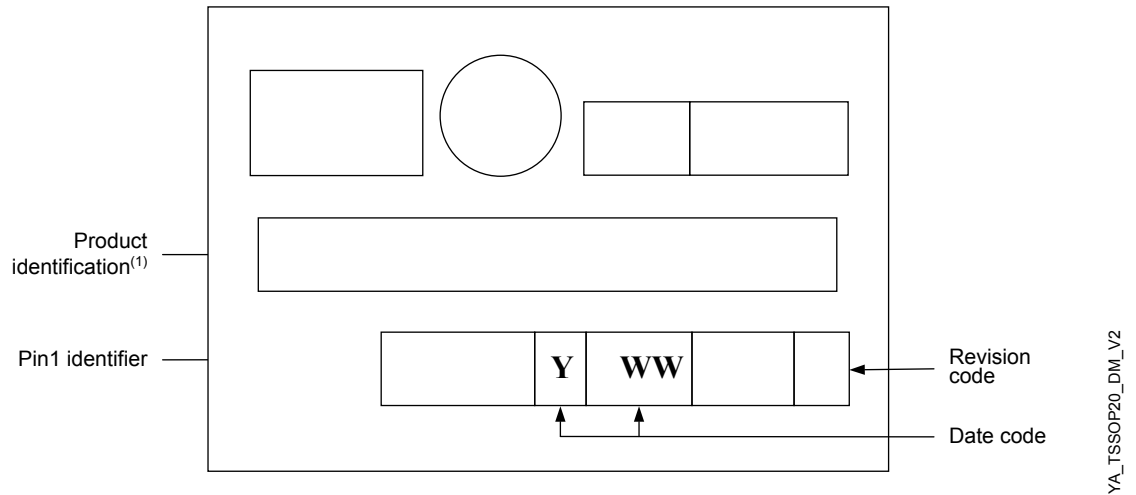
1. Parts marked as ES or E or accompanied by an Engineering Sample notification letter are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

4 Device marking for TSSOP20 (YA)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 3. TSSOP20 marking example (package top view)



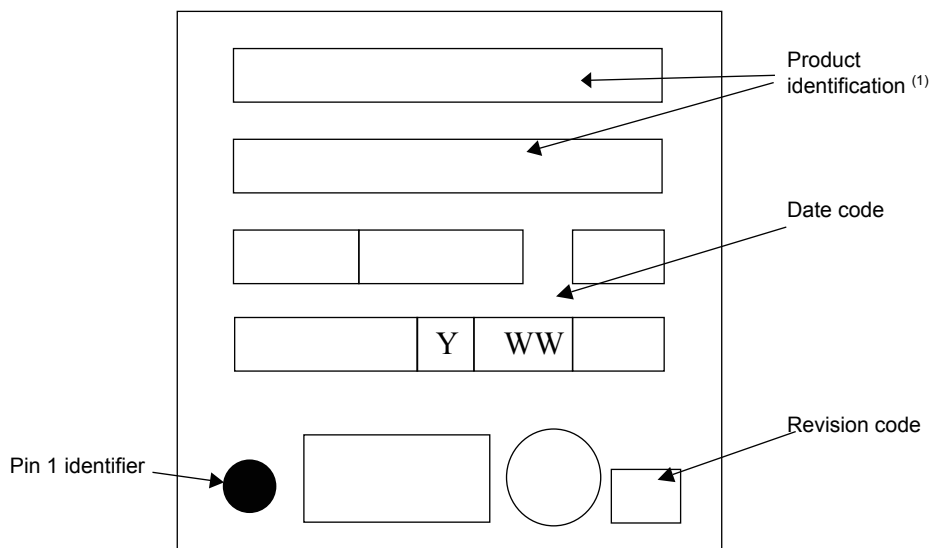
1. Parts marked as ES or E or accompanied by an Engineering Sample notification letter are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event will ST be liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

5 Device marking for LQFP32 (5V)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 4. LQFP32 marking example (package top view)



5V_LQFP32_DM_V1

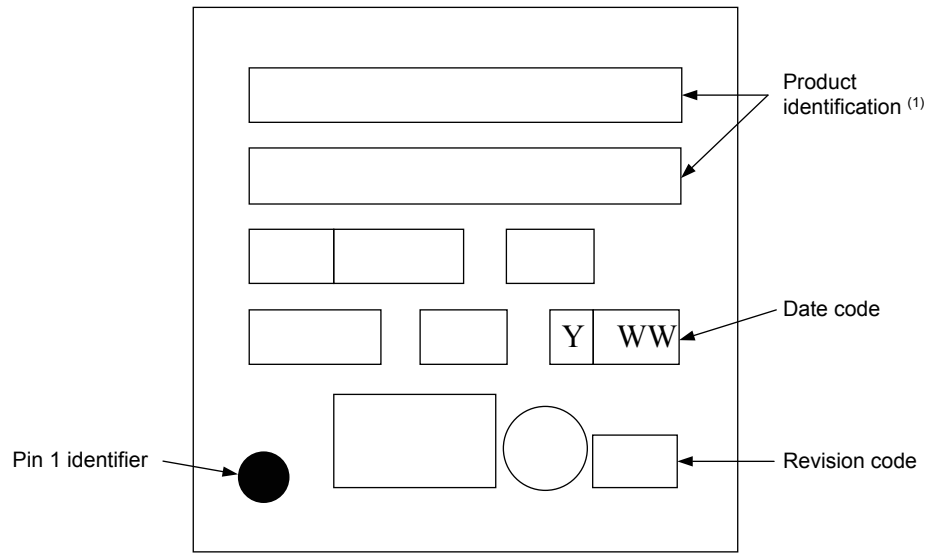
1. Parts marked as ES or E or accompanied by an Engineering Sample notification letter are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

6 Device marking for LQFP44 (4Y)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 5. LQFP44 marking example (package top view)



4Y_LQFP44_DM_V1

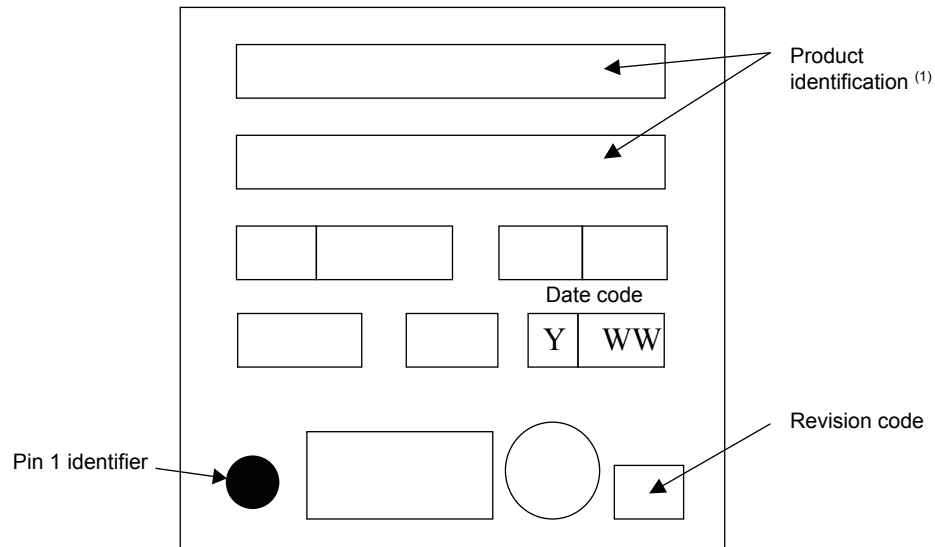
1. Parts marked as ES or E or accompanied by an Engineering Sample notification letter are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

7 Device marking for LQFP48 (5B)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 6. LQFP48 marking example (package top view)



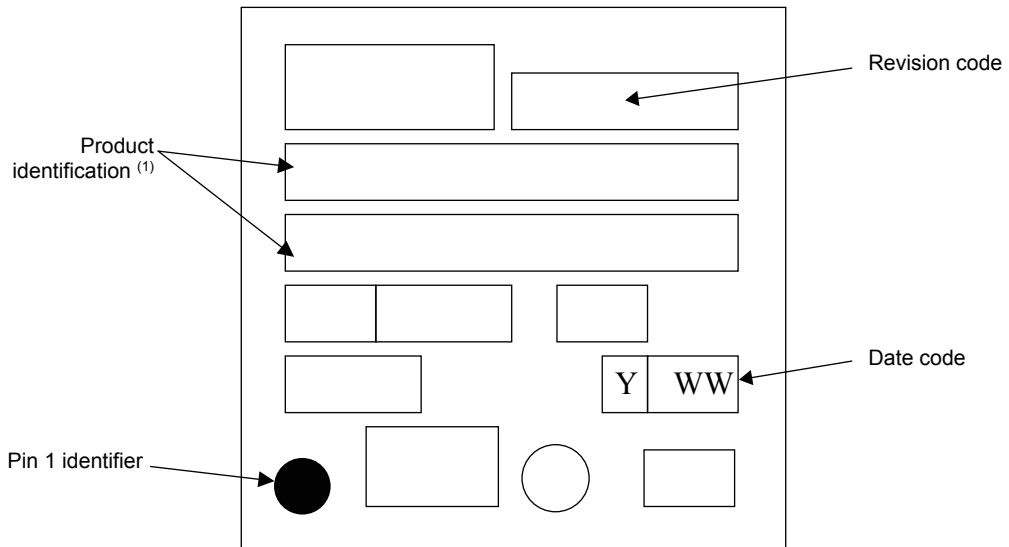
1. Parts marked as ES or E or accompanied by an Engineering Sample notification letter are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

8 Device marking for LQFP64 (5W)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 7. LQFP64 marking example (package top view)



5W_LQFP64_DM_V2

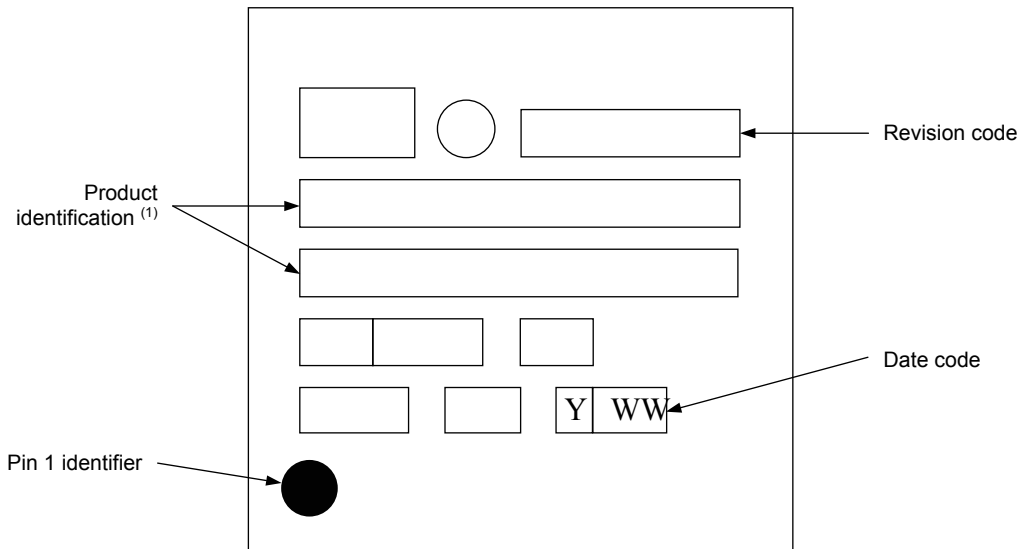
1. Parts marked as ES or E or accompanied by an Engineering Sample notification letter are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

9 Device marking for LQFP64 (1R)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 8. LQFP64 marking example (package top view)



1R_LQFP64_DM_V1

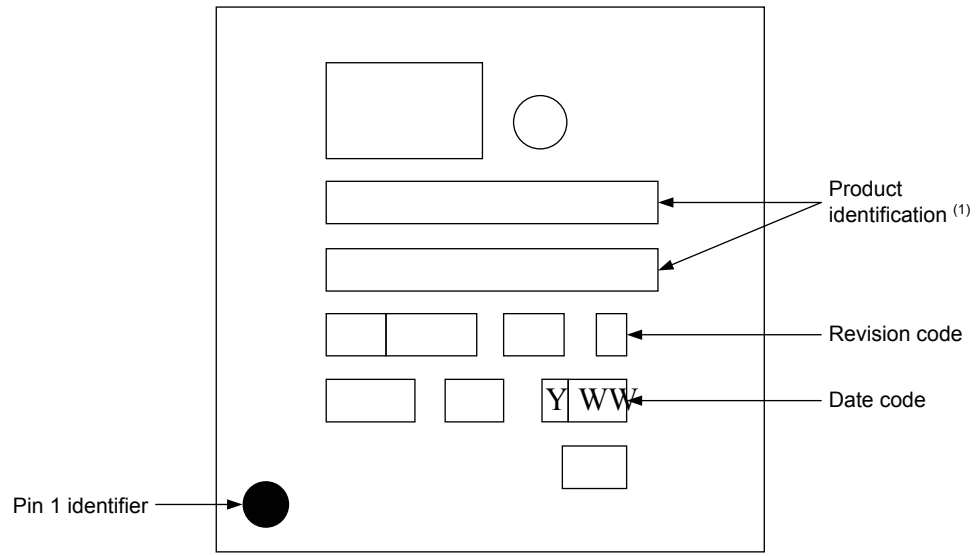
1. Parts marked as ES or E or accompanied by an Engineering Sample notification letter are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

10 Device marking for LQFP80 (9X)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 9. LQFP80 marking example (package top view)



9X_LQFP80_DM_V1

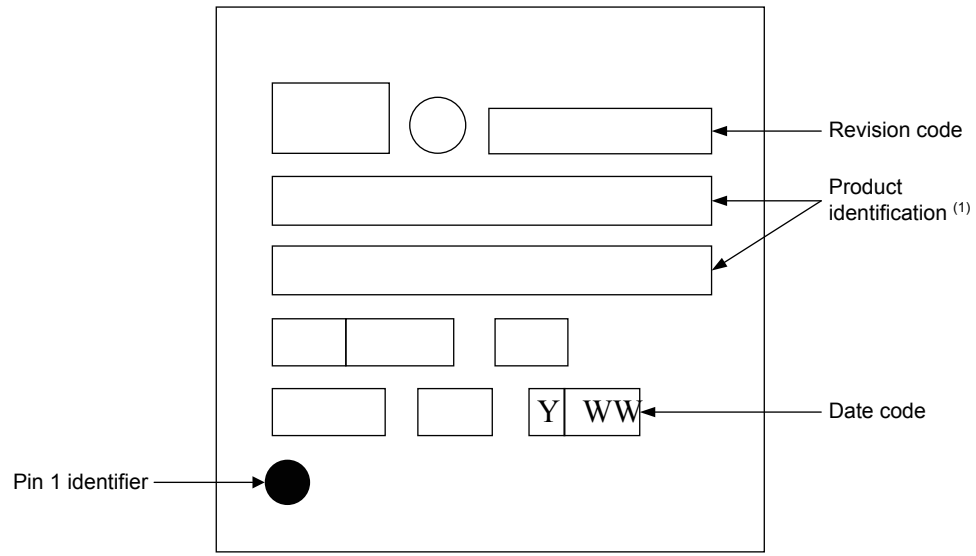
1. Parts marked as ES or E or accompanied by an Engineering Sample notification letter are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

11 Device marking for LQFP80 (1S)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 10. LQFP80 marking example (package top view)



1S_LQFP80_DM_V1

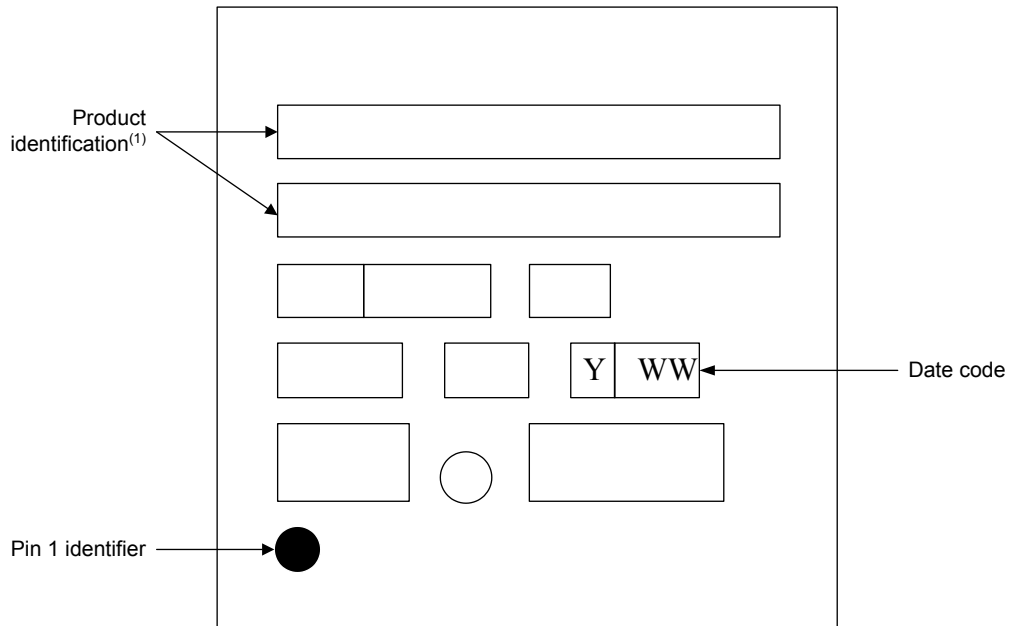
1. Parts marked as ES or E or accompanied by an Engineering Sample notification letter are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

12 Device marking for LQFP100 (1L)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 11. LQFP100 marking example (package top view)



1L_LQFP100_DM_V1

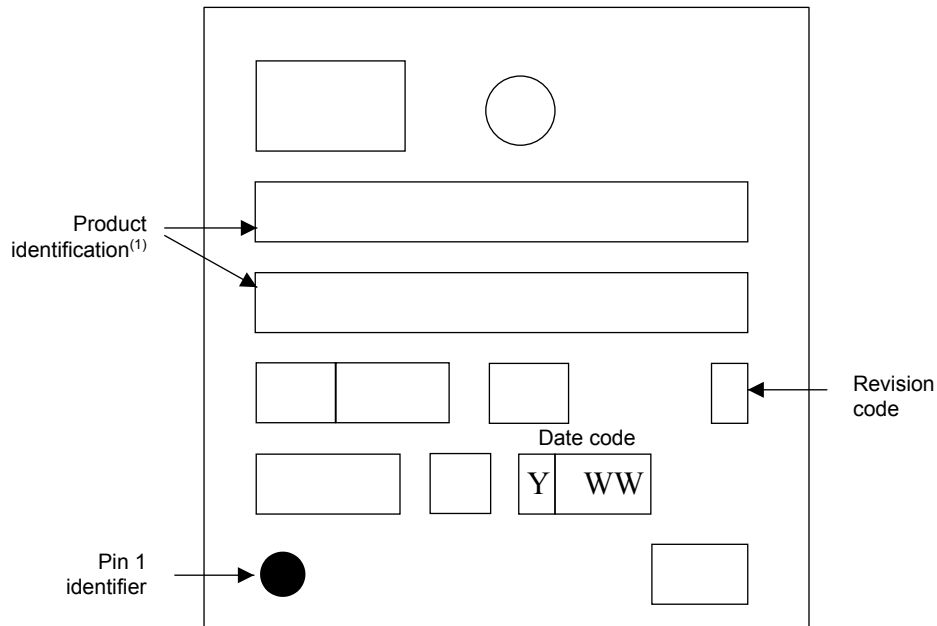
1. Parts marked as ES or E or accompanied by an Engineering Sample notification letter are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

13 Device marking for LQFP128 (TC)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 12. LQFP128 marking example (package top view)



TC_LQFP128_DM_V2

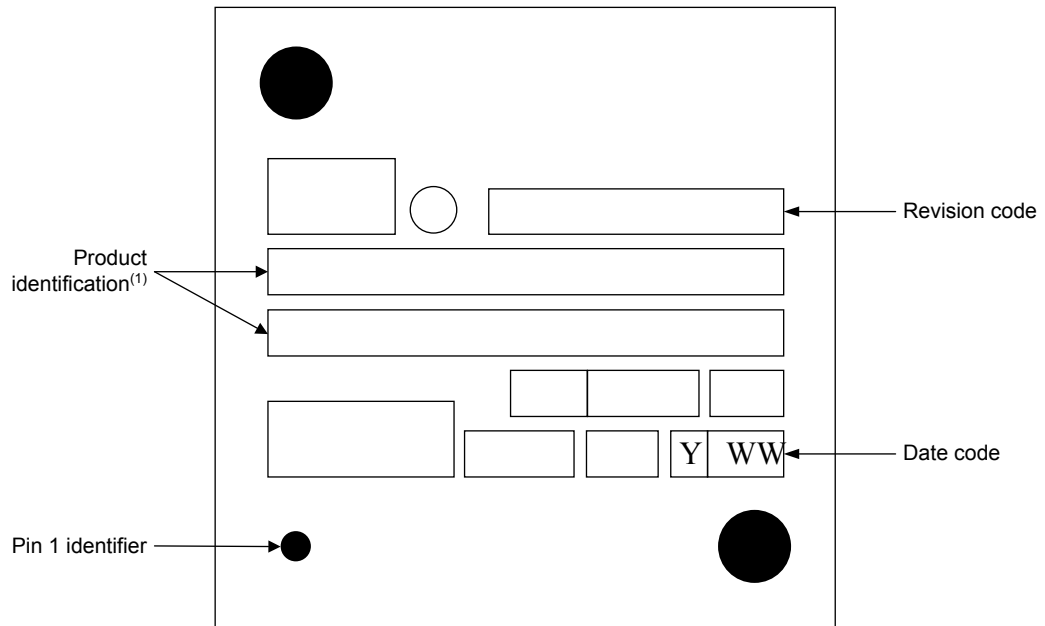
1. Parts marked as ES or E or accompanied by an Engineering Sample notification letter are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event will ST be liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

14 Device marking for LQFP144 (1A)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 13. LQFP144 marking example (package top view)



1A_LQFP144_DM_V1

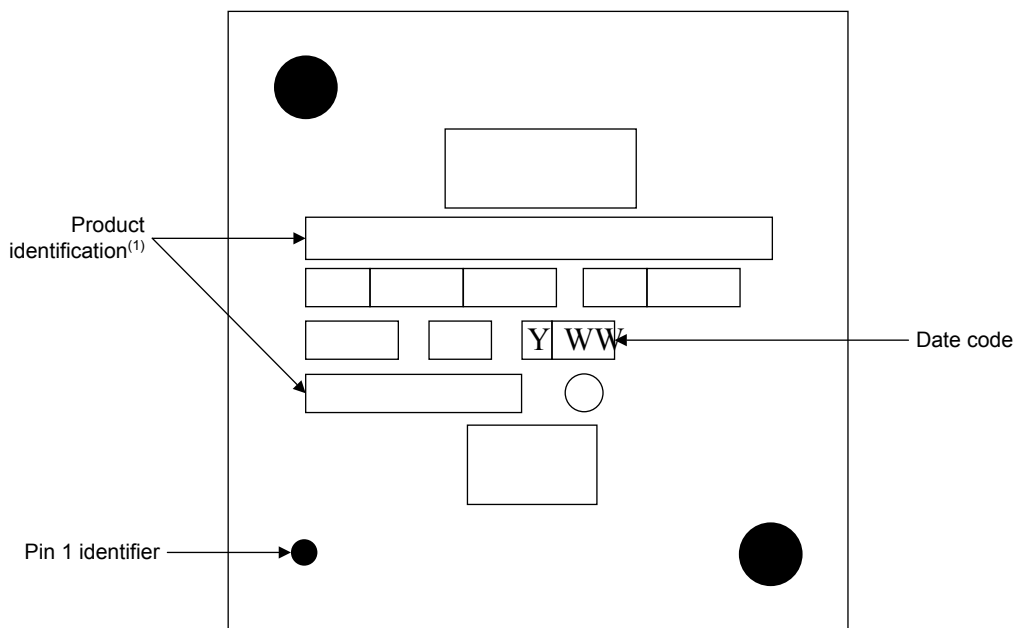
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15 Device marking for LQFP176 (1T)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 14. LQFP176 marking example (package top view)



1T_LQFP176_DM_V1

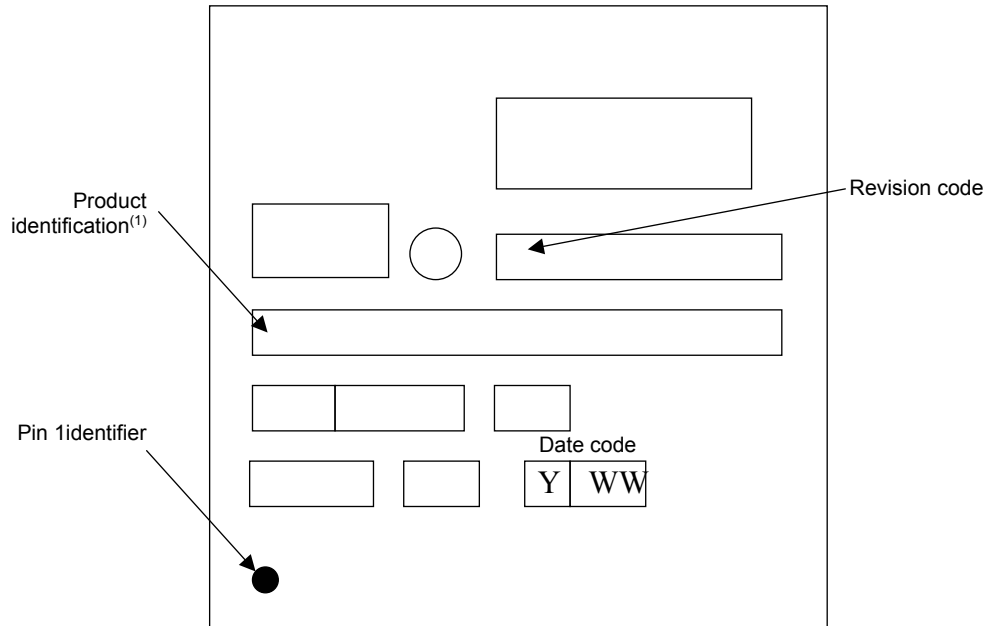
1. Parts marked as ES or E or accompanied by an Engineering Sample notification letter are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

16 Device marking for LQFP208 (UH)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 15. LQFP208 marking example (package top view)



UH_LQFP208_DM_V1

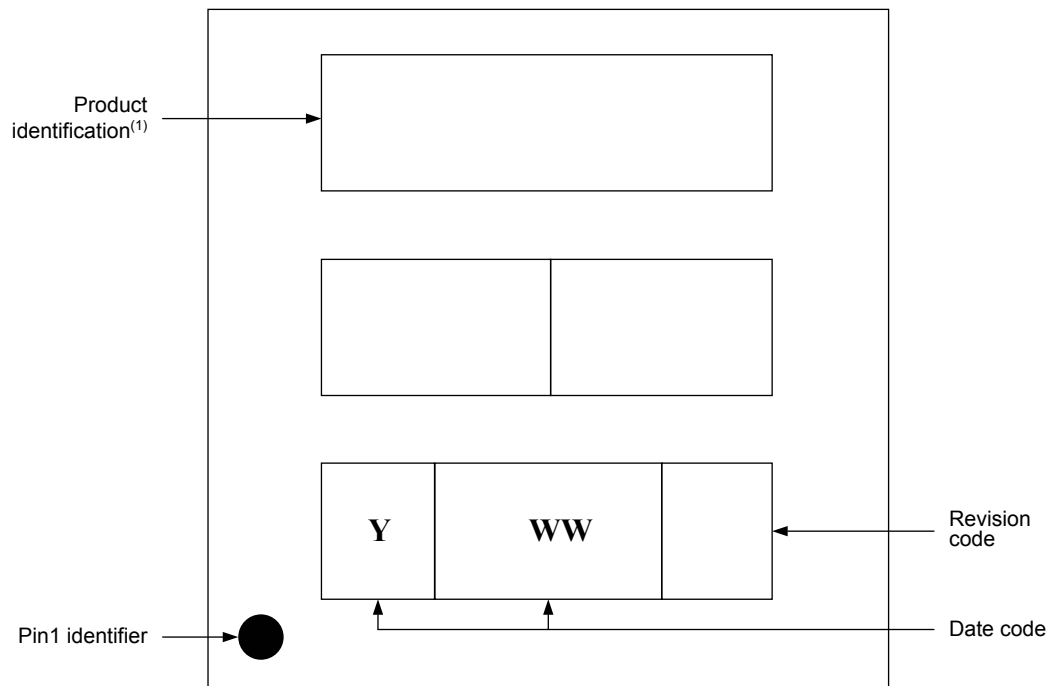
1. Parts marked as ES or E or accompanied by an Engineering Sample notification letter are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event will ST be liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

17 Device marking for UFQFPN20 (A0A5)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 16. UFQFPN20 marking example (package top view)



A0A5_UFQFPN20_DM_V2

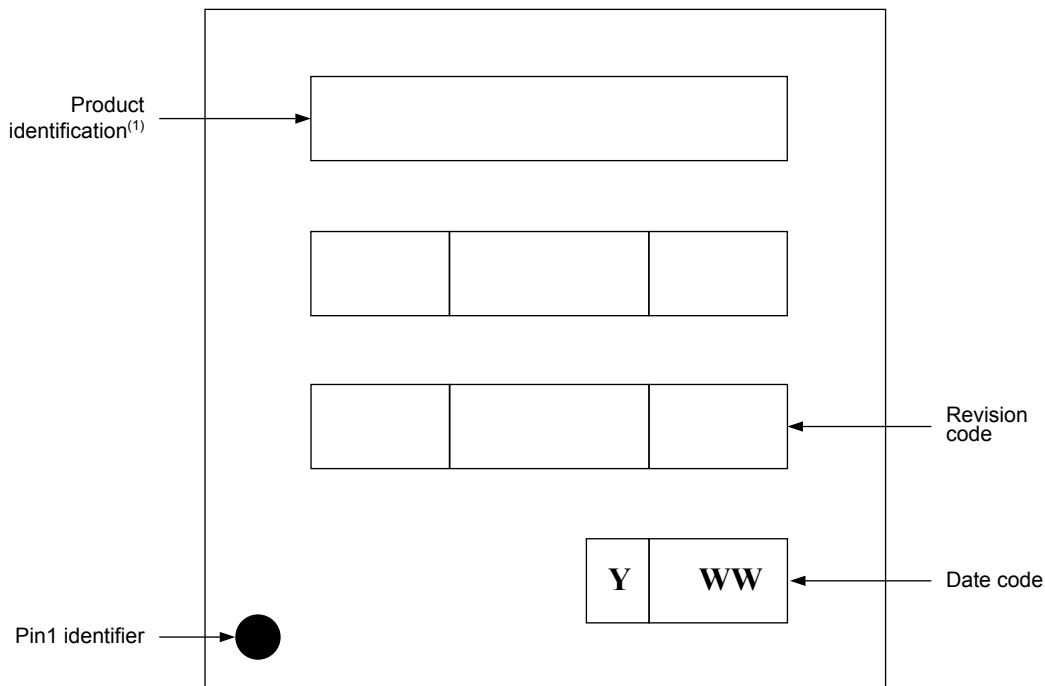
1. Parts marked as ES or E or accompanied by an Engineering Sample notification letter are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

18 Device marking for UFQFPN28 (A0B0)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 17. UFQFPN28 marking example (package top view)



A0B0_UFQFPN28_DM_V3

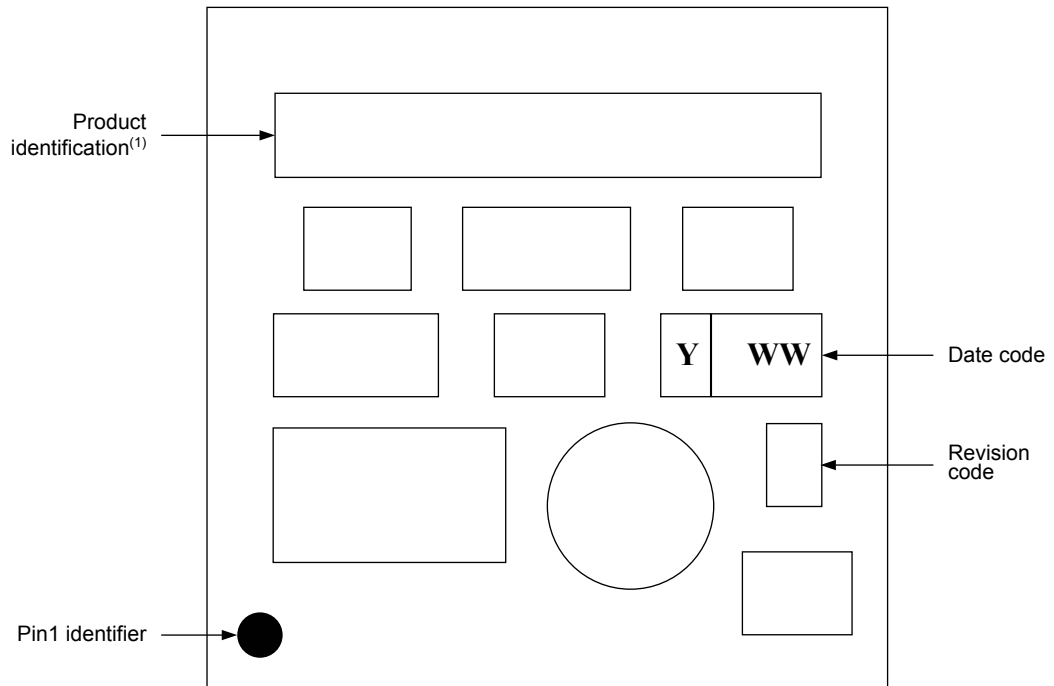
1. Parts marked as ES or E or accompanied by an Engineering Sample notification letter are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

19 Device marking for UFQFPN32 (A09E)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 18. UFQFPN32 marking example (package top view)



A09E_UFQFPN32_DM_V2

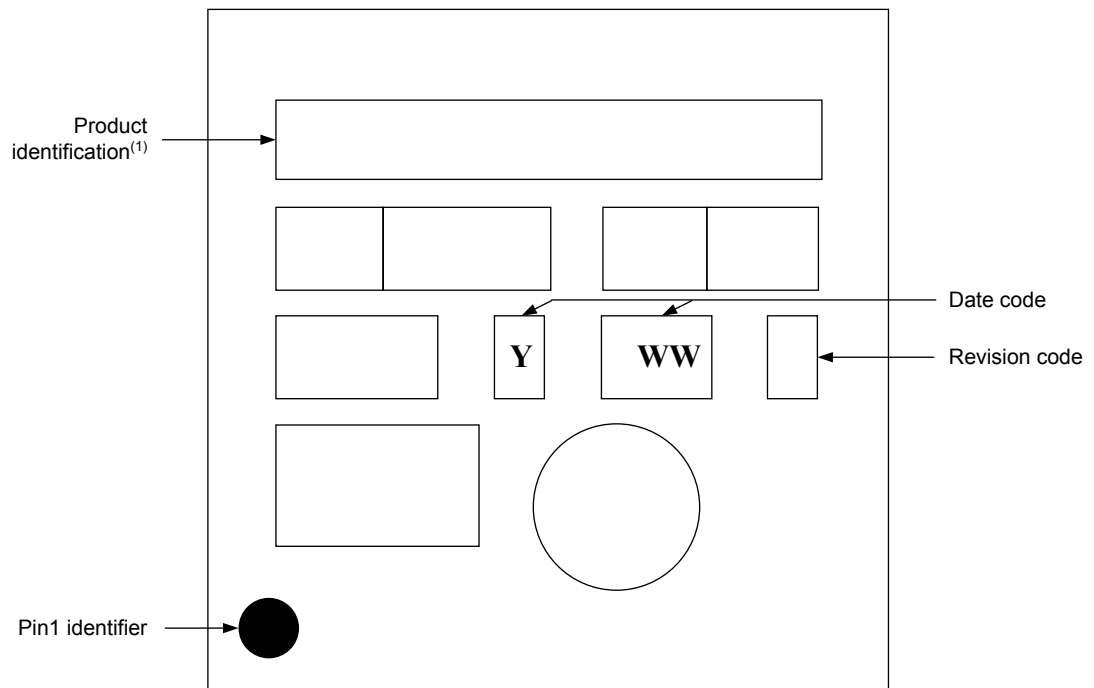
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20 Device marking for UFQFPN32 (A0B8)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 19. UFQFPN32 marking example (package top view)



A0B8_UFQFPN32_DM_V1

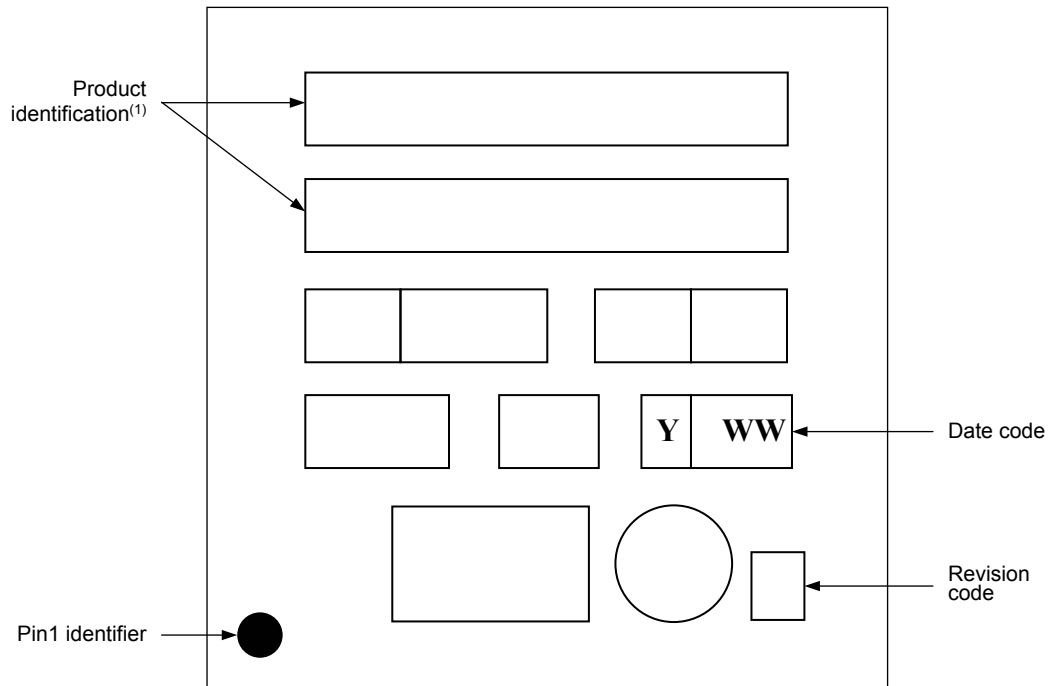
1. Parts marked as ES or E or accompanied by an Engineering Sample notification letter are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

21 Device marking for UFQFPN48 (A0B9)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 20. UFQFPN48 marking example (package top view)



A0B9_UFQFPN48_DM_V3

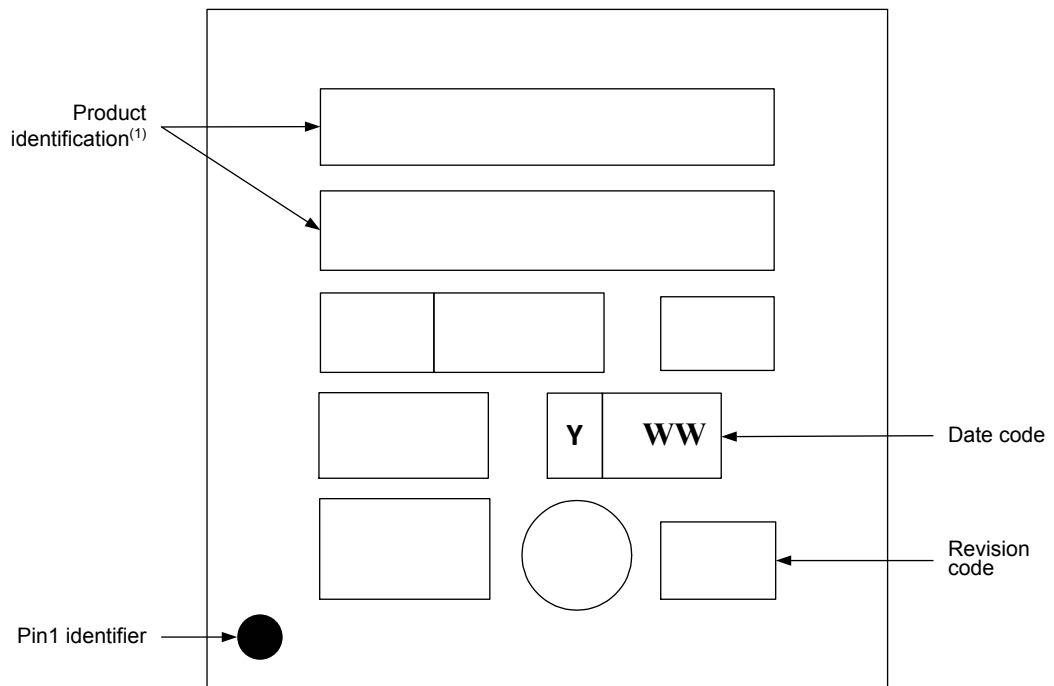
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22 Device marking for VFQFPN36 (ZR)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 21. VFQFPN36 marking example (package top view)



ZR_VFQFPN36_DM_V2

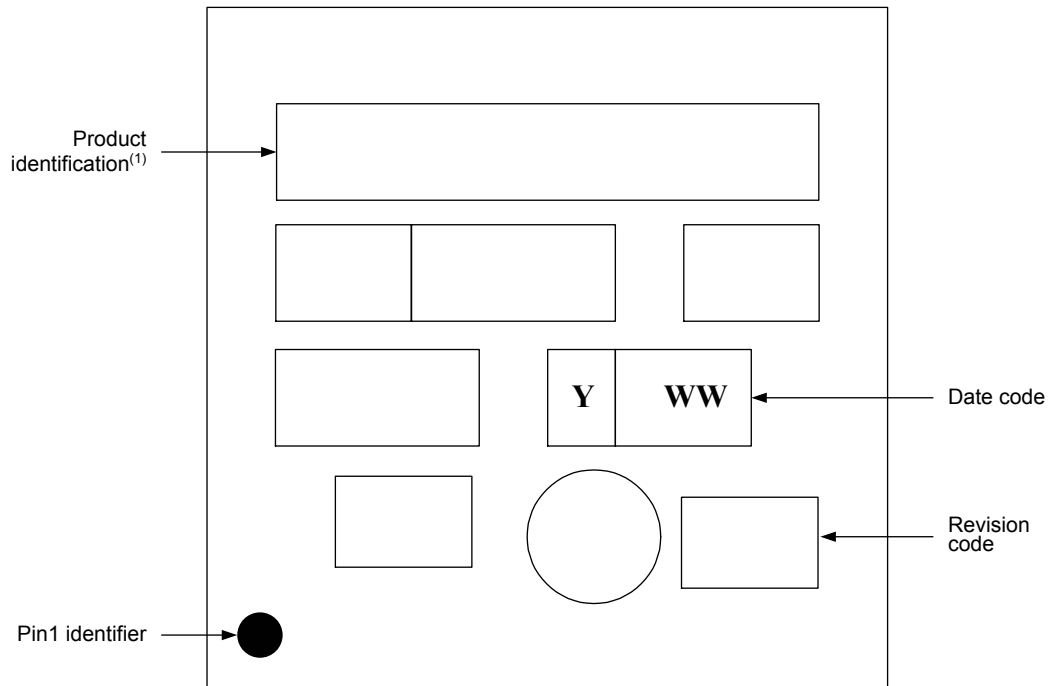
1. Parts marked as ES or E or accompanied by an Engineering Sample notification letter are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event will ST be liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

23 Device marking for VFQFPN32 (42)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 22. VFQFPN32 marking example (package top view)



42_VFQFPN32_DM_V1

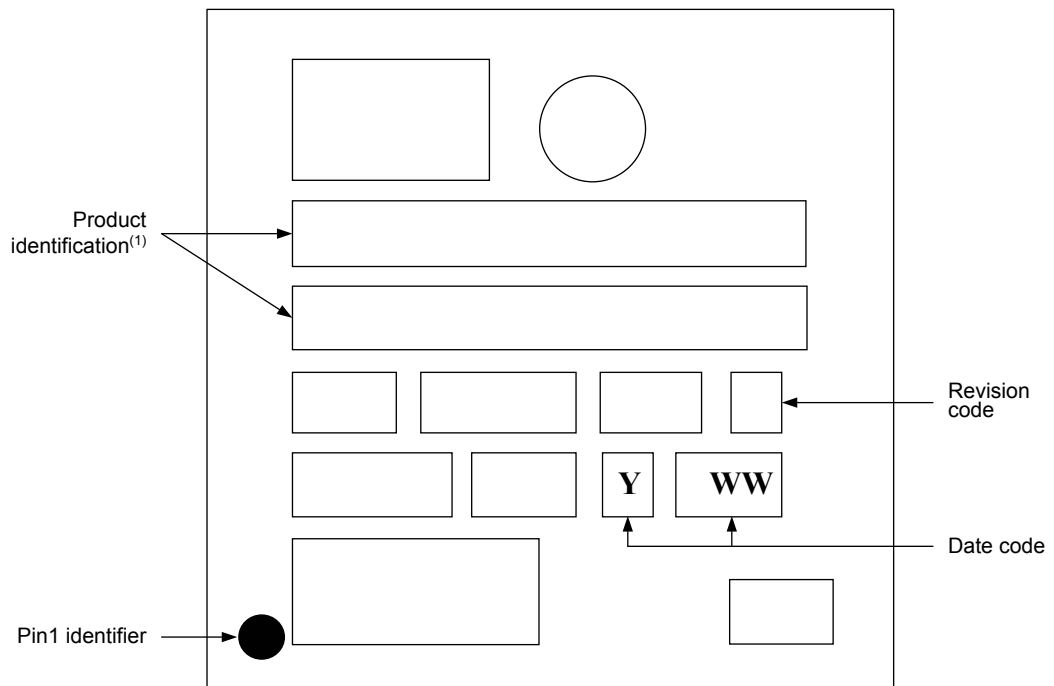
1. Parts marked as ES or E or accompanied by an Engineering Sample notification letter are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

24 Device marking for VFQFPN68 (B029)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 23. VFQFPN68 marking example (package top view)



B029_VFQFPN68_DM_V4

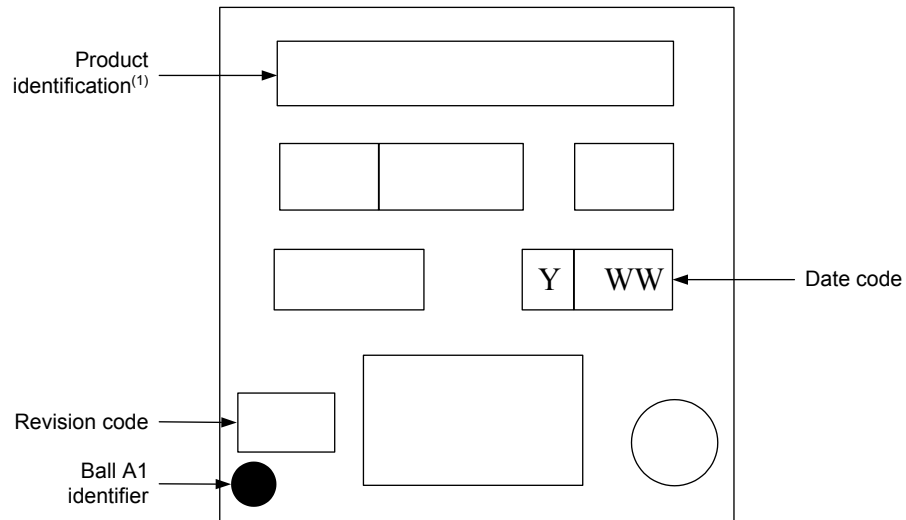
1. Parts marked as ES or E or accompanied by an Engineering Sample notification letter are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

25 Device marking for LFBGA36 (AL)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 24. LFBGA36 marking example (package top view)



AL_LFBGA36_DM_V1

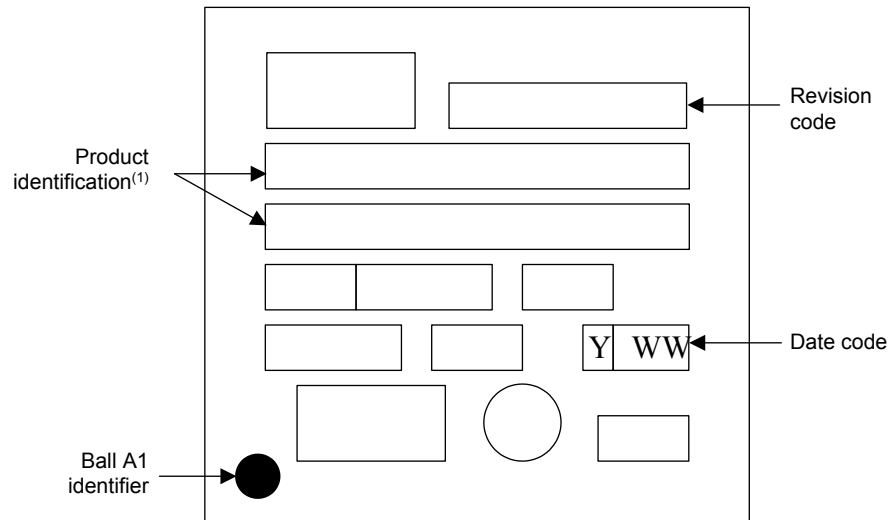
1. Parts marked as ES or E or accompanied by an Engineering Sample notification letter are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

26 Device marking for LFBGA100 (H0)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 25. LFBGA100 marking example (package top view)



H0_LFBGA100_DM_V2

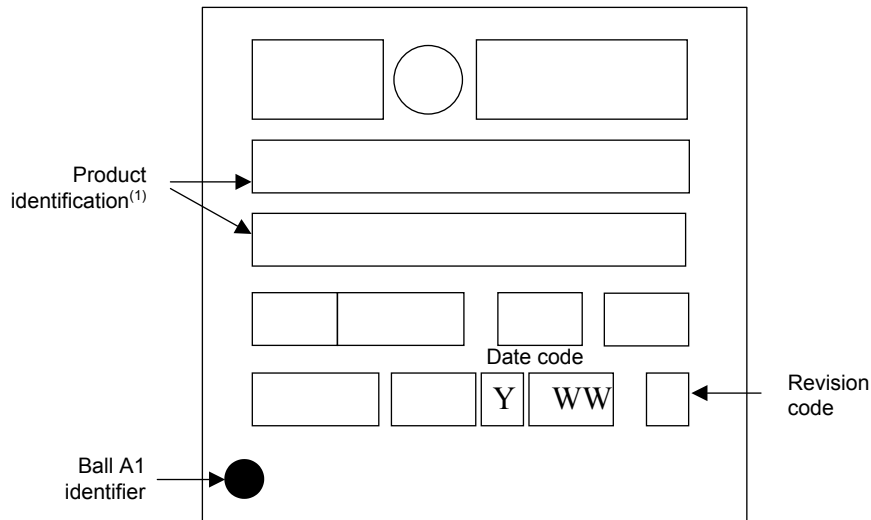
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27 Device marking for LFBGA144 (X3)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 26. LFBGA144 marking example (package top view)



X3_LFBGA144_DM_V2

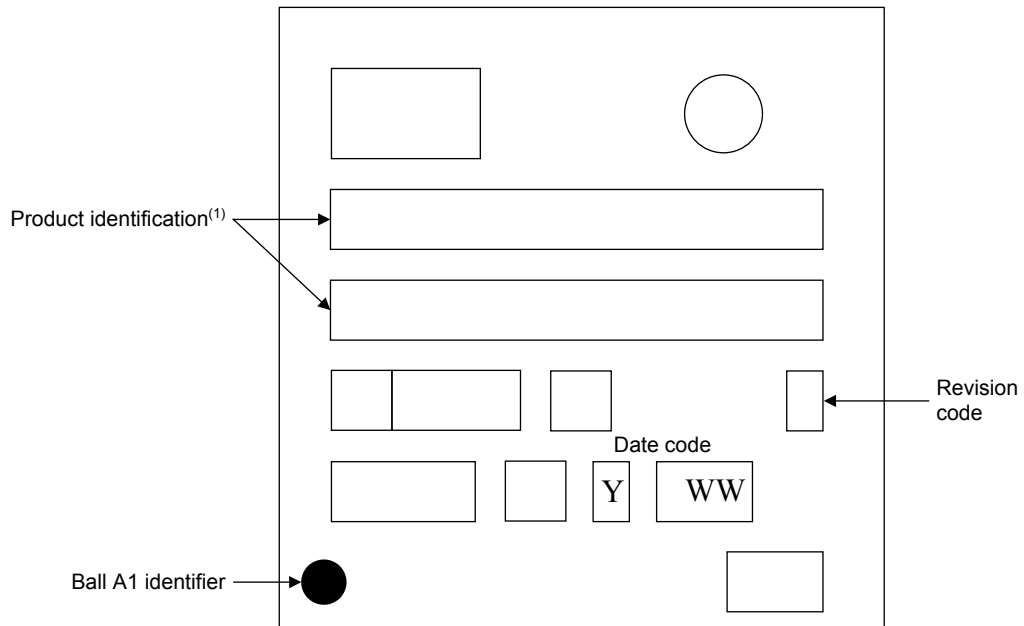
1. Parts marked as ES or E or accompanied by an Engineering Sample notification letter are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event will ST be liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

28 Device marking for LFBGA289 (B0ED)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 27. LFBGA289 marking example (package top view)



B0ED_LFBGA289_DM_V1

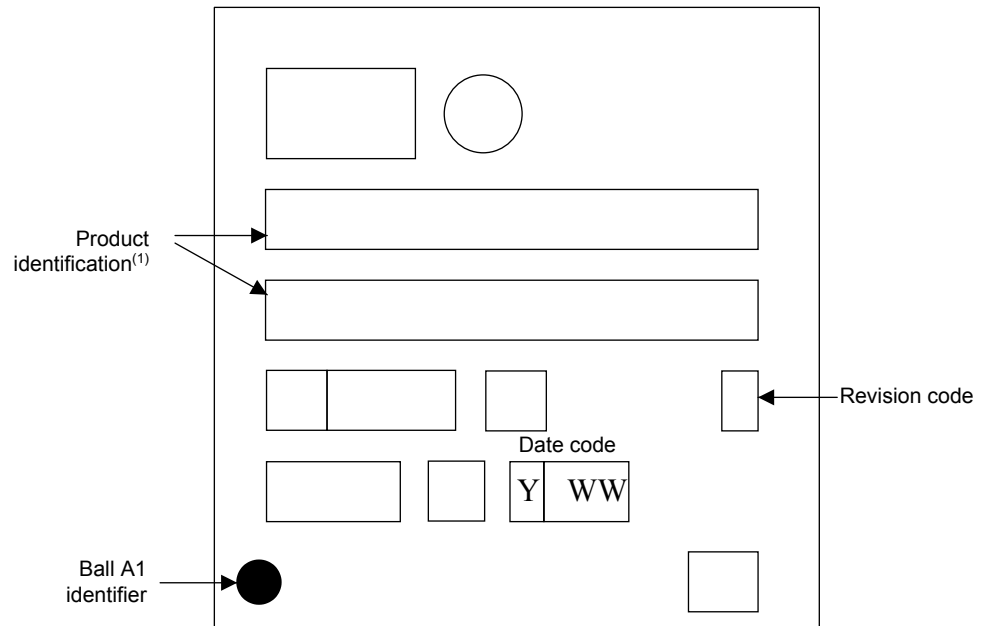
- Parts marked as ES or E or accompanied by an Engineering Sample notification letter are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

29 Device marking for LFBGA354 (B02Z)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 28. LFBGA354 marking example (package top view)



B02Z_LFBGA354_DM_V1

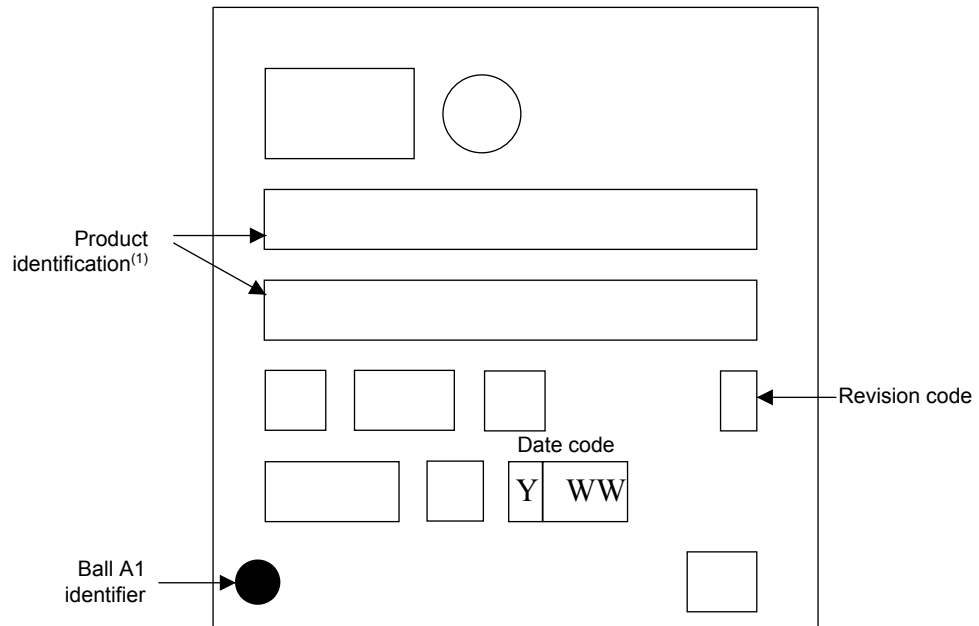
1. Parts marked as ES or E or accompanied by an Engineering Sample notification letter are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

30 Device marking for LFBGA448 (B032)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 29. LFBGA448 marking example (package top view)



B032_LFBGA448_DM_V1

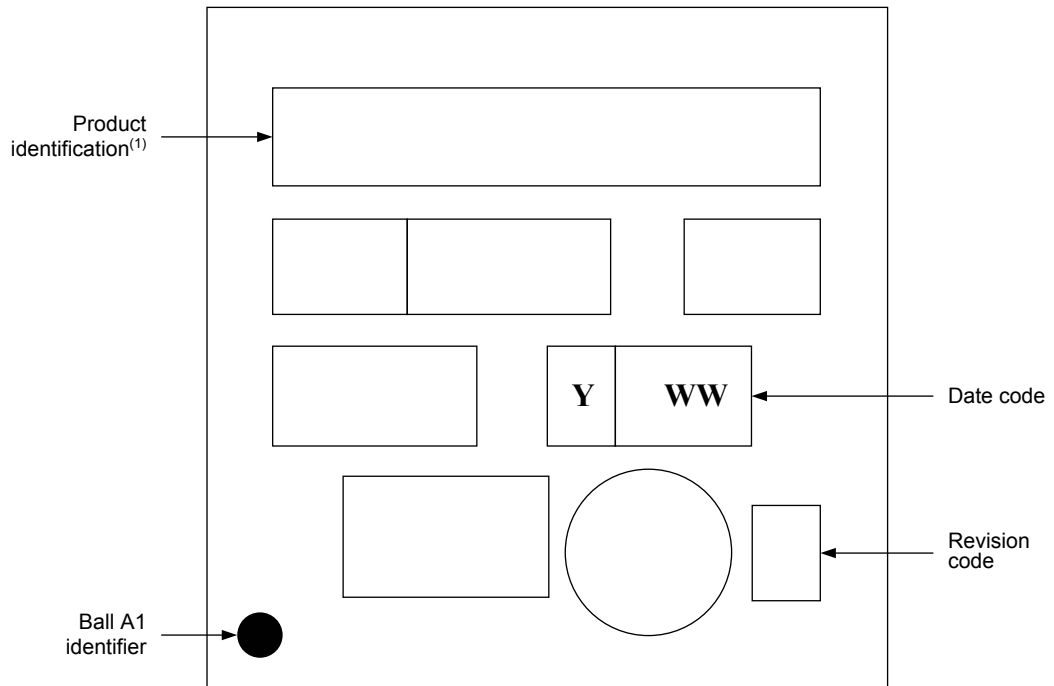
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31 Device marking for TFBGA64 (R8)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 30. TFBGA64 marking example (package top view)



R8_TFBGA64_DM_V2

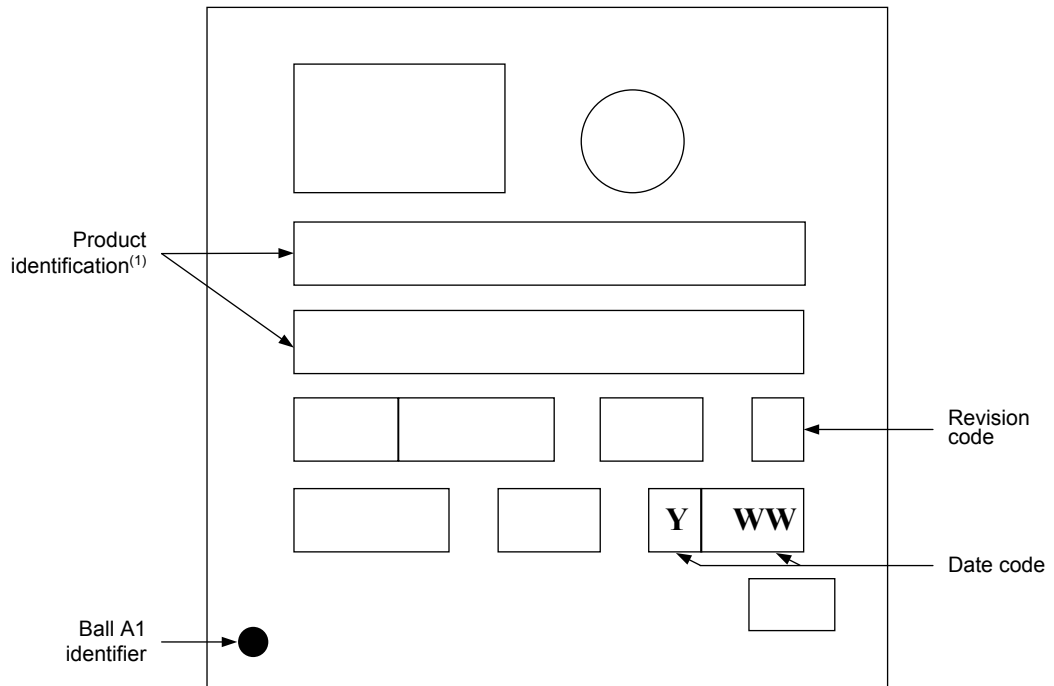
1. Parts marked as ES or E or accompanied by an Engineering Sample notification letter are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event will ST be liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

32 Device marking for TFBGA100 (A08Q)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 31. TFBGA100 marking example (package top view)



A08Q_TFBGA100_DM_V3

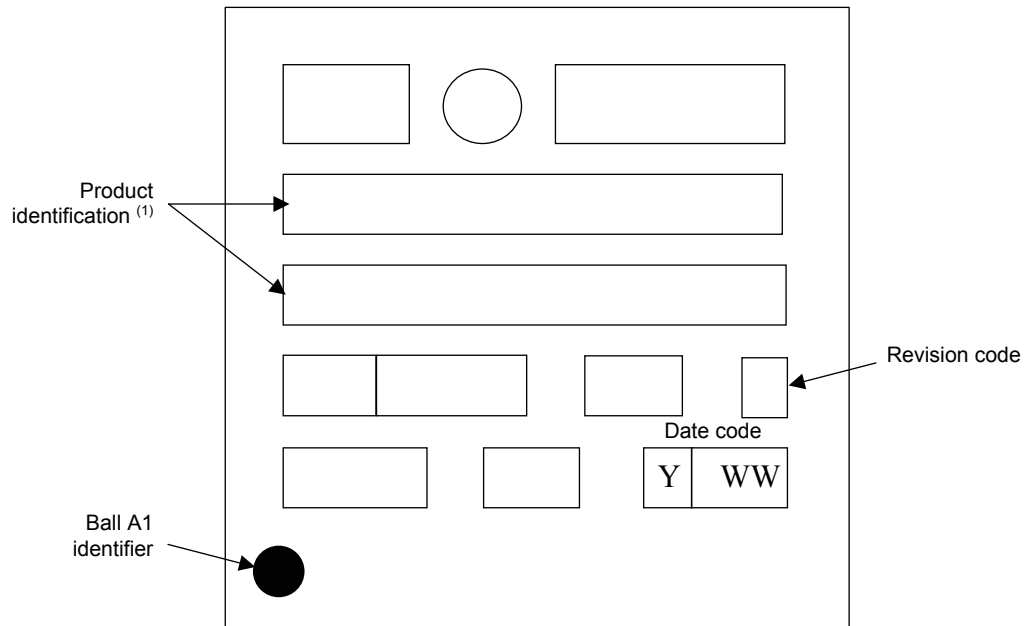
1. Parts marked as ES or E or accompanied by an Engineering Sample notification letter are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

33 Device marking for TFBGA216 (A0L2)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 32. TFBGA216 marking example (package top view)



A0L2_TFBGA216_DM_V1

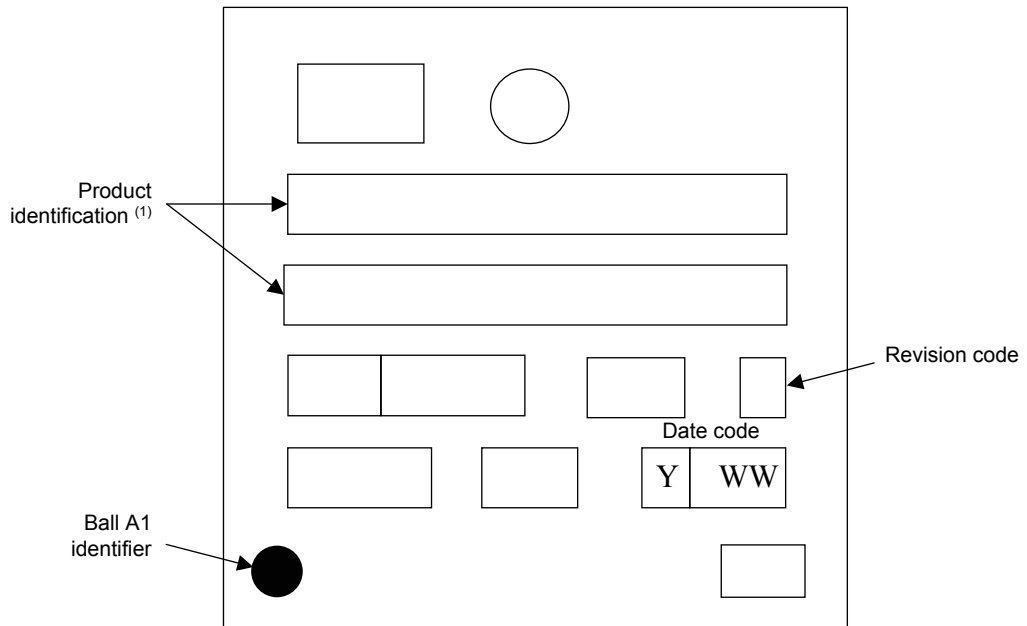
1. Parts marked as ES or E or accompanied by an Engineering Sample notification letter are not yet qualified and therefore not approved for use in production. ST is not responsible for any consequences resulting from such use. In no event is ST liable for the customer using any of these engineering samples in production. ST's Quality department must be contacted prior to any decision to use these engineering samples to run a qualification activity.

34 Device marking for TFBGA225 (B04V)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 33. TFBGA225 marking example (package top view)



B04V_TFBGA225_DM_V1

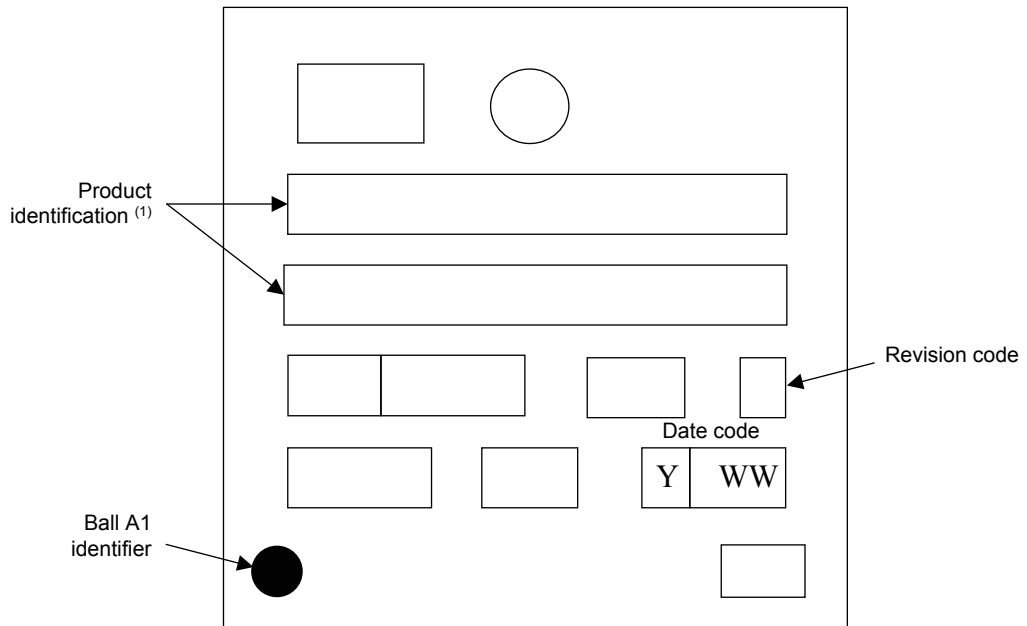
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35 Device marking for TFBGA257 (B02Y)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 34. TFBGA257 marking example (package top view)



B02Y_TFBGA257_DM_V1

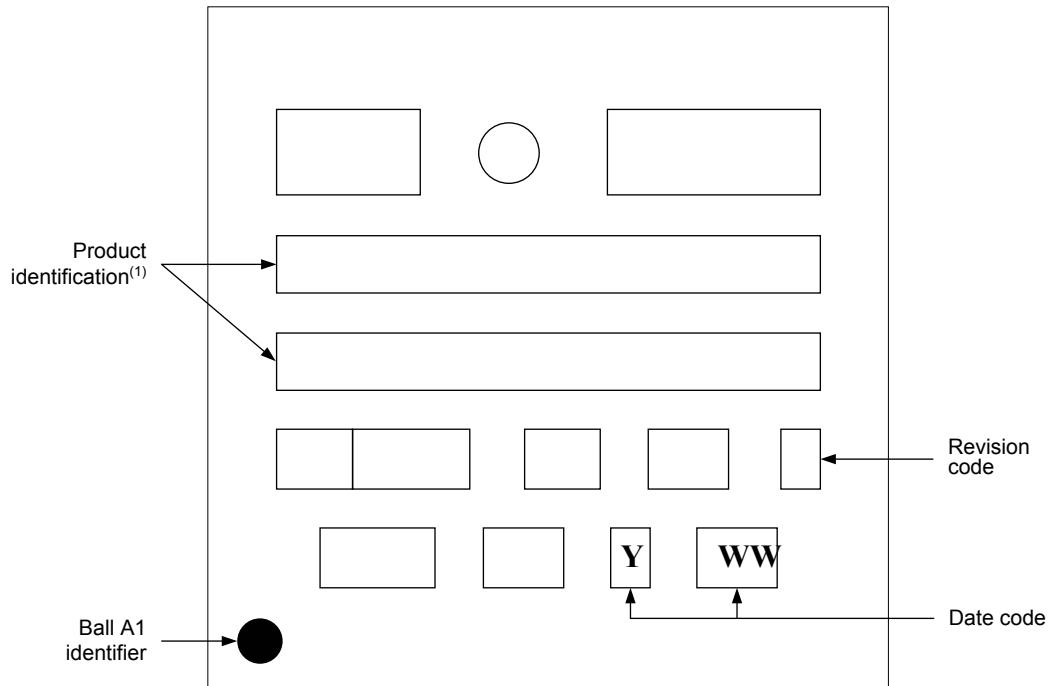
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36 Device marking for TFBGA240+25 (A07U)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 35. TFBGA240+25 marking example (package top view)



A07U_TFBGA240+25_DM_V2

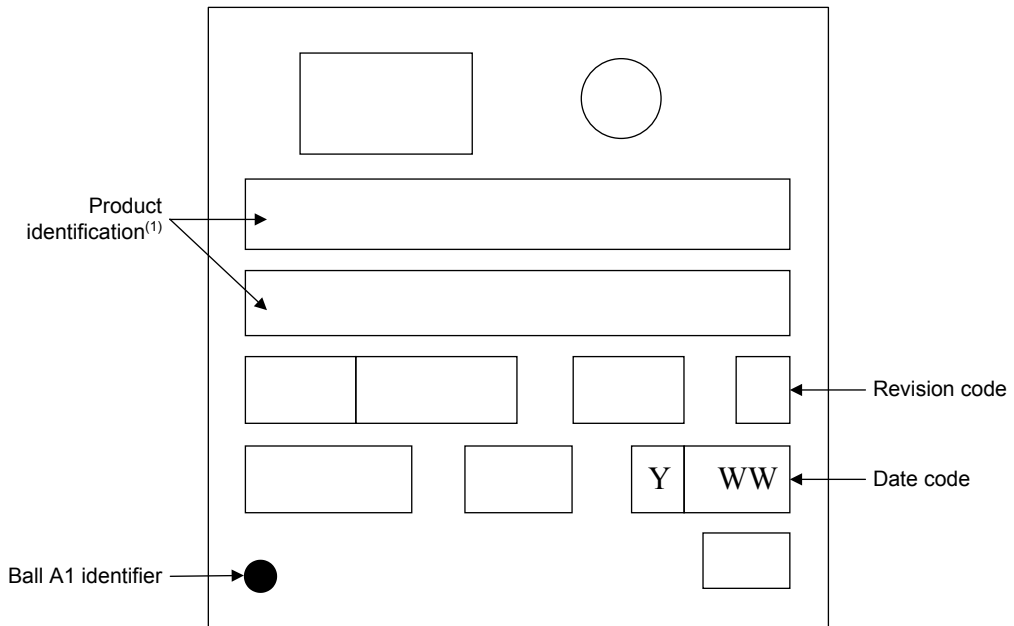
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37 Device marking for TFBGA289 (B0EB)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 36. TFBGA289 marking example (package top view)



B0EB_TFBGA289_DM_V1

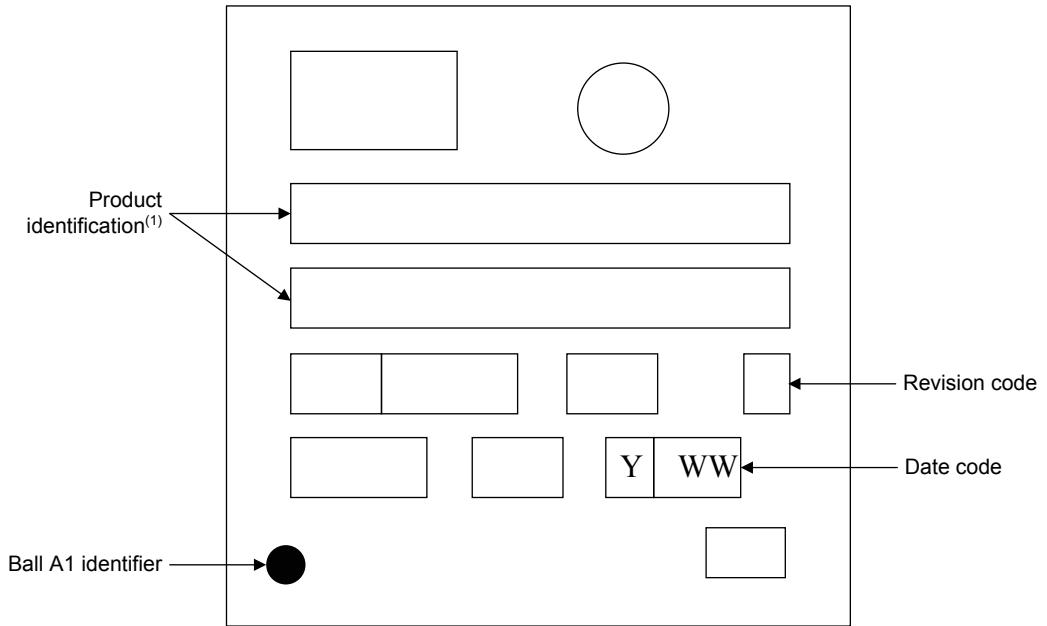
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38 Device marking for TFBGA320 (B0EC)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 37. TFBGA320 marking example (package top view)



B0EC_TFBGA320_DM_V1

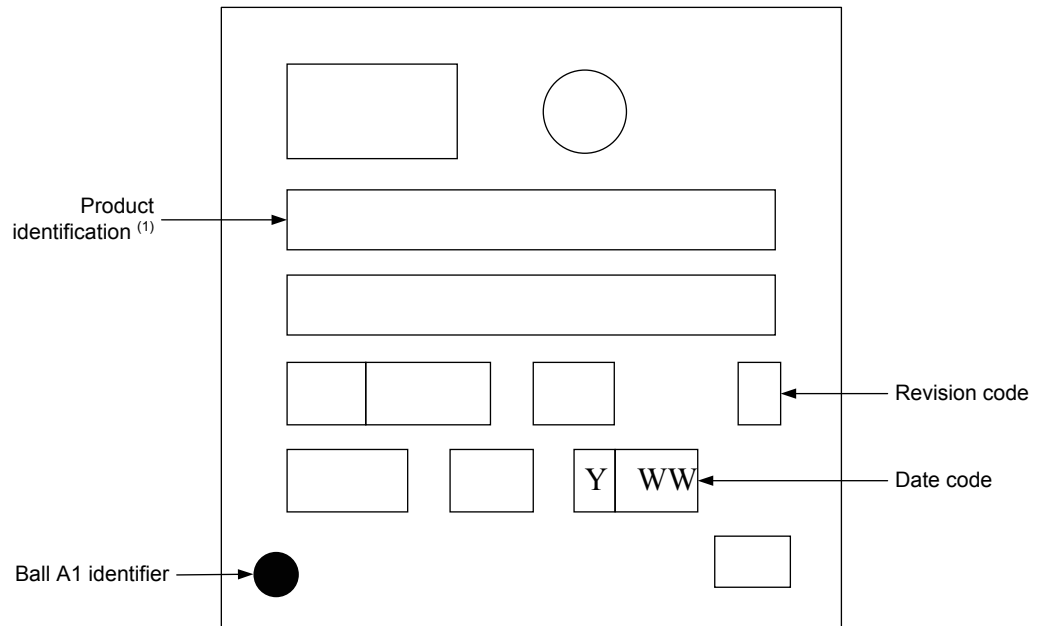
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39 Device marking for TFBGA361 (B031)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 38. TFBGA361 marking example (package top view)



B031_TFBGA361_DM_V1

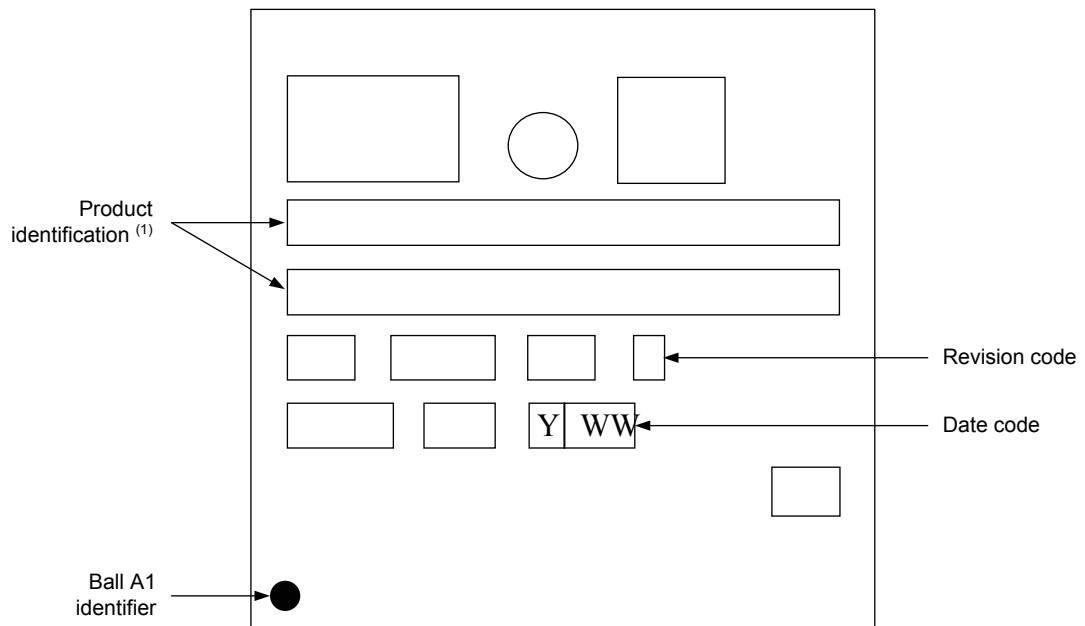
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40 Device marking for TFBGA436 (B0D1)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 39. TFBGA436 marking example (package top view)



B0D1_TFBGA436_DM_V1

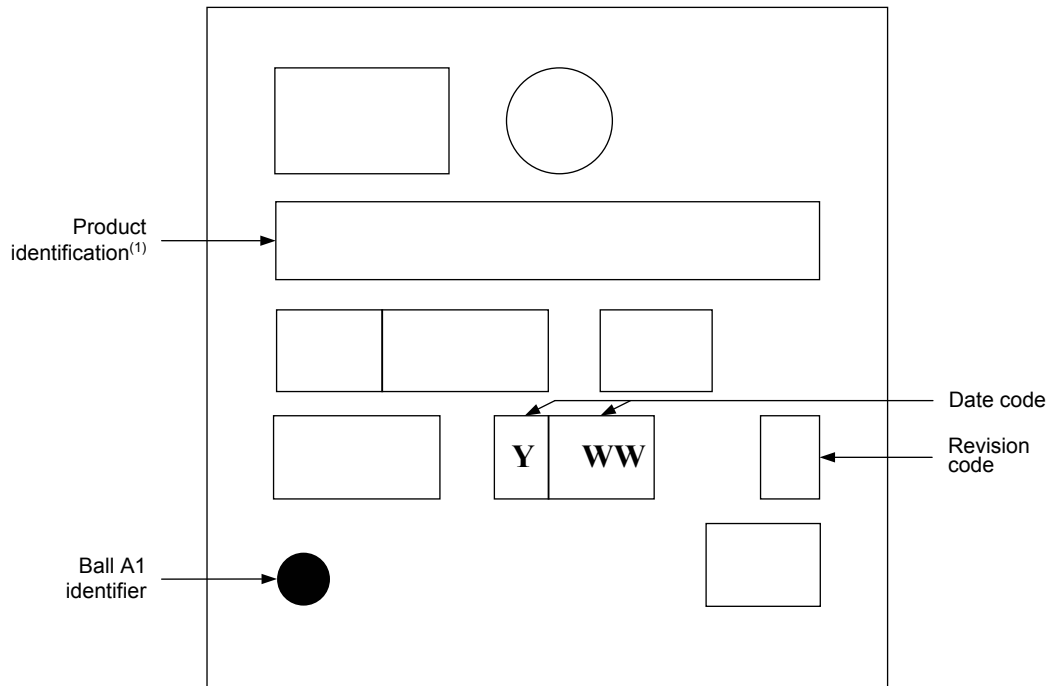
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41 Device marking for UFBGA59 (B0FS)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 40. UFBGA59 marking example (package top view)



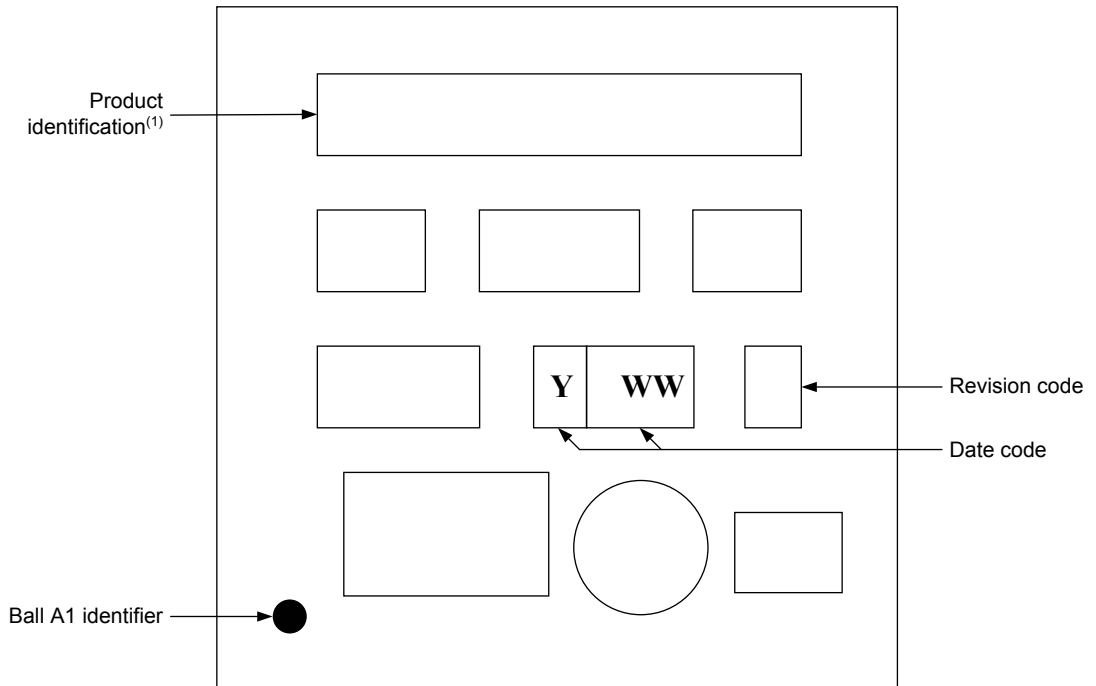
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42 Device marking for UFBGA64 (A019)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 41. UFBGA64 marking example (package top view)



A019_UFBGA64_DM_V2

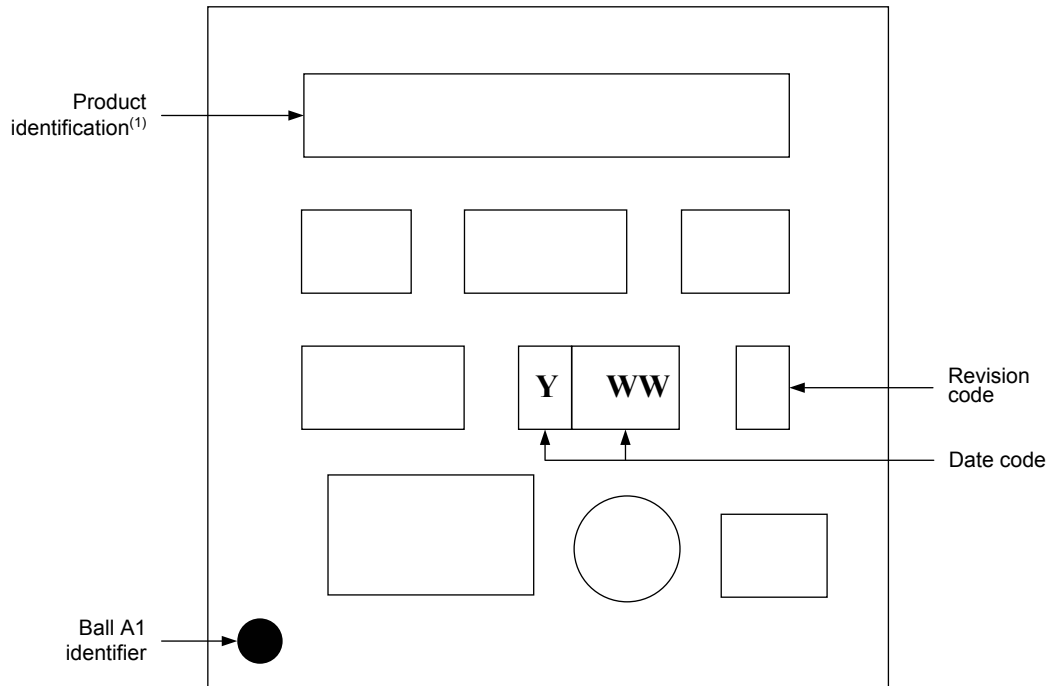
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43 Device marking for UFBGA73 (B08E)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 42. UFBGA73 marking example (package top view)



B08E_UFBGA73_DM_V2

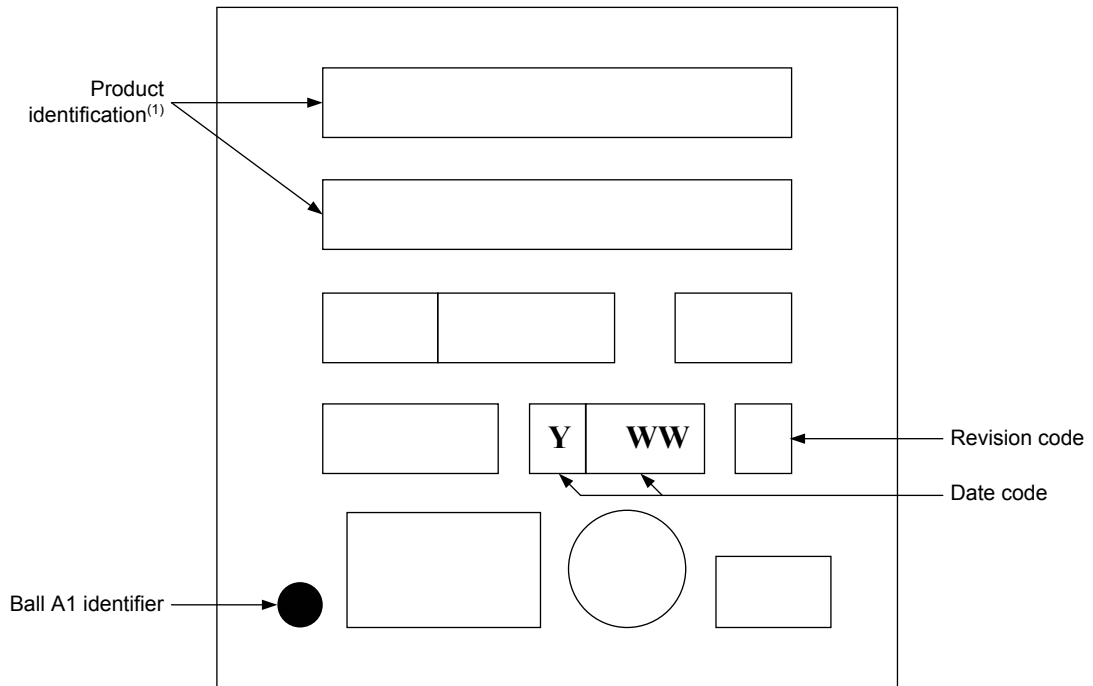
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44 Device marking for UFBGA100 (A0C2)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 43. UFBGA100 marking example (package top view)



A0C2_UFBGA100_DM_V2

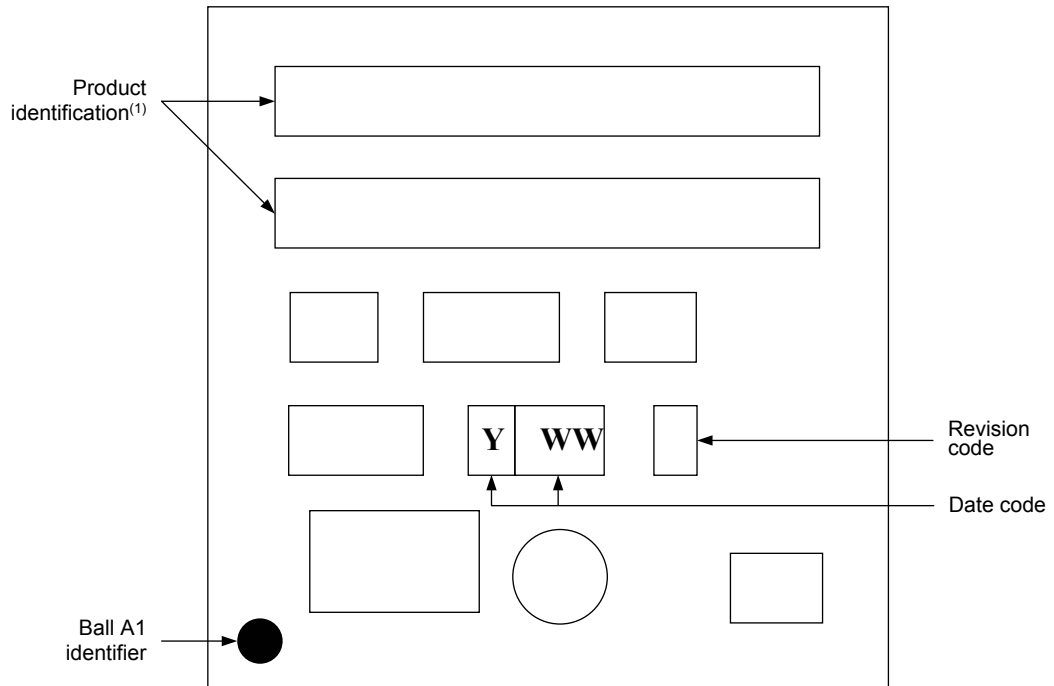
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45 Device marking for UFBGA121 (B0CU)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 44. UFBGA121 marking example (package top view)



B0CU_UFBGA121_DM_V1

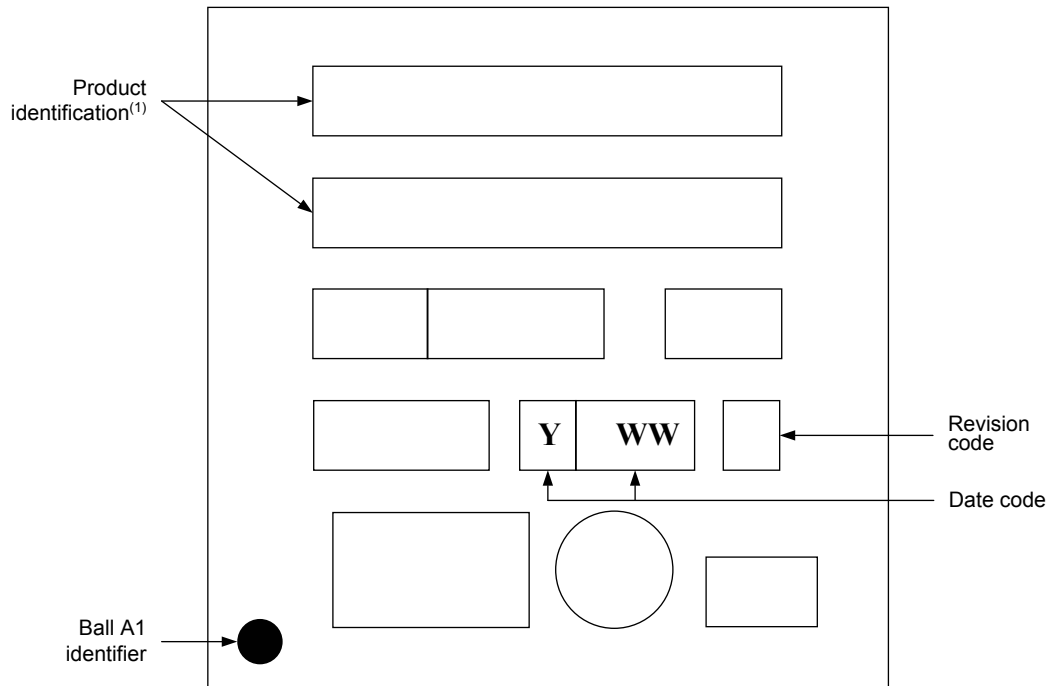
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46 Device marking for UFBGA129 (B09R)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 45. UFBGA129 marking example (package top view)



B09R_UFBGA129_DM_V3

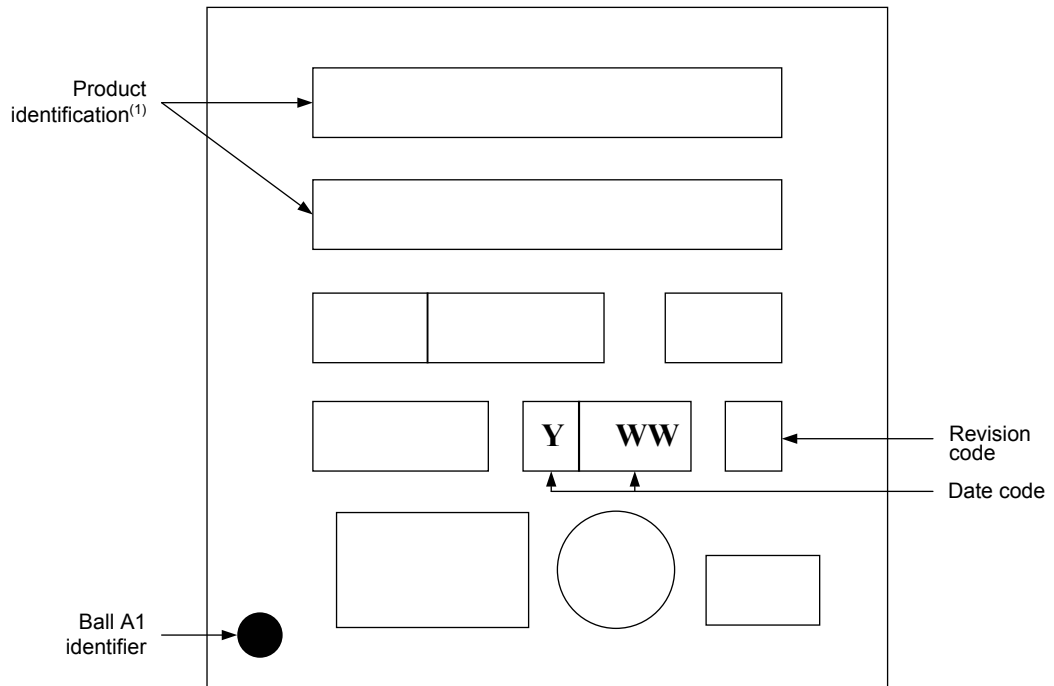
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47 Device marking for UFBGA132 (A0G8)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 46. UFBGA132 marking example (package top view)



A0G8_UFBGA132_DM_V2

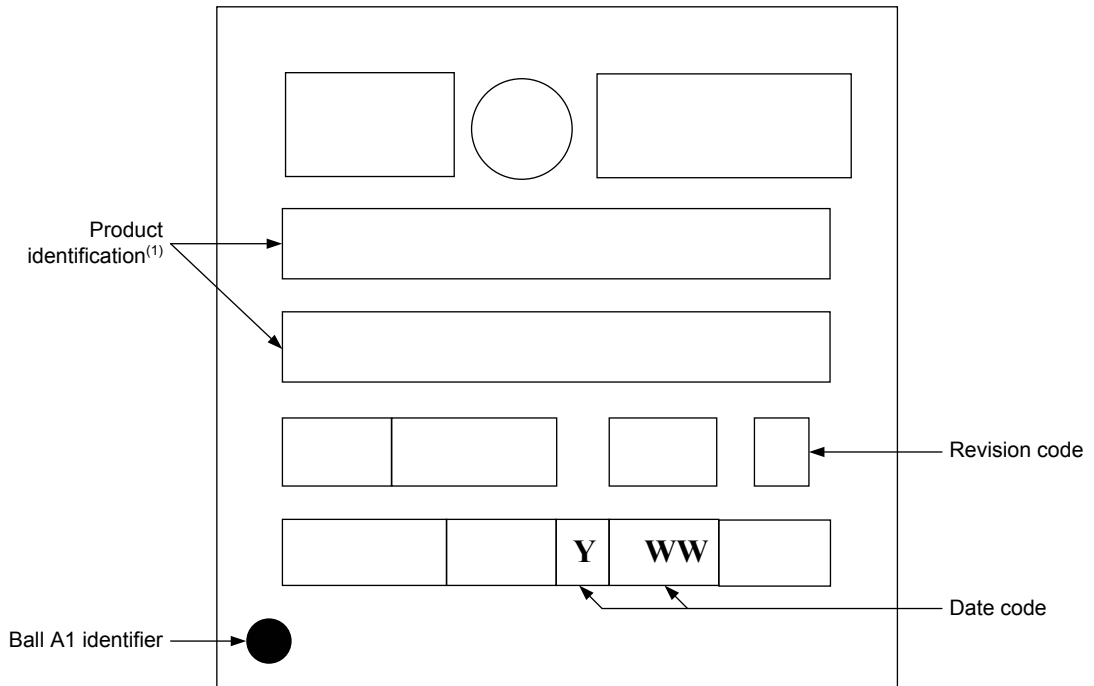
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48 Device marking for UFBGA144 (A02Y)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 47. UFBGA144 marking example (package top view)



A02Y_UFBGA144_DM_V2

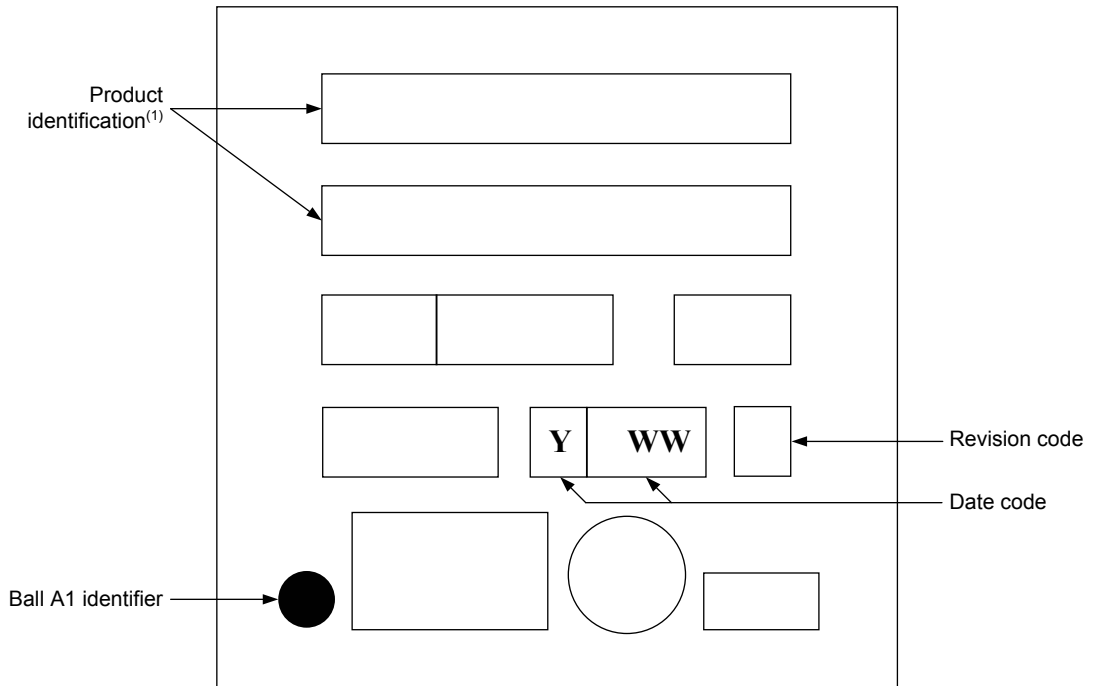
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49 Device marking for UFBGA144 (A0AS)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 48. UFBGA144 marking example (package top view)



A0AS_UFBGA144_DM_V3

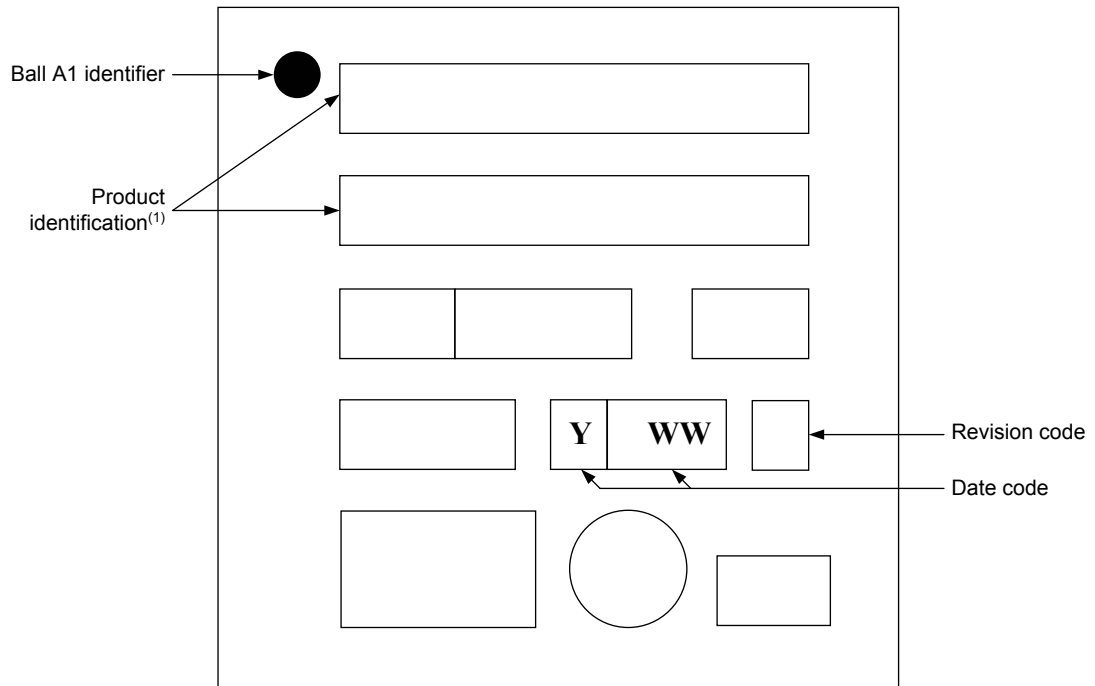
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50 Device marking for UFBGA169 (A0YV)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at top left.

Figure 49. UFBGA169 marking example (package top view)



A0YV_UFBGA169_DM_V4

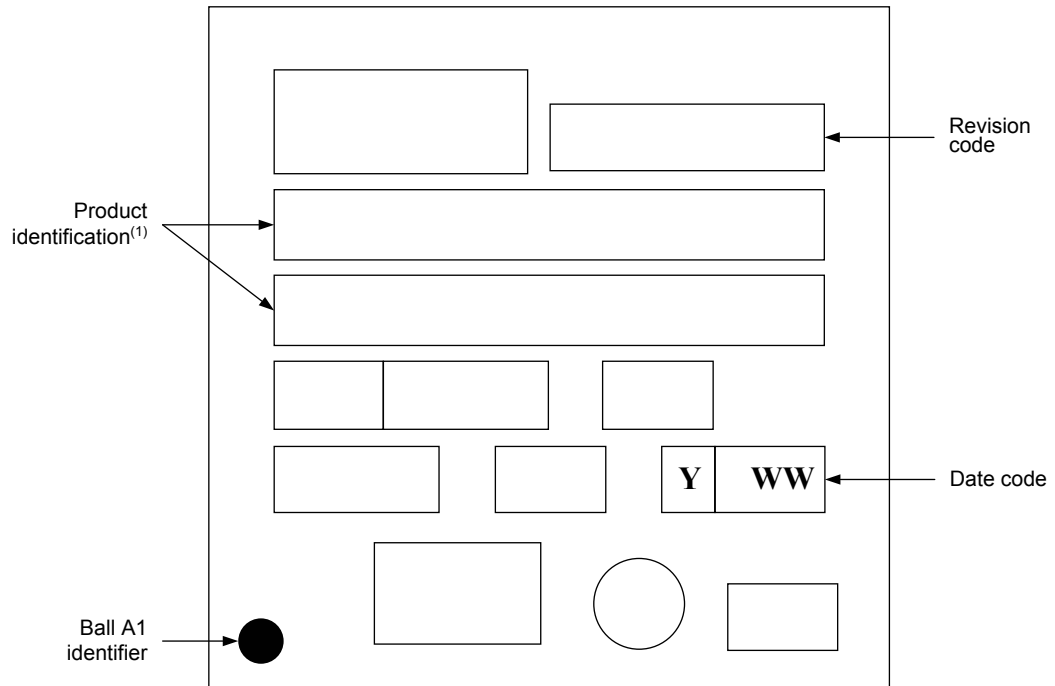
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51 Device marking for UFBGA176+25 (A0E7)

The following figure gives an example of the locations and orientation of the marking areas versus ball A1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, ball A1 is always located at bottom left.

Figure 50. UFBGA176+25 marking example (package top view)



A0E7_UFBGA176+25_DM_V2

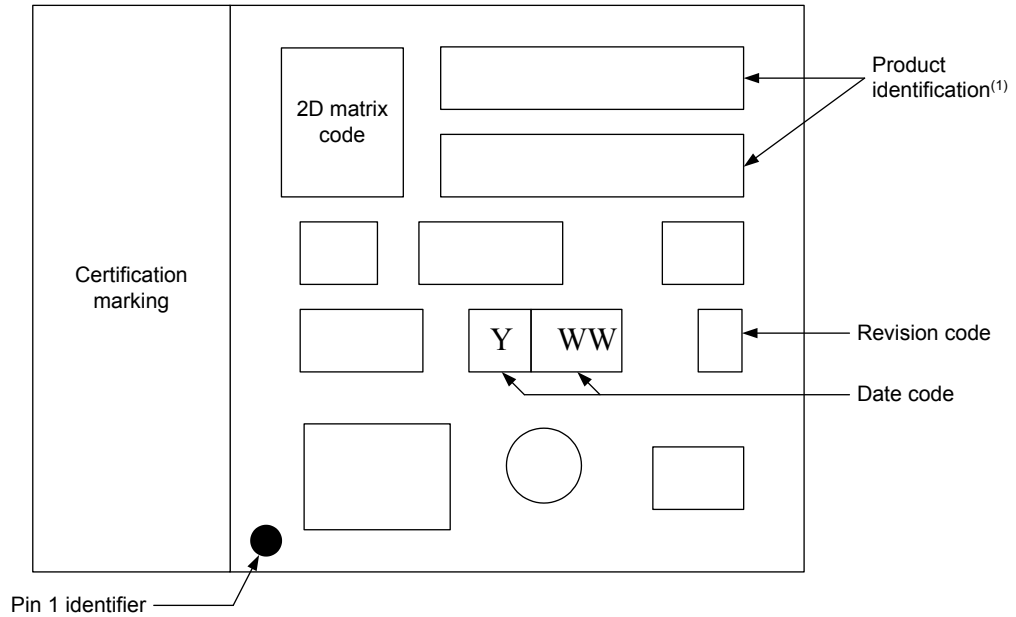
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52 Device marking for SiP-LGA86 (B0CQ)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 51. SiP-LGA86 marking example (package top view)



B0CQ_SiP-LGA86_DM_V1

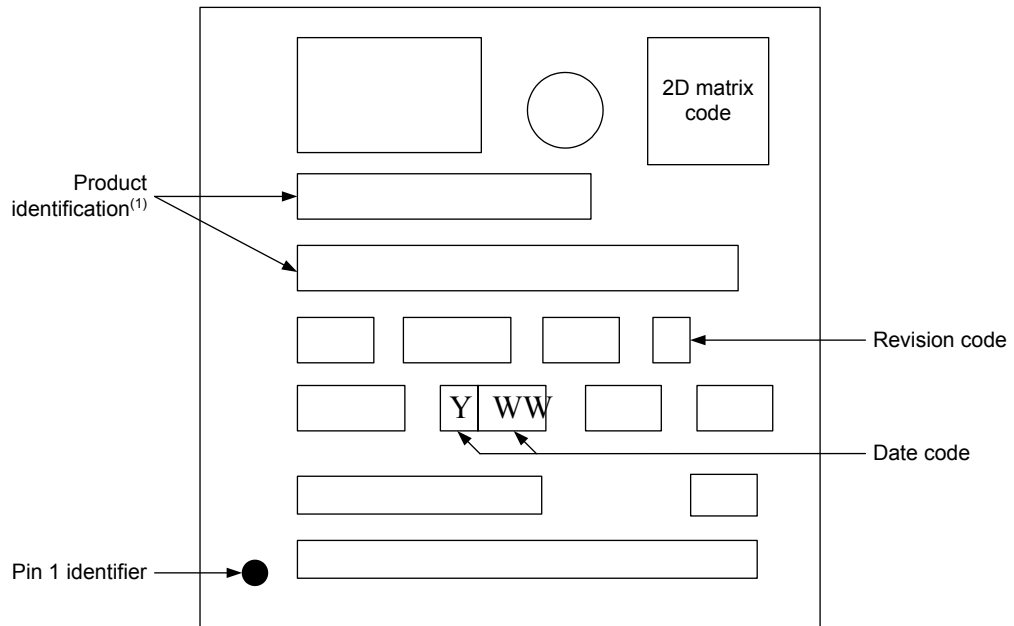
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53 Device marking for SiP-LGA92 (B0HB)

The following figure gives an example of the locations and orientation of the marking areas versus pin 1 and allows engineering samples to be identified.

With the device text markings oriented as shown below, pin 1 is always located at bottom left.

Figure 52. SiP-LGA92 marking example (package top view)



B0HB_SiP-LGA92_DM_V1

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Revision history

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13-Jan-2023	1	Initial release.

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