Introduction

This document reviews the software interface and requirements for the sound detector library. It describes how to integrate the sound detector module into a main program such as the audio STM32Cube expansion software, and provides a basic understanding of the underlying algorithm.

The sound detector library is part of X-CUBE-AUDIO firmware package.
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1 Module overview

1.1 Algorithm functionality

The SoundDetectoR (SDR) module is in charge of detecting an audio signal for its use as a trigger for signal processing (such as speech recognition). It is based on a noise estimator, on the incoming signal energy measurement, and on two configurable thresholds. Based on these metrics, the decision - ACTIVE (presence of signal) or INACTIVE (presence of noise or low-level signal) - is taken.

The current implementation is using 32-bit resolution for all computations, and can be used with 16- or 24- bit input/output format.

1.2 Module configuration

The SDR module supports mono 16- or 24- bit I/O data, and is not memory limited to a maximum input frame size. Nevertheless, the estimators work at best with input frame sizes in the [10 ms ; 30 ms] range. This corresponds to a range of 160 samples to 480 samples at 16 kHz sampling rate.

Several versions of the module are available depending on the I/O format, on the core, and on the used tool chain:

- **SDR_CM4_IAR.a / SDR_CM4_GCC.a / SDR_CM4_Keil.lib**: 16-bit input/output buffers. It runs on any STM32 microcontroller featuring an Arm® core with Cortex®-M4 instruction set
- **SDR_32b_CM4_IAR.a / SDR_CM4_GCC.a / SDR_CM4_Keil**: 24-bit input/output buffers. It runs on any STM32 microcontroller featuring an Arm® core with Cortex®-M4 instruction set
- **SDR_CM7_IAR.a / SDR_CM7_GCC.a / SDR_CM7_Keil.lib**: 16-bit input/output buffers. It runs on any STM32 microcontroller featuring an Arm® core with Cortex®-M7 instruction set
- **SDR_32b_CM7_IAR.a / SDR_CM7_GCC.a / SDR_CM7_Keil**: 24-bit input/output buffers. It runs on any STM32 microcontroller featuring an Arm® core with Cortex®-M7 instruction set
1.3 Resources summary

Table 1 lists the requirements for memories and frequency. The footprints are measured on board, using IAR Embedded Workbench® for Arm® v7.40 (IAR Embedded Workbench® common components v7.2).

Table 1. Summary of resources

<table>
<thead>
<tr>
<th>I/O</th>
<th>Cortex® core</th>
<th>Flash code (.text)</th>
<th>Flash data (.rodata)</th>
<th>Stack</th>
<th>Persistent RAM</th>
<th>Scratch RAM(1)</th>
<th>Frequency (MHz)</th>
</tr>
</thead>
<tbody>
<tr>
<td>16 bits</td>
<td>M4</td>
<td>1060 Bytes</td>
<td></td>
<td>8</td>
<td>100 Bytes</td>
<td>112 Bytes</td>
<td>0.05</td>
</tr>
<tr>
<td></td>
<td>M7(2)</td>
<td>1228 Bytes</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>0.03</td>
</tr>
<tr>
<td>24 bits</td>
<td>M4</td>
<td>1270 Bytes</td>
<td></td>
<td>8</td>
<td>100 Bytes</td>
<td>112 Bytes</td>
<td>0.07</td>
</tr>
<tr>
<td></td>
<td>M7(2)</td>
<td>1470 Bytes</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>0.05</td>
</tr>
</tbody>
</table>

1. Scratch RAM is the memory that can be shared with other processes running on the same priority level. This memory is not used from one frame to another by SDR routines.

2. Footprints on STM32F7 (based on M7 core) are measured on boards with stack and heap sections located in DTCM memory, with a 10 ms framing.
Module interfaces

Two files are needed to integrate the SDR module: SDR_xxx_CMMy_zzz.a/.lib library and the sdr_glo.h header file, which contains all definitions and structures to be exported to the software integration framework.

Note also that audio_fw_glo.h file is a generic header file common to all audio modules and must be included in the audio framework.

2.1 APIs

Six generic functions have a software interface to the main program:

- sdr_reset function
- sdr_setParam function
- sdr_getParam function
- sdr_setConfig function
- sdr_getConfig function
- sdr_process function

Each of these functions is described in the following sections.

2.1.1 sdr_reset function

This procedure initializes the persistent memory of the sound detector module, and initializes static parameters with default values.

```c
int32_t sdr_reset(void *persistent_mem_ptr, void *scratch_mem_ptr);
```

Table 2. Parameters for function sdr_reset

<table>
<thead>
<tr>
<th>I/O</th>
<th>Name</th>
<th>Type</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Input</td>
<td>persistent_mem_ptr</td>
<td>void *</td>
<td>Pointer to internal persistent memory</td>
</tr>
<tr>
<td>Input</td>
<td>scratch_mem_ptr</td>
<td>void *</td>
<td>Pointer to internal scratch memory</td>
</tr>
<tr>
<td>Returned value</td>
<td>-</td>
<td>int32_t</td>
<td>Error value</td>
</tr>
</tbody>
</table>

This routine must be called at least once at initialization time, when the real time processing has not yet started.

2.1.2 sdr_setParam function

This procedure writes the module static parameters from the main framework to the module internal memory. It can be called after reset routine and before real time processing starts. It handles static parameters (i.e. the parameter values cannot be changed during the module processing).

```c
int32_t sdr_setParam(sdr_static_param_t *input_static_param_ptr, void *persistent_mem_ptr);
```
2.1.3 sdr_getParam function

This procedure gets the module static parameters from the module’s internal memory to main framework.

It can be called after reset routine and before real time processing starts. It handles static parameters (i.e. the parameter values that cannot be changed during module processing).

```c
int32_t sdr_getParam(sdr_static_param_t *input_static_param_ptr, void *persistent_mem_ptr);
```

<table>
<thead>
<tr>
<th>I/O</th>
<th>Name</th>
<th>Type</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Input</td>
<td>input_static_param_ptr</td>
<td>sdr_static_param_t*</td>
<td>Pointer to static parameters structure</td>
</tr>
<tr>
<td>Input</td>
<td>persistent_mem_ptr</td>
<td>void *</td>
<td>Pointer to internal persistent memory</td>
</tr>
<tr>
<td>Returned value</td>
<td>-</td>
<td>int32_t</td>
<td>Error value</td>
</tr>
</tbody>
</table>

2.1.4 sdr_setConfig function

This procedure sets module dynamic parameters from main framework to module internal memory.

It can be called at any time during processing.

```c
int32_t sdr_setConfig(sdr_dynamic_param_t *input_dynamic_param_ptr, void *persistent_mem_ptr);
```

<table>
<thead>
<tr>
<th>I/O</th>
<th>Name</th>
<th>Type</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Input</td>
<td>input_dynamic_param_ptr</td>
<td>sdr_dynamic_param_t*</td>
<td>Pointer to dynamic parameters structure</td>
</tr>
<tr>
<td>Input</td>
<td>persistent_mem_ptr</td>
<td>void *</td>
<td>Pointer to internal persistent memory</td>
</tr>
<tr>
<td>Returned value</td>
<td>-</td>
<td>int32_t</td>
<td>Error value</td>
</tr>
</tbody>
</table>
2.1.5 sdr_getConfig function

This procedure gets the module dynamic parameters from internal persistent memory to the main framework.

It can be called at any time during processing.

```
int32_t sdr_getConfig(sdr_dynamic_param_t *input_dynamic_param_ptr, void *persistent_mem_ptr);
```

<table>
<thead>
<tr>
<th>I/O</th>
<th>Name</th>
<th>Type</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Input</td>
<td>input_dynamic_param_ptr</td>
<td>sdr_dynamic_param_t*</td>
<td>Pointer to dynamic parameters structure</td>
</tr>
<tr>
<td>Input</td>
<td>persistent_mem_ptr</td>
<td>void *</td>
<td>Pointer to internal persistent memory</td>
</tr>
<tr>
<td>Returned value</td>
<td></td>
<td>int32_t</td>
<td>Error value</td>
</tr>
</tbody>
</table>

2.1.6 sdr_process function

This procedure is the module main processing routine.

It should be called at any time, to process each frame.

```
int32_t sdr_process(buffer_t *input_buffer, buffer_t *output_buffer, void *persistent_mem_ptr);
```

<table>
<thead>
<tr>
<th>I/O</th>
<th>Name</th>
<th>Type</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Input</td>
<td>input_buffer</td>
<td>buffer_t*</td>
<td>Pointer to input buffer structure</td>
</tr>
<tr>
<td>Output</td>
<td>output_buffer</td>
<td>buffer_t</td>
<td>Pointer to output buffer structure</td>
</tr>
<tr>
<td>Input</td>
<td>persistent_mem_ptr</td>
<td>void *</td>
<td>Pointer to internal persistent memory</td>
</tr>
<tr>
<td>Output</td>
<td></td>
<td>int32_t</td>
<td>Error value</td>
</tr>
</tbody>
</table>

This routine can run in place, meaning that the same buffer can be used for input and output at the same time.

2.2 External definitions and types

2.2.1 Input and output buffers

The SDR library uses extended I/O buffers, which contain, in addition to the samples, some useful information on the stream, such as the number of channels, the number of bytes per sample, and the interleaving mode.
An I/O buffer structure type, like the one described below, must be respected each time, before calling the processing routine; otherwise, errors are returned:

```c
typedef struct {
    int32_t nb_channels;
    int32_t nb_bytes_per_Sample;
    void *data_ptr;
    int32_t buffer_size;
    int32_t mode;
} buffer_t;
```

### Table 8. Input and output buffers

<table>
<thead>
<tr>
<th>Name</th>
<th>Type</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>nb_channels</td>
<td>int32_t</td>
<td>Number of channels in data (1 for mono). SDR only supports mono channel.</td>
</tr>
<tr>
<td>nb_bytes_per_Sample</td>
<td>int32_t</td>
<td>Dynamic of data (2 for 16 bits; 3 for 24 bits). SDR supports audio samples in 16-bit and 24-bit format.</td>
</tr>
<tr>
<td>data_ptr</td>
<td>void *</td>
<td>Pointer to data buffer (must be allocated by the main framework)</td>
</tr>
<tr>
<td>buffer_size</td>
<td>int32_t</td>
<td>Number of samples per channel in the data buffer</td>
</tr>
<tr>
<td>mode</td>
<td>int32_t</td>
<td>In case of stereo stream, left and right channels can be interleaved: 0 = not interleaved, 1 = interleaved.</td>
</tr>
</tbody>
</table>

### 2.2.2 Returned error values

Table 9 lists possible returned error values:

### Table 9. Error values

<table>
<thead>
<tr>
<th>Definition</th>
<th>Value</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>SDR_ERROR_NONE</td>
<td>0</td>
<td>No error.</td>
</tr>
<tr>
<td>SDR_UNSUPPORTED_INTERLEAVING_MODE</td>
<td>-1</td>
<td>Input data is not interleaved.</td>
</tr>
<tr>
<td>SDR_UNSUPPORTED_NUMBER_OF_CHANNELS</td>
<td>-2</td>
<td>Input data is not mono.</td>
</tr>
<tr>
<td>SDR_UNSUPPORTED_NB_OF_BYTEPERSAMPLES</td>
<td>-3</td>
<td>Input data is not 16-bit or 24-bit sample format.</td>
</tr>
<tr>
<td>SDR_BAD_LEARNING_FRAME_NB</td>
<td>-4</td>
<td>learning_frame_nb is not in the [2 : 20] range.</td>
</tr>
<tr>
<td>SDR_BAD_THR_VALUE</td>
<td>-5</td>
<td>Thresholds should be in the [1 : 40] range.</td>
</tr>
<tr>
<td>SDR_BAD_NOISE_PWR_MIN</td>
<td>-6</td>
<td>noise_pwr_min_db is not in the [-87 : 0] range.</td>
</tr>
<tr>
<td>SDR_BAD_HW</td>
<td>-7</td>
<td>Unsupported HW for the library.</td>
</tr>
<tr>
<td>SDR_LOOPSIZE_BUFFERSIZE_DIFFERENT</td>
<td>-8</td>
<td>buffer_size parameter is not identical to the input buffer size to process.</td>
</tr>
</tbody>
</table>
2.3 Static parameters structure

The SDR initial parameters are set using the corresponding static parameter structure before calling the `sdr_setParam()` function.

```c
struct sdr_static_param {
    int32_t sampling_rate;
    int32_t buffer_size;
    int32_t learning_frame_nb;
};
```

<table>
<thead>
<tr>
<th>Name</th>
<th>Type</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>sampling_rate</td>
<td>int32_t</td>
<td>Input buffer sampling rate in Hz. All standard audio sampling rates are supported. Default value is 16 kHz. This value is used in the calculation of some constant times.</td>
</tr>
<tr>
<td>buffer_size</td>
<td>int32_t</td>
<td>Input buffer size in samples. This value is used in the calculation of some constant times.</td>
</tr>
<tr>
<td>learning_frame_nb</td>
<td>int32_t</td>
<td>Number of frames on which the SDR initializes the noise estimator. Initialization happens once after the <code>sdr_reset()</code> function and during the call to the <code>sdr_process()</code> function.</td>
</tr>
</tbody>
</table>

2.4 Dynamic parameters structure

It is possible to change the SDR configuration by setting new values in the dynamic parameter structure before calling the `sdr_setConfig()` function.

The SDR library proposes input and output parameters in its dynamic parameters structure.

For input parameters, it is possible to set or change the SDR configuration by setting the dynamic parameter structure before calling the `sdr_setConfig()` function.

For output parameters, the `sdr_getConfig()` function must be called before accessing to the updated output dynamic parameters.

The input and output parameters, as per the structure presented below, are described in Table 11.

```c
struct sdr_dynamic_param {
    int32_t enable;        /* input variable */
    int32_t ratio_thr1_dB; /* input variable */
    int32_t ratio_thr2_dB; /* input variable */
    int32_t noise_pwr_min_dB; /* input variable */
    int32_t hangover_nb_frame; /* input variable */
    int32_t output_state;   /* output variable */
    int32_t output_nrj;     /* output variable */
    int32_t output_noise_pwr; /* output variable */
    int32_t output_thr1;    /* output variable */
    int32_t output_thr2;    /* output variable */
};
```
Table 11. Dynamic parameters

<table>
<thead>
<tr>
<th>Name</th>
<th>Type</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>enable</td>
<td>int32_t</td>
<td>1: enable the processing of the SDR. 0: disable the processing of the SDR. SDR output state is not reliable.</td>
</tr>
<tr>
<td>ratio_thr1_dB</td>
<td>int32_t</td>
<td>Threshold (in dB) above which SDR state goes from SDR_STATE_INACTIVE to SDR_STATE_ACTIVE. This threshold is added to the noise power estimation and compared to signal energy. Default value is 9 dB. It must be in the [1 : 40] range.</td>
</tr>
<tr>
<td>ratio_thr2_dB</td>
<td>int32_t</td>
<td>Threshold (in dB) below which SDR state goes from SDR_STATE_ACTIVE to SDR_STATE_INACTIVE. This threshold is added to the noise power estimation and compared to signal energy. Default value is 7 dB. It must be in the [1 : 40] range.</td>
</tr>
<tr>
<td>noise_pwr_min_dB</td>
<td>int32_t</td>
<td>Minimum noise power (in dBFS) below which noise power estimation cannot run. Default value is -60 dBFS. It must be in the [-87 : 0] range.</td>
</tr>
<tr>
<td>hangover_nb_frame</td>
<td>int32_t</td>
<td>Number of frames of hangover for state going from SDR_STATE_ACTIVE to SDR_STATE_INACTIVE. Default value is 4. It must be in the [2 : 20] range.</td>
</tr>
<tr>
<td>output_state</td>
<td>int32_t</td>
<td>SDR decision. state = SDR_STATE_INACTIVE means no speech is detected. state = SDR_STATE_ACTIVE means speech is detected.</td>
</tr>
<tr>
<td>output_nrj</td>
<td>int32_t</td>
<td>For debug. Current frame energy.</td>
</tr>
<tr>
<td>output_noise_pwr</td>
<td>int32_t</td>
<td>For debug. Current frame noise power estimation.</td>
</tr>
<tr>
<td>output_thr1</td>
<td>int32_t</td>
<td>For debug. Current frame THR1 estimation (from SDR_STATE_INACTIVE to SDR_STATE_ACTIVE).</td>
</tr>
<tr>
<td>output_thr2</td>
<td>int32_t</td>
<td>For debug. Current frame THR2 estimation (from SDR_STATE_ACTIVE to SDR_STATE_INACTIVE).</td>
</tr>
</tbody>
</table>
3 Algorithm description

3.1 Processing steps

The SDR block diagram presented in Figure 1.

![Figure 1. SDR block diagram](MSv46838V1)

The algorithm starts with the computation of the energy of the current frame continues with the update of the noise power estimation. During the learning phase (corresponding to the learning_frame_nb parameter), the algorithm assumes that the incoming signal is noise in order to get a reference noise level. This estimation is then used to compute the two thresholds used for deciding if the current frame is ACTIVE or INACTIVE. Finally the hangover mechanism prevents the SDR state from excessive toggling.

The output variables are then updated in the persistent memory structure, and can be accessed through the call to the sdr_getConfig() function and the dynamic parameters structure.

The output buffer contains the copy of the input buffer.

3.2 Data formats

Input of SDR module is expected to be an audio stream, mono, in 16- or 32-bit format. All operations are done with 32 bits resolution. The algorithm can run with almost any buffer size. Nevertheless, like every speech processing algorithm, it will perform at best with input frame sizes ranging from 10 ms to 200 ms. All operations are done with 32-bit resolution. The output format is the same as the format of the input buffer.
4  System requirement and hardware setup

SDR libraries are built to run either on a Cortex®-M4 or on a Cortex®-M7 core. They can be integrated and run on corresponding STM32F4/STM32L4 or STM32F7 family devices. There is no other hardware dependency.

4.1 Recommendations for optimal setup

The SDR module can be executed at any place in an audio processing chain. It generally takes place in an audio front-end together with sound capture mechanisms, and is used as a speech or audio detector to trigger other modules or applications. Figure 2 shows the optimal setup.

![Figure 2. SDR in the audio processing chain](image)

4.1.1 Module integration example

Cube expansion SDR integration examples are provided on STM32F746G-Discovery and STM32F469I-Discovery boards. Refer to the provided integration code for more details.
4.1.2 Module APIs calls

The sequence is shown in Figure 3 on page 16. Each step is described in the list below:

1. As explained in Chapter 2: Module interfaces on page 7, SDR scratch and persistent memories have to be allocated, as well as input and output buffer according to the structures defined in Section 2.3 and in Section 2.4. The CRC HW block must be enabled to unlock the library.

2. Once the memory is allocated, the call to the sdr_reset() function initializes internal variables.

3. The SDR static parameters structure can then be set.

4. Call the sdr_setParam() routine to apply static parameters.

5. The dynamic parameters need to be set before going to the processing step. Configure the SDR dynamic parameters as desired and execute the sdr_setConfig() function.

6. The audio stream is read from the audio front-end interface and the input_buffer structure has to be filled according to the stream characteristics (number of channels, sample rate, interleaving and data pointer). The output buffer structure has to be set as well.

7. Calling the sdr_process() function executes the SDR algorithm.

8. The output audio stream can then be written in the proper interface. In the case of the SDR, the output buffer is the same as the input buffer.

9. In order to get the SDR output_state variable updated, the sdr_getConfig() function has to be called before accessing the dynamic parameter structure.

10. If needed, the user can set new dynamic parameters and call the sdr_setConfig() function to update module configuration.

11. If the application is still running and provides new input samples to be processed, execution must branch to step 6 and continue from step 6 onward; otherwise, the processing loop is over.

12. Once the processing loop is over, the allocated memory must be released.
Figure 3. Sequence of APIs

1. Memory allocation
   CRC enable and reset

2. sdr_reset()

3. static_param initialization

4. sdr_setParam()

5. sdr_setConfig()

6. Audio front-end read and input buffer preparation

7. sdr_process()

8. Audio stream write

9. sdr_getConfig()
   Get output_state variable

   New configuration needed?

   yes

   Set new dynamic parameters
   sdr_setConfig()

   no

   Samples to proceed?

   yes

   Memory freeing

   no
5 How to tune and run the application

The SDR is designed to process preferably a signal coming from a speech front-end, with an input sampling rate of 16 kHz or 8 kHz. SDR also behaves properly with higher sampling rates.

The learning phase is important. During this phase, the SDR averages the signal during the configured number of frames and uses it as its starting point for noise estimation. ratio\_thr1\_dB and ratio\_thr2\_dB have to be properly tuned.

Too low values (e.g. ratio\_thr1\_dB = 4) can lead to an excessive sensitivity of the SDR algorithm to fluctuating noise as illustrated in Figure 4.

Conversely, too high values (e.g. ratio\_thr1\_dB = 12) can lead to have the SDR algorithm not sensitive enough. As a result, speech utterance can be detected as noise as shown in Figure 5.

**Figure 4. SDR excessive sensitivity**

Conversely, too high values (e.g. ratio\_thr1\_dB = 12) can lead to have the SDR algorithm not sensitive enough. As a result, speech utterance can be detected as noise as shown in Figure 5.

**Figure 5. SDR poor sensitivity**

It is strongly recommended to tune ratio\_thr2\_dB so that it is always 2 to 4 dB smaller than ratio\_thr1\_dB.

The noise\_pwr\_min\_dB value must reflect the minimum level under which the input signal is not speech (background level or noise floor).

In the next two examples, the input signal has a background noise of about -58 dB. If the noise\_pwr\_min\_dB parameter is much higher than the noise level (e.g. noise\_pwr\_min\_dB = -40dB), SDR\_STATE\_INACTIVE state detection can occur even if there is speech present at low level. This is shown in Figure 6.
How to tune and run the application

Figure 6. SDR noise_pwr_min_dB too high

On the opposite, having noise_pwr_min_dB being much lower than noise floor (e.g. noise_pwr_min_dB = -65 dB) makes the SDR detect speech even if there is only low level fluctuating noise as shown in Figure 7.

Figure 7. SDR noise_pwr_min_dB too low

hangover_nb_frame must be tuned in order to avoid excessive toggling from SDR_STATE_INACTIVE to SDR_STATE_ACTIVE. Having this parameter tuned too low (ex. = 2) keeps the state toggling too much as shown in Figure 8.

Figure 8. SDR hangover_nb_frame too low

On the opposite, if hangover_nb_frame is too high (e.g. 50), the state will remain stuck to SDR_STATE_ACTIVE even in the absence of speech. Such occurrences are shown in Figure 9.

Figure 9. SDR hangover_nb_frame too high
6 Revision history

Table 12. Document revision history

<table>
<thead>
<tr>
<th>Date</th>
<th>Revision</th>
<th>Changes</th>
</tr>
</thead>
<tbody>
<tr>
<td>09-Jan-2018</td>
<td>1</td>
<td>Initial release.</td>
</tr>
</tbody>
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