



Getting started with X-NUCLEO-53L3A2 multi target ranging ToF sensor expansion board based on VL53L3CX for STM32 Nucleo

Introduction

This document provides detailed hardware information on the X-NUCLEO-53L3A2 expansion board. This expansion board is compatible with the STM32 Nucleo family and the Arduino TM electronic boards. It is designed around the VL53L3CX ranging sensor and is based on the ST patented FlightSense technology.

To allow the user to validate the VL53L3CX in an environment as close as possible to its final application, the X-NUCLEO-53L3A2 expansion board is delivered with a holder in which three different height spacers of 0.25 mm, 0.5 mm, and 1 mm can be fitted with the cover glass above the spacer. The height spacers are used to simulate different air gap distances between the VL53L3CX sensor and the cover glass.

The X-NUCLEO-53L3A2 expansion board is delivered with two VL53L3CX breakout boards.



Figure 1. X-NUCLEO-53L3A2 expansion board, spacers, cover glass, and breakout boards



UM2757 - Rev 2 page 2/17



1 Overview

The X-NUCLEO-53L3A2 expansion board features the VL53L3CX ranging sensor, based on ST's FlightSense, Time-of-Flight (ToF) technology.

It is compatible with the STM32 Nucleo development board family, and with the Arduino UNO R3 connector layout.

Several ST expansion boards can be stacked through the Arduino connectors, which allows, for example, the development of VL53L3CX applications with Bluetooth or Wi-Fi interfaces.

The X-NUCLEO-53L3A2 expansion board is delivered with:

- Three spacers of 0.25 mm, 0.5 mm, and 1 mm height, used to simulate different air gaps between the VL53L3CX and the cover glass.
- Two cover windows to simulate the integration of the VL53L3CX into the customer's final product.
- Two VL53L3CX breakout boards which can be plugged onto the X-NUCLEO-53L3A2 expansion board or connected through flying wires to the X-NUCLEO-53L3A2 expansion board.
- Two 10-pin connectors to enable the customer to connect the two breakout boards onto the X-NUCLEO-53L3A2 expansion board.

Note:

The VL53L3CX is delivered with a liner to prevent potential foreign material from penetrating inside the module holes during the assembly process. This liner must be removed at the latest possible step during final assembly, before module calibration.

Table 1. Ordering information

Order code	Description
X-NUCLEO-53L3A2	STM32 Nucleo expansion board - spacers and glass - two breakout boards

UM2757 - Rev 2 page 3/17



2 Document references

Table 2. Document references

Description	DocID
VL53L3CX datasheet	DS13204
X-NUCLEO-53L3A2 data brief	DB4226
P-NUCLEO-53L3A2 data brief	DB4194
X-CUBE-53L3A2 data brief	DB4193

UM2757 - Rev 2 page 4/17



3 X-NUCLEO-53L3A2 expansion board

This section describes the X-NUCLEO-53L3A2 expansion board features and provides useful information for understanding the electrical characteristics.

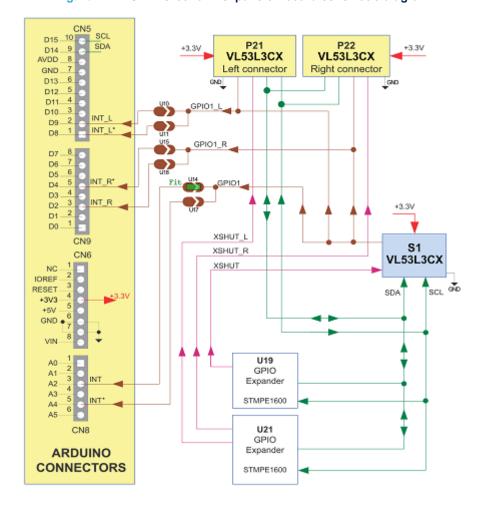


Figure 2. X-NUCLEO-53L3A2 expansion board schematic diagram

UM2757 - Rev 2 page 5/17



3.1 Description

The board allows the user to test the VL53L3CX functionality, to program it and to understand how to develop an application using the VL53L3CX. It integrates:

- 2.8 V regulator to supply the VL53L3CX
- Level translators to adapt the I/O level to the main board of the microcontroller
- Arduino UNO R3 connectors
- Optional VL53L3CX breakout board connectors
- · Solder drops to allow different configurations of the expansion board

It is fundamental to program a microcontroller to control the VL53L3CX through the I2C bus. The application software and an example of the C-ANSI source code are available on www.st.com/VL53L3CX.

The X-NUCLEO-53L3A2 expansion board and STM32 Nucleo development board are connected through the Arduino UNO R3 connectors CN5, CN6, CN8, and CN9 as shown in Figure 3. X-NUCLEO-53L3A2 expansion board connector layout and as described in Table 3. Left Arduino connector and Table 4. Right Arduino connector.

The X-NUCLEO-53L3A2 must be plugged onto the STM32 Nucleo development board through the Arduino UNO R3 connectors.

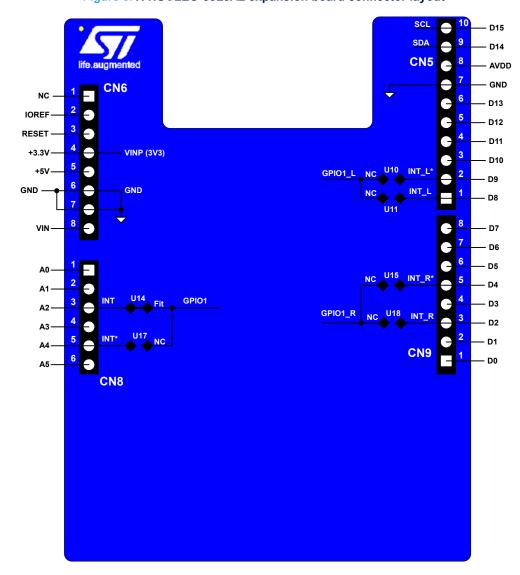


Figure 3. X-NUCLEO-53L3A2 expansion board connector layout

UM2757 - Rev 2 page 6/17



Table 3. Left Arduino connector

CN number	VL53L3CX board	Pin number	Pin name	MCU pin	X-NUCLEO-53L3A2 expansion board function	
		1	NC	NC		
		2	NC	IOREF	Not used	
		3	NC	RESET	Not used	
CNC navvan	Power	4	3V3	3V3	3.3 V supply	
CN6 power		5	NC	5V	Not used	
	Gnd	6	Gnd	Gnd	Gnd	
	Gnd	7	Gnd	Gnd	Gild	
		8	NC	VIN		
CN8 analog		1	NC	PAO	Not used	
		2	NC	PA1		
	GPIO1	3	INT	PA4	Interrupt signal from VL53L3CX o board soldered device	
		4	NC	PB0	Not used	
	GPIO1	5	INT	PC1 ⁽¹⁾	By default not used, interrupt signal from VL53L3CX on board soldered device	
		6	NC	PC0	Not used	

^{1.} Depends on STM32 Nucleo board solder bridges, see details in Section 3.3: Solder drop configurations. These interrupt signals are duplicated, but not used. This offers hardware connection flexibility in case of conflict on the MCU interface management when the expansion board is used superimposed with other expansion boards. In this case, remove the solder drop from the used interrupt and instead, fit the solder drop in "NC".

UM2757 - Rev 2 page 7/17



Table 4. Right Arduino connector

CN number	VL53L3CX board	Pin number	Pin name	MCU pin	X-NUCLEO-53L3A2 expansion board function	
	SCL	10	D15	PB8	I2C1_SCL	
	SDA	9	D14	PB9	I2C1_SDA	
		8	NC	AVDD	Not used	
	Gnd	7	Gnd	Gnd	Gnd	
		6	INT_L	PA5		
CN5 digital		5	NC	PA6	Not used	
		4	NC	PA7	- Not used	
		3	NC	PB6		
	GPIO1_L	2	INT_L	PC7	By default not used, interrupt	
	GPIO1_L	1	INT_L	PA9	signal from optional VL53L3CX left breakout board (1)	
		8	NC	PA8		
		7	NC	PB10	Not used	
		6	NC	PB4		
CN9 digital		5	INT_R	PB5	By default not used, interrupt signal from optional VL53L3CX right breakout board (1)	
		4	NC	PB3	Not used	
		3	INT_R	PA10	By default not used, interrupt signal from optional VL53L3CX right breakout board (1)	
		2	NC	PA2	Not used	
		1	NC	PA3	- Not used	

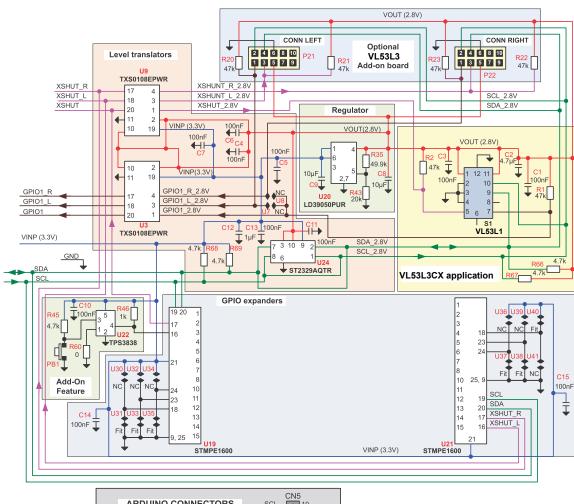
^{1.} These interrupt signals are duplicated, but not used by default. This offers hardware connection of the breakout board VL53L3CX interrupt signals and flexibility in case of conflict on the MCU interface management when the expansion board is used superimposed with other expansion boards. In this case, select, through a solder drop, the MCU port which is free.

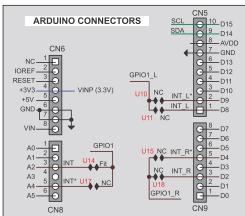
UM2757 - Rev 2 page 8/17



3.2 Electrical schematic

Figure 4. X-NUCLEO-53L3A2 expansion board schematic





VL53L3CX					
AVDDVCSEL	1	12	GND4		
AVSSVCSEL	2	11	AVDD		
GND	3	10	SCL		
GND2	4	9	SDA		
XSHUT	5	8	DNC		
GND3*	6	7	GPIO1		

^{*} Can be NC or grounded

UM2757 - Rev 2 page 9/17



3.3 List of materials

Table 5. List of materials

Components	Value	Reference	Supplier	Comments		
VL53L3CX application						
C1, C3	100 nF	X5R		Currel coality and description		
C2	4.7 μF	X5R - 6.3 V		Supply voltage decoupling		
R1	47 k			Interrupt output pull up		
R2	47 k			Reset input pull up		
R66, R67	4.7 k			SDA and SCL line pull up at 2.8 V		
S1		VL53L3CX	ST	ToF ranging sensor		
		VL53L3CX breakout bo	pard interfaces			
R20	47 k			Left breakout board interrupt output pull up		
R21	47 k			Left breakout board reset input pull up		
R22	47 k			Right breakout board reset input pull up		
R23	47 k			Right breakout board interrupt output pull up		
		2.8 V regulator ap	plication			
C8	10 μF	X5R - 6.3 V		Output voltage decoupling		
C9	10 μF	X5R - 6.3 V		Input voltage decoupling		
R35	49.9 k 20 k			Feedback resistor bridge to set the output voltage to 2.8 V		
U20	20 K	LD39050PUR	ST	Output programmable regulator		
323		Level translator a		Output programmable regulator		
C4, C6, C11	100 nF			2.8 V decoupling capacitor		
C5, C7, C13	100 nF					
C12	1 μF	X5R - 6.3V		3.3 V decoupling capacitor		
R68, R69	4.7 k			SDA and SCL line pull up at 3.3 V		
U3, U9		TXS0108PWR	TI	For all signals except I2C interface		
U24		ST2329AQTR	ST	For I2C interface		
		Add-on feat	:ure			
C10	100 nF			Supply decoupling capacitor		
R45	4.7 k			Push button pull up		
R46	1 k			Output pull up		
R60				Delay time setting (def = 10 ms)		
PB1				Push button		
U22		TPS3838K33	TI	Supervisory circuit		
GPIO expander						
C14, C15	100 nF			Supply decoupling capacitor		

UM2757 - Rev 2 page 10/17

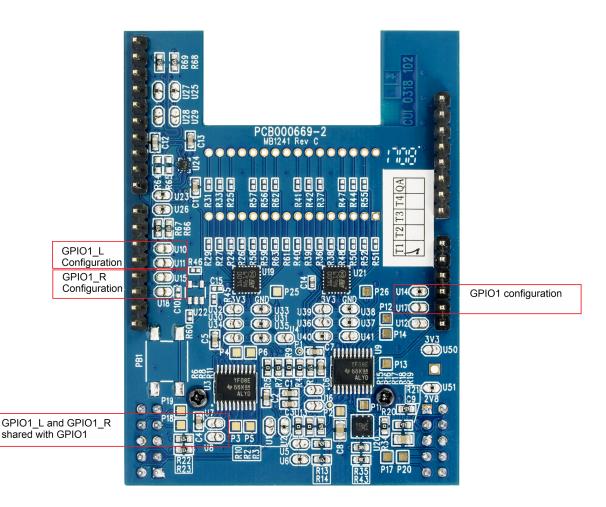


3.4 Solder drop configurations

Solder drops allow the following configurations of the X-NUCLEO-53L3A2 expansion board:

- If the developer wants to make an application with several expansion boards stacked and there is:
 - conflict with the microcontroller port allocation, the GPIO1 can be output on the CN8/A4 (U17 fitted) of the Arduino connector. The default configuration is that GPIO1 is output on the CN8/A2 (U14 fitted) of the Arduino connector.
 - conflict on the I2C addresses, the addresses of the STMPE1600 can be modified (the default addresses are A2, A1, A0, 000, and 001).
- If the developer wants to connect breakout boards (see Figure 5. Interrupt configurations) to the X-NUCLEO-53L3A2 expansion board:
 - the VL53L3CX interrupt of the left breakout board can be output on the CN5/D9 (U10 fitted) or CN5/D8 (U11 fitted) of the Arduino connector. By default, the U10 and U11 are not fitted.
 - the VL53L3CX interrupt of the right breakout board can be output on the CN9/D4 (U15 fitted) or CN9/D2 (U18 fitted) of the Arduino connector. By default, the U15 and U18 are not fitted.
 - the VL53L3CX interrupt of the left and right breakout boards, GPIO1_L and GPIO1_R, can be shared with the VL53L3CX interrupt on the main board, GPIO1, by fitting U7 and U8 solder drops. By default U7 and U8 are not fitted.

Figure 5. Interrupt configurations



UM2757 - Rev 2 page 11/17



3.5 Integrated device pinning

STMPE1600 A2 A1 INT VCC SDA SCL LD39050PUR **TPS3838** 20 22 ΕN CT 1 5 V_{DD} 6 VIN GPIO_0 18 A0 GND 2 5 ADJ GND 2 GPIO_1 17 GPIO_15 2 PG 3 MR 3 4 RESET 4 Vout GPIO_2 16 GPIO_14 GPIO_3 15 GPIO_13 4 ST2329A VL VCC GPIO_4 GPIO_12 5 14 10 9 I/O_{VCC1} 8 GPIO_5 6 13 GPIO 11 I/O_{VL1} I/O_{VCC2} I/O_{VL2} 2 8 9 10 **TOP VIEWS** GND OE GPIO_6 GND GPIO_9 10 9 GPIO_7 GPIO_8 GPIO_10 TXS0108EPWR 20 B1 19 VCCB 18 B2 17 B3 Input:output 1. Referenced to VCCA В1 Input:output 1. Referenced to VCCB A1 VCCA VCCB B-port supply voltage VCCA A-port supply voltage Input:output 2. Referenced to VCCB Input:output 2. Referenced to VCCA <u>A2</u> B2 A2 Input:output 3. Referenced to VCCA A3 Input:output 3. Referenced to VCCB А3 **B3** A<u>4</u> A<u>5</u> 16 B4 15 B5 Input:output 4. Referenced to VCCA Input:output 4. Referenced to VCCB A4 B4

6

8

9

10

<u>A6</u>

<u> 47</u>

<u> 8</u>

O<u>E</u>

B5

В6

B8

GND Ground

14 B6 13 B7

12 B8

11 GND

Input:output 5. Referenced to VCCB

Input:output 6. Referenced to VCCB

Input:output 7. Referenced to VCCB

Input:output 8. Referenced to VCCB

Input:output 5. Referenced to VCCA

Input:output 6. Referenced to VCCA

Input:output 7. Referenced to VCCA

Input:output 8. Referenced to VCCA

3-state output-mode enable

A5

A6

A7

A8

Figure 6. Integrated device pinning

<u>UM27</u>57 - Rev 2 page 12/17



4 VL53L3CX breakout board

The VL53L3CX breakout boards are supplied at 2.8 V by the regulator present on the X-NUCLEO-53L3A2 expansion board.

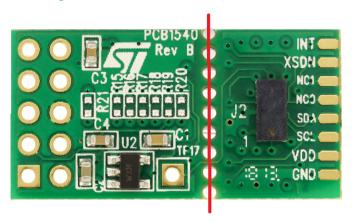


Figure 7. VL53L3CX breakout board schematic

The VL53L3CX breakout boards can be directly plugged onto the X-NUCLEO-53L3A2 expansion board through the two 10-pin connectors or connected to the board through flying leads.

When connected through flying leads, developers should break off the mini PCB from the breakout board, and use only the "VL53L3CX mini PCB" which because of its small size, is easier to integrate into customer devices.

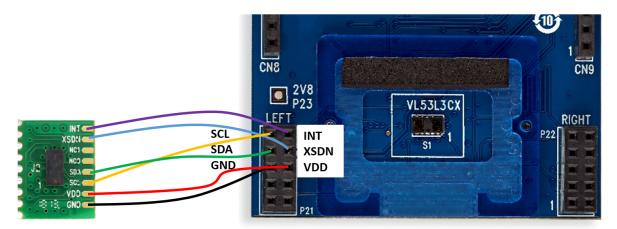


Figure 8. VL53L3CX mini PCB flying lead connection to X-NUCLEO-53L3A2 expansion board

UM2757 - Rev 2 page 13/17



5 Safety considerations

5.1 Electrostatic precaution

The user should exercise electrostatic precautions, including using ground straps when using the X-NUCLEO-53L3A2 expansion board. Failure to prevent electrostatic discharge could damage the device.

Figure 9. Electrostatic logo



5.2 Laser safety considerations

The VL53L3CX contains a laser emitter and corresponding drive circuitry. The laser output is designed to remain within Class 1 laser safety limits under all reasonably foreseeable conditions including single faults, in compliance with the IEC 60825-1:2014 (third edition). The laser output remains within Class 1 limits as long as STMicroelectronic's recommended device settings are used and the operating conditions specified in the datasheet are respected. The laser output power must not be increased by any means and no optics should be used with the intention of focusing the laser beam.

Figure 10. Class 1 laser product label



UM2757 - Rev 2 page 14/17



Revision history

Table 6. Document revision history

Date	Version	Changes
10-Sep-2020	1	Initial release
23-Apr-2021	2	Updated Figure 2. X-NUCLEO-53L3A2 expansion board schematic diagram

UM2757 - Rev 2 page 15/17



Contents

1	Overview					
2	Document references					
3		IUCLEO-53L3A2 expansion board				
	3.1	Description	6			
	3.2	Electrical schematic				
	3.3	List of materials	10			
	3.4	Solder drop configurations	11			
	3.5	Integrated device pinning				
4	VL5	53L3CX breakout board	13			
5	Safe	ety considerations	14			
	5.1	Electrostatic precaution	14			
	5.2	Laser safety considerations				
Rev	/ision	history	15			
Coi	ntents	S	16			



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UM2757 - Rev 2 page 17/17