



Evaluation board for L98GD8



Features

- Voltage min/max: 3.8 V to 48 V.
- 8-channel configurable MOSFET pre-driver:
 - High-side (N-channel and P-channel MOS)
 - Low-side (N-channel MOS)
 - H-bridge (up to 2 H-bridge)
 - Peak & Hold (2 loads)
- Device registers setting and the full diagnostic are available through SPI.
- Access to all relevant pins by test points.
- Input signal connector compatible with the SPC56M-DIS (SPC563M64L5 Discovery+ evaluation board).
- Possibility of connecting a generic microcontroller board by using a simple adapter.

Product status link

EVL-L98GD8

Prod	uct si	ımmarv

Order code

EVL-L98GD8

Description

The EVL-L98GD8 is an evaluation board designed to evaluate L98GD8, a 48 V rated smart power device designed by STMicroelectronics in advanced BCD technology. L98GD8 is a flexible high-side/low-side configurable pre-driver which is able to drive both NMOS and PMOS. It is possible to configure the device as an independent 8 high-side and low-side pre-driver or as 2 H-Bridge pre-driver or 2 pick and hold pre-driver by using SPI configuration and jumper on the board.

All channels are protected against short circuit, over current and over-temperature conditions.

The board can be connected to the SPC56M-DIS, the Discovery+ board developed for the SPC563M64L5.



System requirements, HW and SW resources

1.1 System requirements

- Power supply: 4 V ÷ 48 V; up to 30 A
- SPC56 discovery board or microcontroller board able to offer:
 - SPI signals
 - 12 GPIO to drive injector and ignition and to monitor status channels and enable pin
 - +5 V or 3.3 V (V_{cc})

1.2 Development toolchain

- Labview and UDE Visual Platform
- USB RS232 cable

1.3 Demonstration software

The companion software STSW-L98GD8 includes both a GUI allowing the full control of the EVL-L98GD8 and an example script for a first evaluation.

For more information and download, please refer to www.st.com.

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Revision history

Table 1. Document revision history

Date	Revision	Changes
09-Jul-2025	1	Initial release.

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